

April 2015

# 4N25M, 4N26M, 4N27M, 4N28M, 4N35M, 4N36M, 4N37M 6-Pin General Purpose Phototransistor Optocouplers

### Features

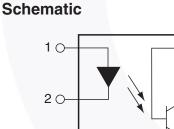
- Minimum Current Transfer Ratio at I<sub>F</sub> = 10 mA, V<sub>CE</sub> = 10 V:
  - 10% for 4N27M and 4N28M
  - 20% for 4N25M and 4N26M
  - 100% for 4N35M, 4N36M and 4N37M
- Safety and Regulatory Approvals:
  - UL1577, 4,170 VAC<sub>RMS</sub> for 1 Minute
  - DIN-EN/IEC60747-5-5, 850 V Peak Working Insulation Voltage

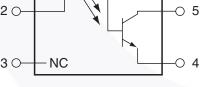
### Applications

- Power Supply Regulators
- Digital Logic Inputs
- Microprocessor Inputs

### Description

The general purpose optocouplers consist of a gallium arsenide infrared emitting diode driving a silicon phototransistor in a standard plastic six-pin dual-in-line package.





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PIN 1. ANODE 2. CATHODE 3. NO CONNECTION 4. EMITTER 5. COLLECTOR 6. BASE



### **Package Outlines**

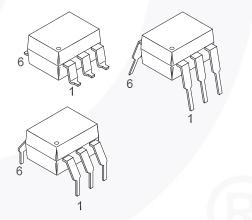


Figure 2. Package Outlines

### Safety and Insulation Ratings

As per DIN EN/IEC 60747-5-5, this optocoupler is suitable for "safe electrical insulation" only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.

Parameter	Characteristics	
Installation Classifications per DIN VDE	< 150 V <sub>RMS</sub>	I–IV
0110/1.89 Table 1, For Rated Mains Voltage	< 300 V <sub>RMS</sub>	I–IV
Climatic Classification		55/100/21
Pollution Degree (DIN VDE 0110/1.89)		2
Comparative Tracking Index		175

Symbol	Parameter	Value	Unit	
V	Input-to-Output Test Voltage, Method A, $V_{IORM} \times 1.6 = V_{PR}$ , Type and Sample Test with $t_m = 10$ s, Partial Discharge < 5 pC		V <sub>peak</sub>	
V <sub>PR</sub>	Input-to-Output Test Voltage, Method B, $V_{IORM} \times 1.875 = V_{PR}$ , 100% Production Test with $t_m = 1$ s, Partial Discharge < 5 pC	1594	V <sub>peak</sub>	
V <sub>IORM</sub>	Maximum Working Insulation Voltage	850	V <sub>peak</sub>	
V <sub>IOTM</sub>	Highest Allowable Over-Voltage	6000	V <sub>peak</sub>	
	External Creepage	≥ 7	mm	
	External Clearance	≥ 7	mm	
	External Clearance (for Option TV, 0.4" Lead Spacing)	≥ 10	mm	
DTI	Distance Through Insulation (Insulation Thickness)	≥ 0.5	mm	
Τ <sub>S</sub>	Case Temperature <sup>(1)</sup>	175	°C	
I <sub>S,INPUT</sub>	Input Current <sup>(1)</sup>	350	mA	
P <sub>S,OUTPUT</sub>	Output Power <sup>(1)</sup>	800	mW	
R <sub>IO</sub>	Insulation Resistance at $T_S$ , $V_{IO}$ = 500 V <sup>(1)</sup>	> 10 <sup>9</sup>	Ω	

Note:

1. Safety limit values - maximum values allowed in the event of a failure.

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.  $T_A = 25^{\circ}C$  unless otherwise specified.

Symbol	Parameter	Value	Unit
TOTAL DEV	ICE		
T <sub>STG</sub>	Storage Temperature	-40 to +125	°C
T <sub>OPR</sub>	Operating Temperature	-40 to +100	°C
ТJ	Junction Temperature	-40 to +125	°C
T <sub>SOL</sub>	Lead Solder Temperature	260 for 10 seconds	°C
D	Total Device Power Dissipation @ T <sub>A</sub> = 25°C	270	mW
PD	Derate Above 25°C	2.94	mW/°C
EMITTER			
I <sub>F</sub>	DC/Average Forward Input Current	60	mA
V <sub>R</sub>	Reverse Input Voltage	6	V
l <sub>F</sub> (pk)	Forward Current – Peak (300 µs, 2% Duty Cycle)	3	А
-	LED Power Dissipation @ T <sub>A</sub> = 25°C	120	mW
PD	Derate Above 25°C	1.41	mW/°C
DETECTOR			
V <sub>CEO</sub>	Collector-to-Emitter Voltage	30	V
V <sub>CBO</sub>	Collector-to-Base Voltage	70	V
V <sub>ECO</sub>	Emitter-to-Collector Voltage	7	V
	Detector Power Dissipation @ T <sub>A</sub> = 25°C	150	mW
PD	Derate Above 25°C	1.76	mW/°C

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## **Electrical Characteristics**

TA = 25°C unless otherwise specified.

#### Individual Component Characteristics

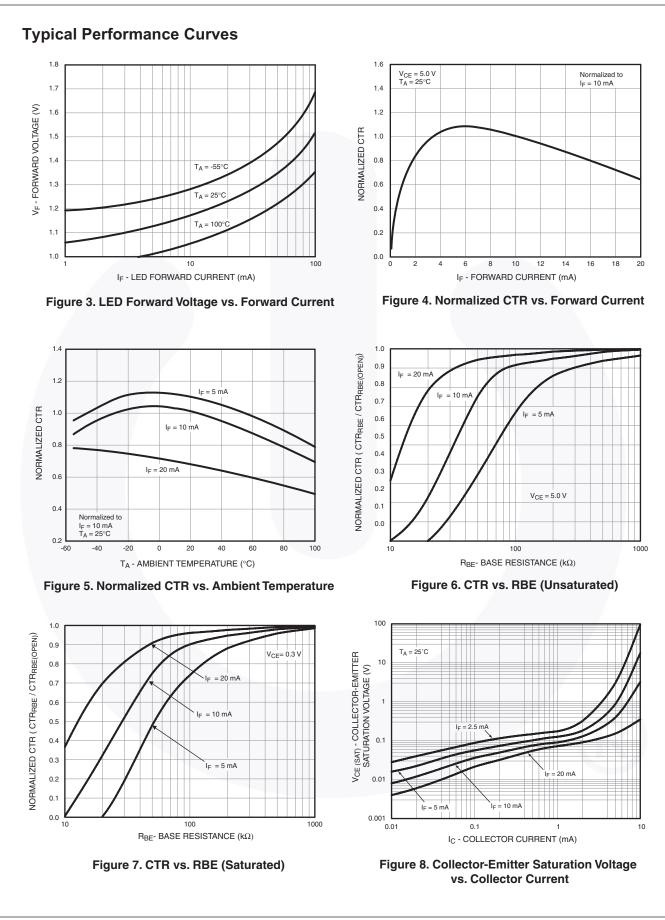
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
EMITTER						
V <sub>F</sub>	Input Forward Voltage	I <sub>F</sub> = 10 mA		1.18	1.50	V
I <sub>R</sub>	Reverse Leakage Current	V <sub>R</sub> = 6.0 V		0.001	10	μA
DETECTOR				•	•	
BV <sub>CEO</sub>	Collector-to-Emitter Breakdown Voltage	I <sub>C</sub> = 1.0 mA, I <sub>F</sub> = 0	30	100		V
BV <sub>CBO</sub>	Collector-to-Base Breakdown Voltage	I <sub>C</sub> = 100 μA, I <sub>F</sub> = 0	70	120		V
BV <sub>ECO</sub>	Emitter-to-Collector Breakdown Voltage	$I_{E} = 100 \ \mu A, I_{F} = 0$	7	10		V
I <sub>CEO</sub>	Collector-to-Emitter Dark Current	$V_{CE} = 10 \text{ V}, \text{ I}_{F} = 0$		1	50	nA
I <sub>CBO</sub>	Collector-to-Base Dark Current	V <sub>CB</sub> = 10 V	1		20	nA
C <sub>CE</sub>	Capacitance	$V_{CE} = 0 V$ , f = 1 MHz		8		pF

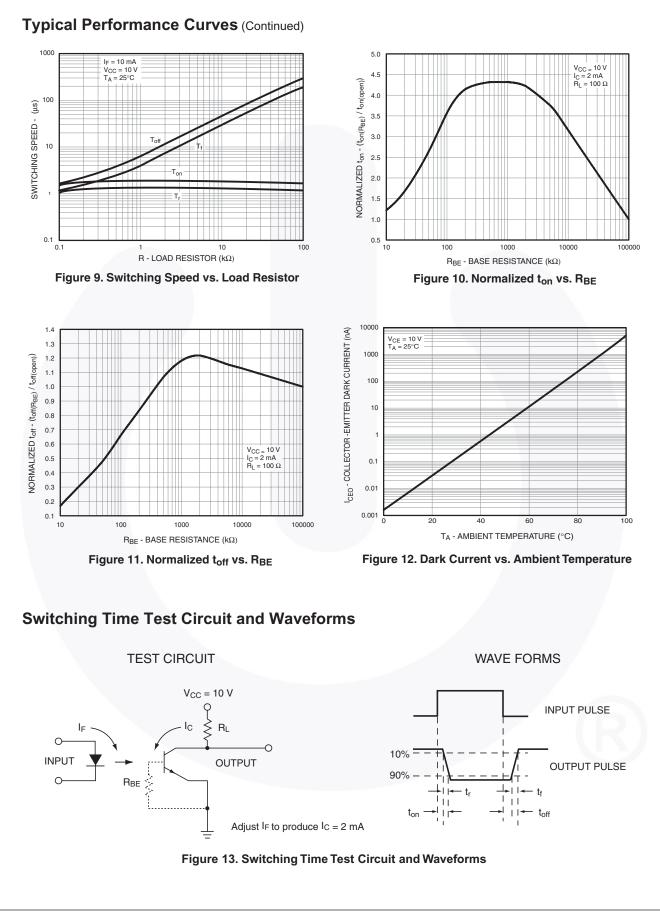
#### **Transfer Characteristics**

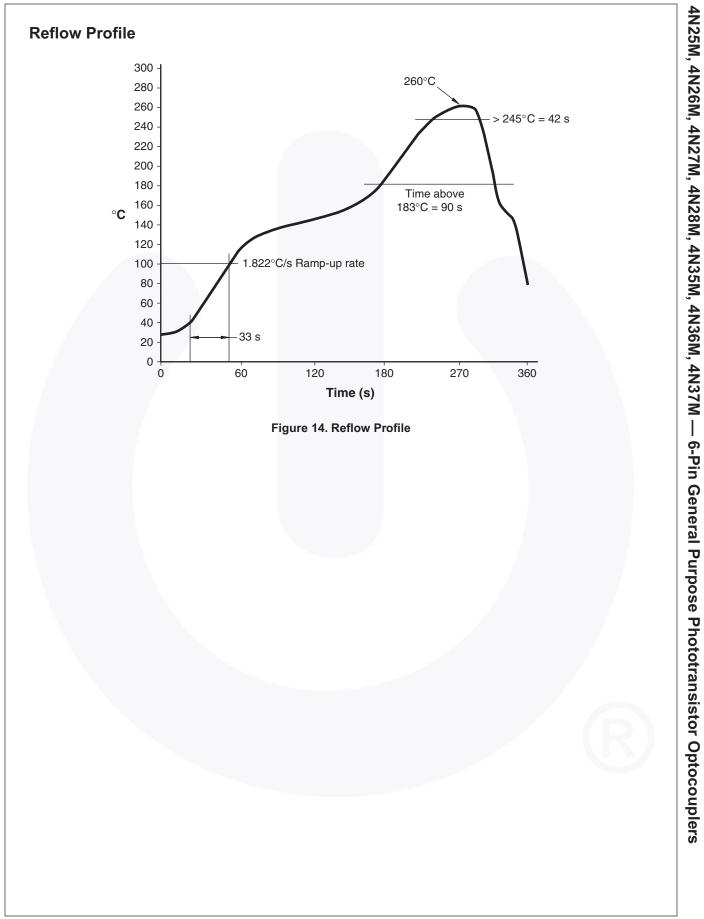
Symbol	Parameter	Test Conditions	Device	Min.	Тур.	Max.	Unit
DC CHARA	ACTERISTICS				1		
			4N35M, 4N36M, 4N37M	100			%
		I <sub>F</sub> = 10 mA, V <sub>CE</sub> = 10 V	4N25M, 4N26M	20			%
OTD	Current Transfer Ratio,		4N27M, 4N28M	10			%
CTR Collector-to-Emitter	Collector-to-Emitter	$I_F = 10 \text{ mA}, V_{CE} = 10 \text{ V},$ $T_A = -55^{\circ}\text{C}$	4N35M, 4N36M, 4N37M	40			%
		$I_F = 10 \text{ mA}, V_{CE} = 10 \text{ V},$ $T_A = +100^{\circ}\text{C}$	4N35M, 4N36M, 4N37M	40			%
	Collector-to-Emitter	I <sub>C</sub> = 2 mA, I <sub>F</sub> = 50 mA	4N25M, 4N26M, 4N27M, 4N28M			0.5	V
	Saturation Voltage	$I_{C} = 0.5 \text{ mA}, I_{F} = 10 \text{ mA}$	4N35M, 4N36M, 4N37M			0.3	V
AC CHARA	ACTERISTICS					1	
T	Non-Saturated	$I_{F} = 10 \text{ mA}, V_{CC} = 10 \text{ V}, \\ R_{L} = 100 \ \Omega \text{ (Figure 13)}$	4N25M, 4N26M, 4N27M, 4N28M		2		μs
T <sub>ON</sub>	Turn-on Time	$I_{C} = 2 \text{ mA}, V_{CC} = 10 \text{ V},$ $R_{L} = 100 \Omega \text{ (Figure 13)}$	4N35M, 4N36M, 4N37M		2	10	μs
T <sub>OFF</sub>	Turn-off Time	$I_F = 10$ mA, $V_{CC} = 10$ V, R <sub>L</sub> = 100 Ω (Figure 13)	4N25M, 4N26M, 4N27M, 4N28M		2	6	μs
		$I_{C} = 2 \text{ mA}, V_{CC} = 10 \text{ V},$ $R_{L} = 100 \Omega \text{ (Figure 13)}$	4N35M, 4N36M, 4N37M		2	10	μs

#### **Isolation Characteristics**

Symbol	Characteristic	Test Conditions	Min.	Тур.	Max.	Unit
V <sub>ISO</sub>	Input-Output Isolation Voltage	t = 1 Minute	4170			VAC <sub>RMS</sub>
C <sub>ISO</sub>	Isolation Capacitance	V <sub>I-O</sub> = 0 V, f = 1 MHz		0.2		pF
R <sub>ISO</sub>	Isolation Resistance	$V_{I-O}$ = ±500 VDC, T <sub>A</sub> = 25°C	10 <sup>11</sup>			Ω







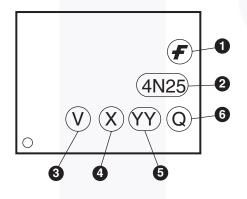
## **Ordering Information**

Part Number	Package	Packing Method
4N25M	DIP 6-Pin	Tube (50 Units)
4N25SM	SMT 6-Pin (Lead Bend)	Tube (50 Units)
4N25SR2M	SMT 6-Pin (Lead Bend) Tape and Reel (1000 Ur	
4N25VM	DIP 6-Pin, DIN EN/IEC60747-5-5 Option Tube (50 Units)	
4N25SVM	SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option	Tube (50 Units)
4N25SR2VM	SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option Tape and Reel (1000 Uni	
4N25TVM	DIP 6-Pin, 0.4" Lead Spacing, DIN EN/IEC60747-5-5 Option Tube (50 Units)	

#### Note:

2. The product orderable part number system listed in this table also applies to the 4N26M, 4N27M, 4N28M, 4N35M, 4N36M, and 4N37M devices.

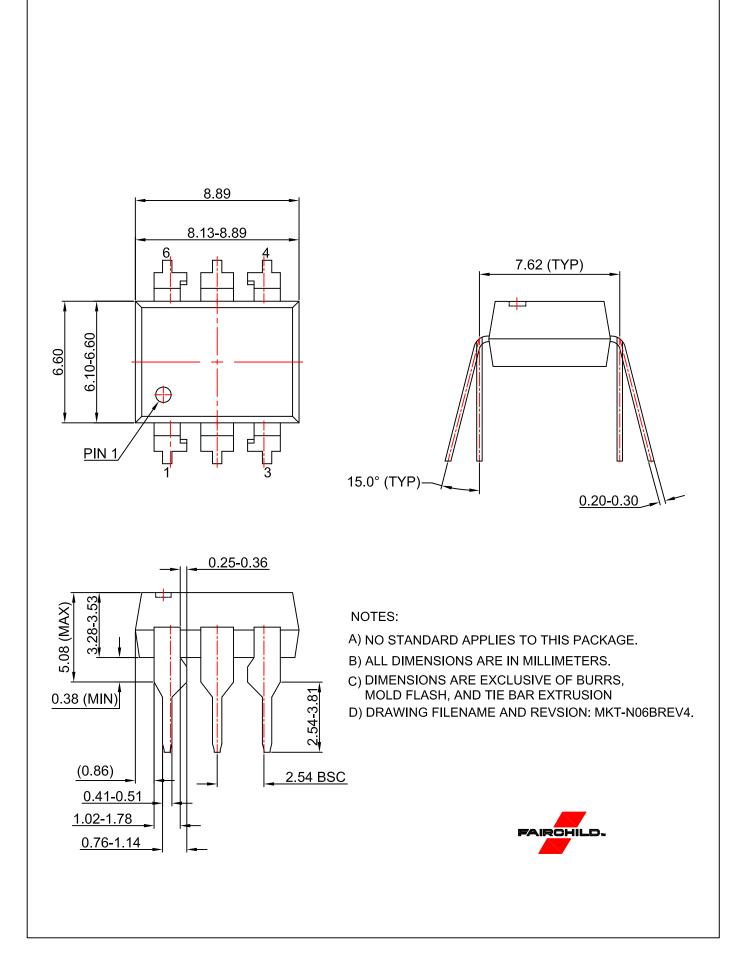
## **Marking Information**

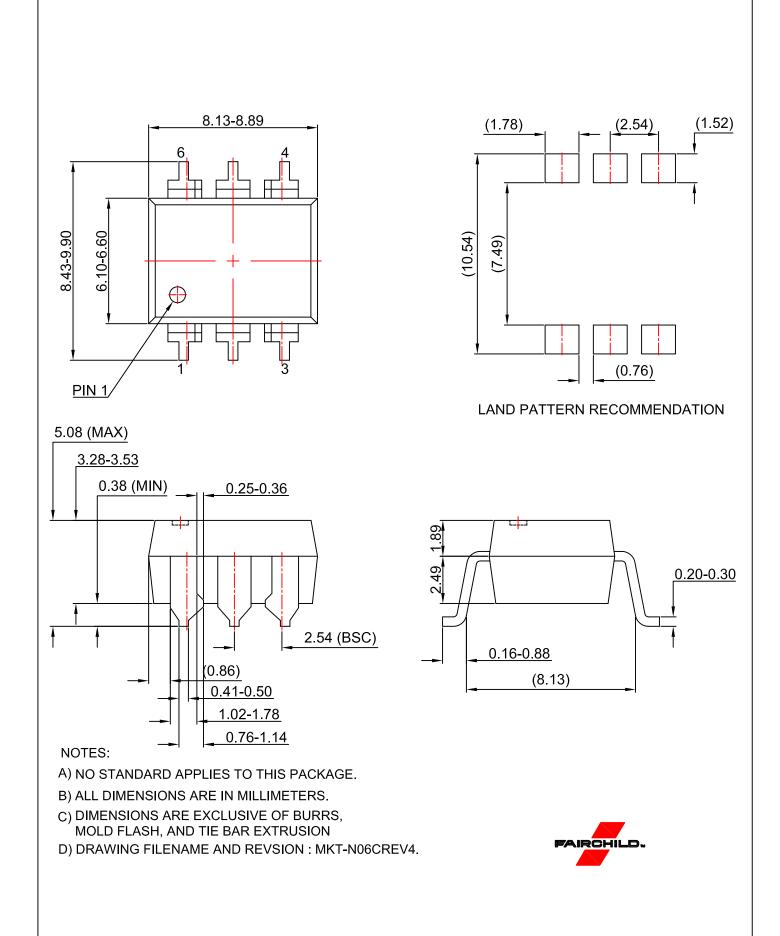


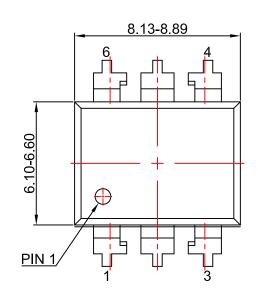
#### Figure 15. Top Mark

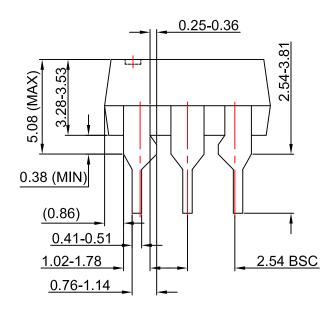
#### Table 1. Top Mark Definitions

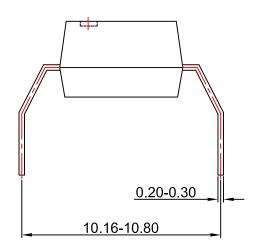
1	Fairchild Logo	
2	Device Number	
3	DIN EN/IEC60747-5-5 Option (only appears on component ordered with this option)	
4	One-Digit Year Code, e.g., "5"	
5	Digit Work Week, Ranging from "01" to "53"	
6	Assembly Package Code	







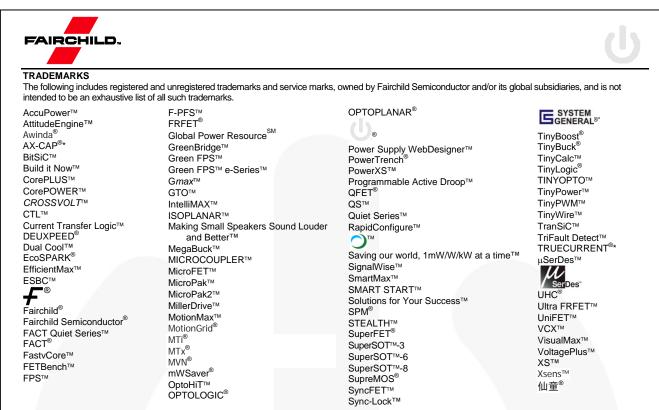




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