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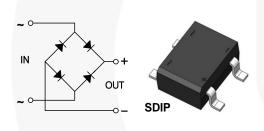
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# DF005S2 - DF10S2 Bridge Rectifier

### Features

- Maximum Surge Rating:  $I_{FSM} = 85 \text{ A}$  $I^2t = 30 \text{ A}^2\text{Sec}$
- Optimized V<sub>F</sub>: Typical 0.93 V at 2 A, 25°C
- DF10S Socket Compatible
- Glass Passivated Junctions
- Lead Free Compliant to EU RoHS 2002/95/EU Directives
- Green Molding Compound: IEC61249
- Qualified with IR Reflow and Wave Soldering



### Description

With the ever-pressing need to improve power supply efficiency, improve surge rating, improve reliability, and reduce size, the DFxS2 family sets a new standard in performance.

The new design offers an improved surge rating of 85 A. This is especially important when striving to improve reliability and increase efficiency. High efficiency designs strive to reduce circuit resistance, which, unfortunately can result in increased inrush surge. As such higher surge current ratings can be required to maintain or improve reliability.

The design also offers improved efficiency by achieving a 2 A V<sub>F</sub> of 1.1 V maximum at 25°C. This lower V<sub>F</sub> also supports cooler and more efficient operation.

Finally, the DFxS2 achieves all this in a SDIP surface mount form factor, reducing board space and volumetric requirements vs. competitive devices.

### **Ordering Information**

Part Number	Top Mark	Package	Packing Method
DF005S2	DF005S2	SDIP 4L	Tape and Reel
DF01S2	DF01S2	SDIP 4L	Tape and Reel
DF02S2	DF02S2	SDIP 4L	Tape and Reel
DF04S2	DF04S2	SDIP 4L	Tape and Reel
DF06S2	DF06S2	SDIP 4L	Tape and Reel
DF08S2	DF08S2	SDIP 4L	Tape and Reel
DF10S2	DF10S2	SDIP 4L	Tape and Reel

June 2015

### **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value							Unit
Symbol	DF005S2 DF01S2 DF02		DF02S2	DF04S2	DF06S2	DF08S2	DF10S2	onn	
V <sub>RRM</sub>	Maximum Recurrent Peak Reverse Voltage	50	100	200	400	600	800	1000	V
V <sub>RMS</sub>	Maximum RMS Bridge Input Voltage	35	70	140	280	420	560	700	V
V <sub>DC</sub>	Maximum DC Blocking Voltage	50	100	200	400	600	800	1000	V
I <sub>F(AV)</sub>	Maximum Average Forward Current $T_A = 40^{\circ}C$				2.0				А
I <sub>FSM</sub>	Peak Forward Surge Current 8.3 ms Single Half-Sine Wave Superimposed on Rated Load(JEDEC Method)				85				A
T <sub>STG</sub>	Storage Temperature Range			-{	55 to +150				°C
TJ	Operating Junction Temperature Range			-	55 to +150				°C

### Thermal Characteristics<sup>(1)</sup>

Symbol	Parameter	Conditions	Max.	Unit
		Single-Die Measurement (Maximum Land Pattern: 13 x 13 mm)	60	
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient			°C/W
		Multi-Die Measurement (Minimum Land Pattern: 1.3 x 1.5 mm)	100	
ΨJL	Thermal Characterization Parameter, Junction to Lead	Single-Die Measurement (Maximum and Minimum Land Pattern)	25	°C/W

### Note:

1. The thermal resistances ( $R_{\theta JA} \& \psi_{JL}$ ) are characterized with the device mounted on the following FR4 printed circuit boards, as shown in Figure 1 and Figure 2. PCB size: 76.2 x 114.3 mm.

Heating effect from adjacent dice is considered and only two dices are powered at the same time.

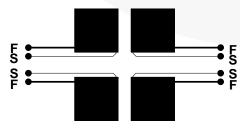
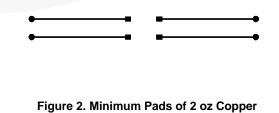


Figure 1. Maximum Pads of 2 oz Copper

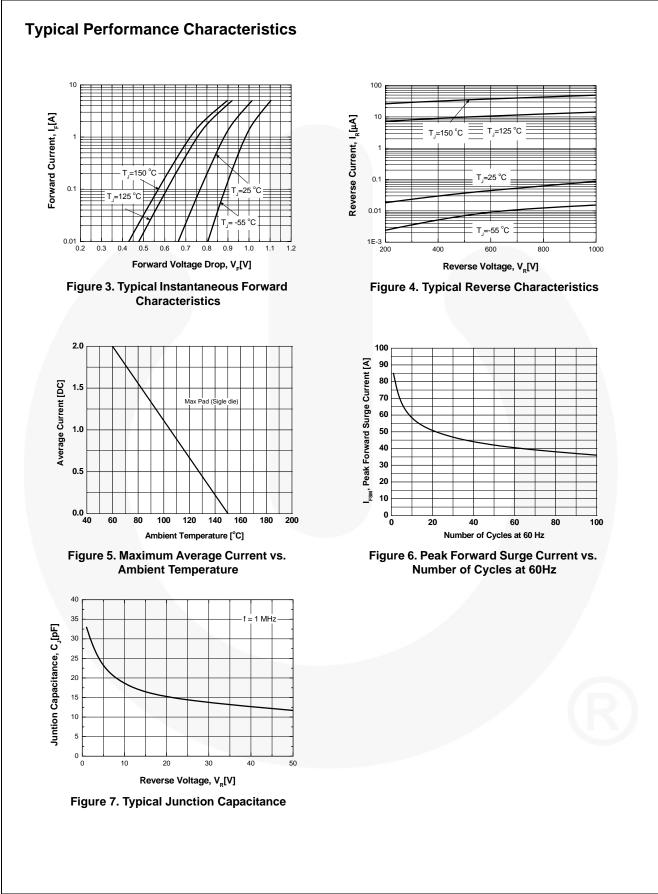


# DF005S2 - DF10S2 — Bridge Rectifier

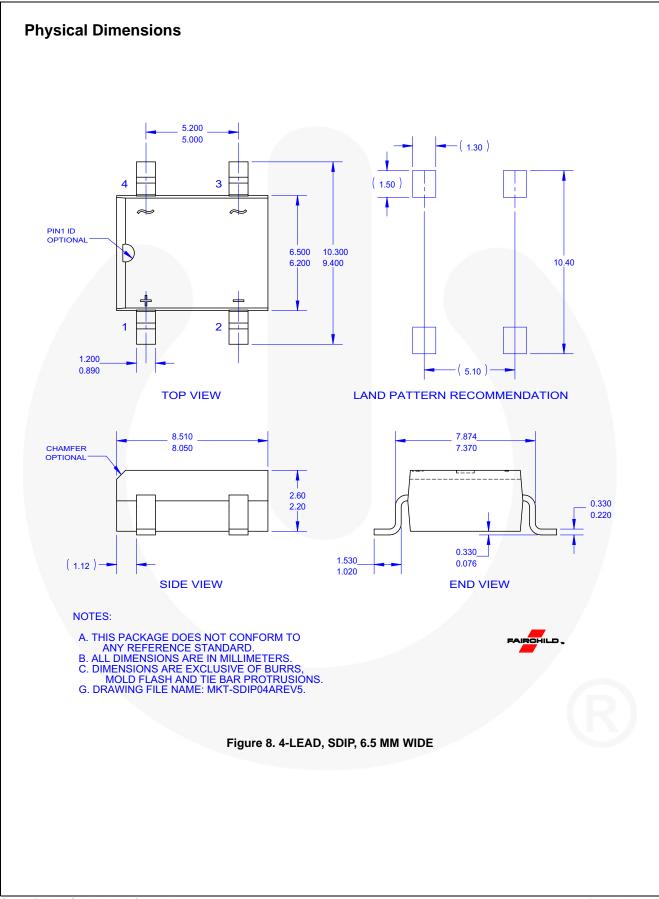
## **Electrical Characteristics**

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V <sub>F</sub>	Forward Voltage Drop per Bridge Element	I <sub>F</sub> = 2.0 A			1.1	V
1	DC Reverse Current	$T_J = 25^{\circ}C$			3	
۱ <sub>R</sub>	at Rated DC Blocking Voltage	T <sub>J</sub> = 125°C			500	μA
l <sup>2</sup> t	Rating for Fusing (t < 8.3 ms)				30	A <sup>2</sup> S
CJ	Junction Capacitance	V <sub>R</sub> = 4.0 V, f = 1.0 MHz		23		pF



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DF005S2 - DF10S2 — Bridge Rectifier

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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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