

N-Channel PowerTrench[®] MOSFET 30 V, 6.1 A, 26 m Ω

Features

- Max $r_{DS(on)} = 26 \text{ m}\Omega \text{ at } V_{GS} = 10 \text{ V}, I_D = 6.1 \text{ A}$
- Max $r_{DS(on)} = 33 \text{ m}\Omega \text{ at } V_{GS} = 4.5 \text{ V}, I_D = 5.3 \text{ A}$
- High Performance Trench Technology for Extremely Low rDS(on)
- High Power and Current Handling Capability in a Widely Used Surface Mount Package
- Fast Switching Speed
- RoHS Compliant



General Description

This N-Channel PowerTrench MOSFET is produced using Fairchild's advanced PowerTrench[®] process that has been especially tailored to minimize on-state resistance and yet maintain low gate charge for superior switching performance.

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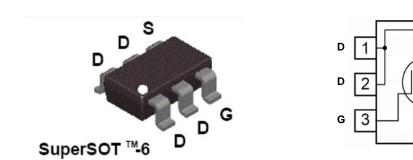
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Applications

- Load Switch
- Battery Protection
- Power Management



MOSFET Maximum Ratings TA= 25°C unless otherwise noted.

Symbol	Parameter			Ratings	Units	
V _{DS}	Drain to Source Voltage			30	V	
V _{GS}	Gate to Source Voltage		(Note 3)	±20	V	
I _D	-Continuous	$T_A = 25^{\circ}C$	(Note 1a)	6.1	^	
	-Pulsed		(Note 4)	62	Α	
Ĺ	Power Dissipation		(Note 1a)	1.6	14/	
PD	Power Dissipation (Note 1b)		(Note 1b)	0.7	W	
T _J , T _{STG}	Operating and Storage Junction Temperature Range			-55 to + 150	°C	

Thermal Characteristics

$R_{ ext{ heta}JA}$	Thermal Resistance, Junction to Ambient	(Note 1a)	78	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1b)	175	°C/W

Package Marking and Ordering Information

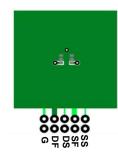
Device Marking	Device	Package	Reel Size	Tape Width	Quantity
21N	FDC021N30	SSOT-6 [™]	7 "	8 mm	3000 units

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Chara	octeristics					
BV _{DSS}	Drain to Source Breakdown Voltage	I _D = 250 μA, V _{GS} = 0 V	30			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	$I_D = 250 \ \mu$ A, referenced to 25°C		16		mV/°0
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 24 V, V _{GS} = 0 V			1	μA
I _{GSS}	Gate to Source Leakage Current	$V_{GS} = 20 V, V_{DS} = 0 V$			100	nA
On Chara	cteristics					
V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 250 \mu A$	1.0	1.8	3.0	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	$I_D = 250 \ \mu$ A, referenced to 25°C		-5		mV/°C
	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 6.1 A		19	26	
r _{DS(on)}		V _{GS} = 4.5 V, I _D = 5.3 A		23	33	mΩ
		V _{GS} = 10 V, I _D = 6.1 A, T _J = 125°C		26	37	
9 _{FS}	Forward Transconductance	V _{DS} = 5 V, I _D = 6.1 A		30		S
	Characteristics					
C _{iss}	Input Capacitance	── V _{DS} = 15 V, V _{GS} = 0 V, ── f = 1 MHz		510	710	pF
C _{oss}	Output Capacitance			170	240	pF
C _{rss}	Reverse Transfer Capacitance			22	30	pF
R _g	Gate Resistance		0.1	1.3	2.6	Ω
Switching	g Characteristics					
d(on)	Turn-On Delay Time			6	12	ns
r	Rise Time	V _{DD} = 15 V, I _D = 6.1 A,		2	10	ns
d(off)	Turn-Off Delay Time	V_{GS} = 10 V, R_{GEN} = 6 Ω		13	24	ns
f	Fall Time			2	10	ns
ຊ _{g(TOT)}	Total Gate Charge	$\frac{V_{GS} = 0 \text{ V to } 10 \text{ V}}{V_{GS} = 0 \text{ V to } 4.5 \text{ V}} V_{DD} = 15 \text{ V},$ $I_{D} = 6.1 \text{ A}$		7.7	10.8	nC
Q _{g(TOT)}	Total Gate Charge	$V_{GS} = 0 V \text{ to } 4.5 V$ $V_{DD} = 15 V$, $V_{DD} = 6.1 A$		3.7	5.2	nC
ସ _{gs}	Gate to Source Charge			1.4		nC
Q _{gd}	Gate to Drain "Miller" Charge			1.1		nC
Drain-Sou	urce Diode Characteristics					
V _{SD}	Source to Drain Diode Forward Voltage	$V_{GS} = 0 V, I_S = 6.1 A$ (Note 2)		0.8	1.2	V
t _{rr}	Reverse Recovery Time			14	25	ns
Q _{rr}	Reverse Recovery Charge	I _F = 6.1 A, di/dt = 100 A/μs		3	10	nC

Reverse Recovery Charge

 Q_{rr} Notes:

1: R_{0JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{0JC} is guaranteed by design while R_{0CA} is determined by the user's board design.



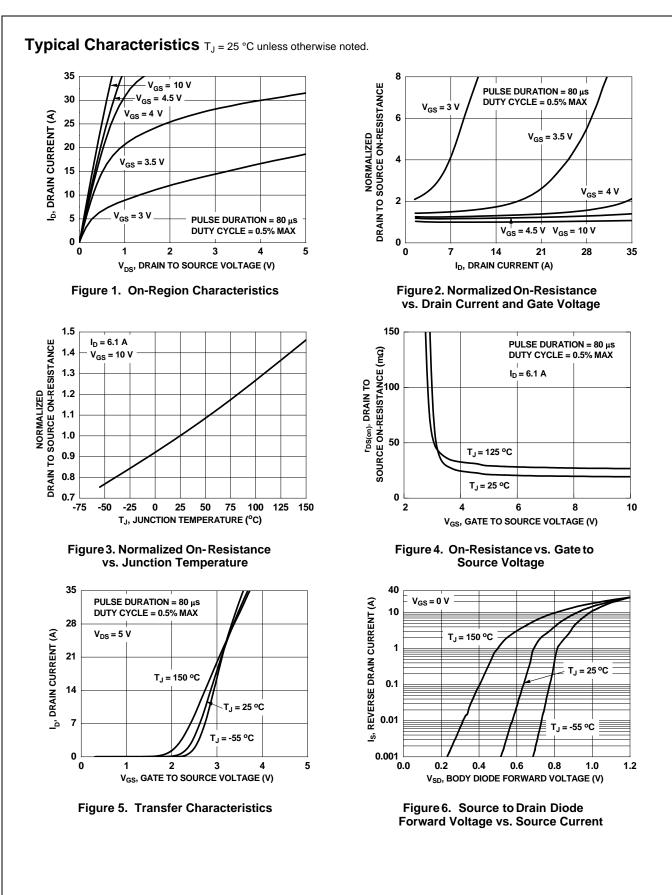
a. 78 °C/W when mounted on a 1 in² pad of 2 oz copper



b.175 °C/W when mounted on a minimum pad of 2 oz copper

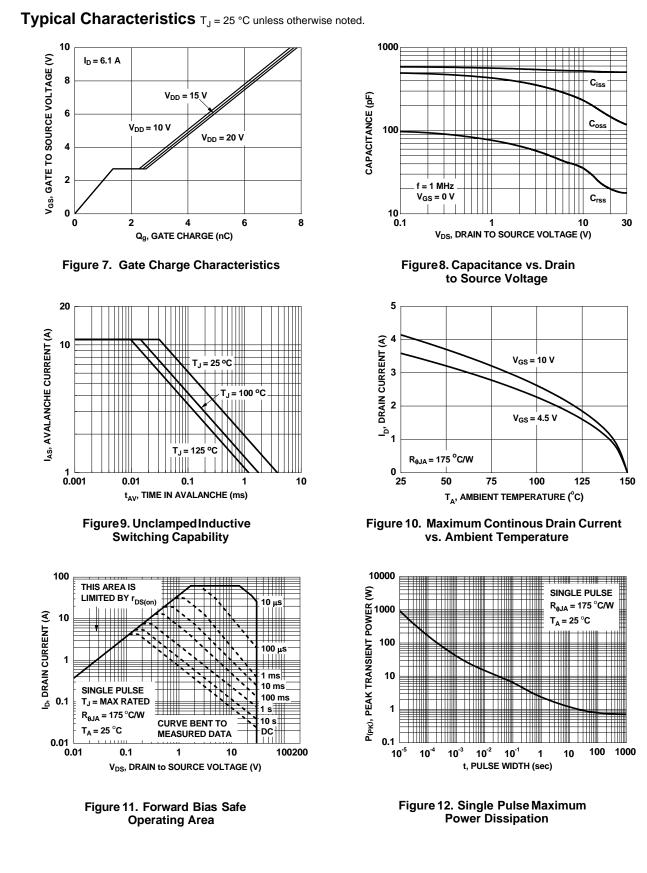
Pulse Test: Pulse Width<300 us, Duty Cycle<2.0%.
 As an N-ch device, the negative Vgs rating is for low duty cycle pulse occurrence only. No continuous rating is implied.
 Pulsed Id please refer to Fig 11 SOA graph for more details.

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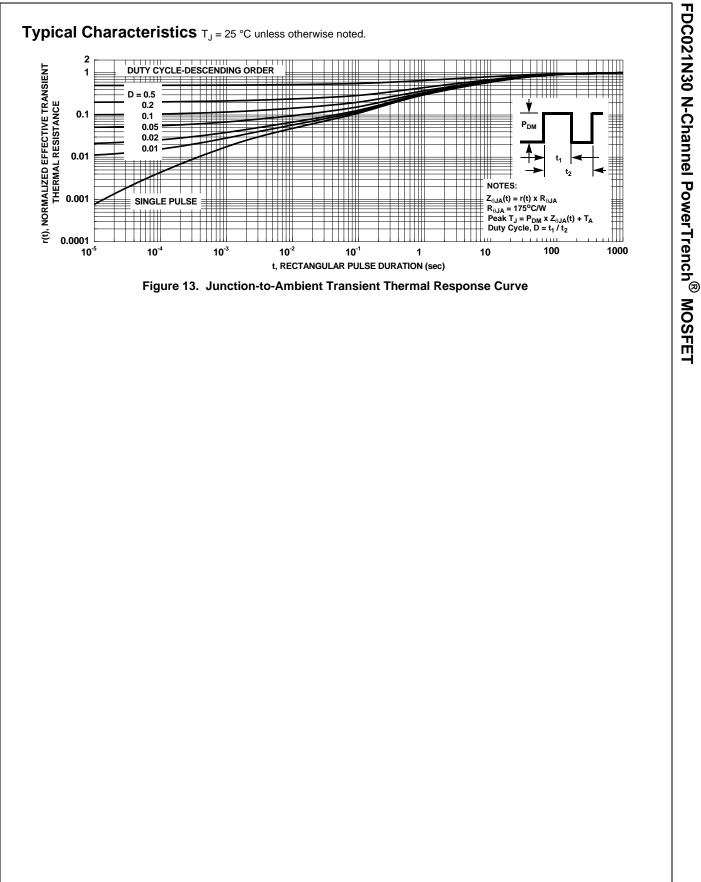


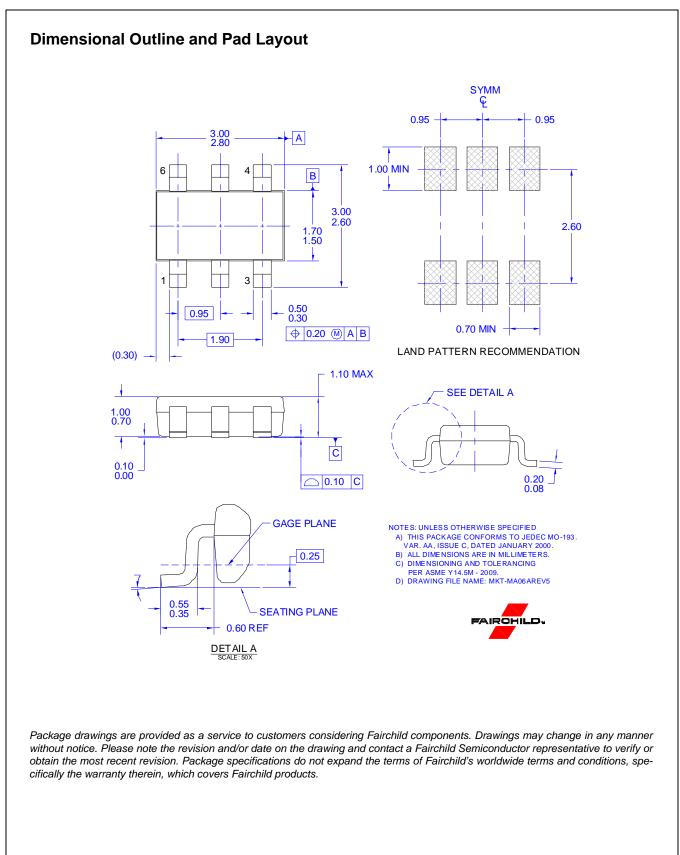
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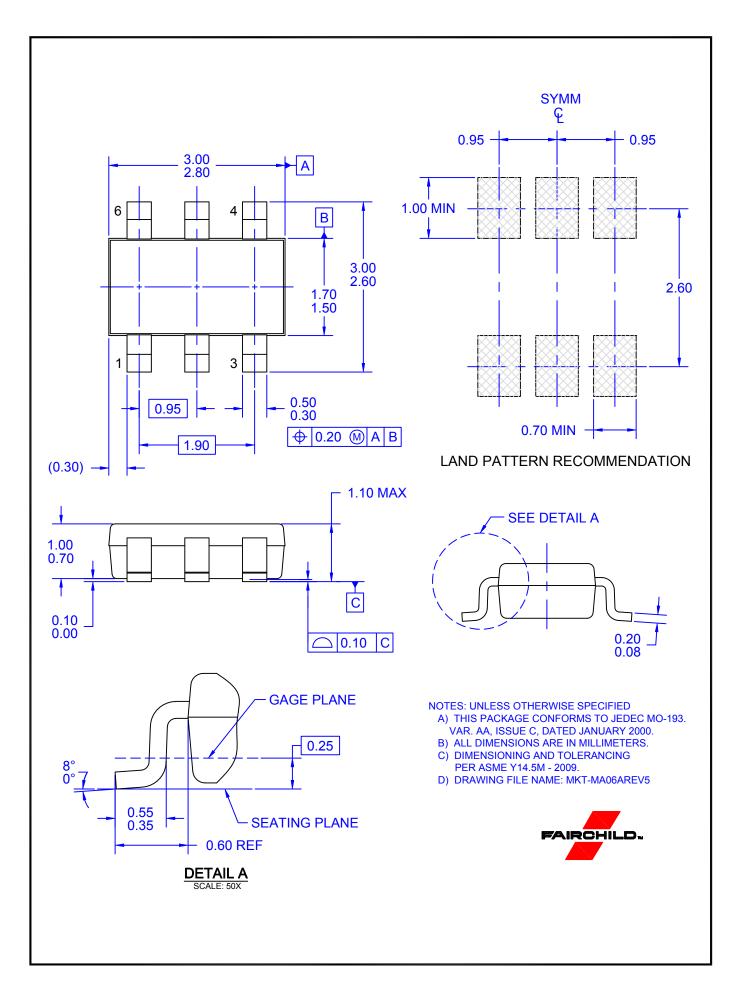
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