FDC3612 100V N-Channel PowerTrench[®] MOSFET

General Description

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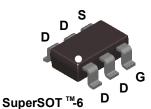
This N-Channel MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers. It has been optimized for low gate charge, low $R_{DS(ON)}$ and fast switching speed.

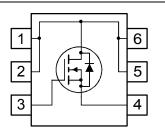
Applications

DC/DC converter

Features

- 2.6 A, 100 V $R_{DS(ON)} = 125 \text{ m}\Omega @ V_{GS} = 10 \text{ V}$ $R_{DS(ON)} = 135 \text{ m}\Omega @ V_{GS} = 6 \text{ V}$
- High performance trench technology for extremely low $R_{\text{DS}(\text{ON})}$
- Low gate charge (14nC typ)
- High power and current handling capability
- Fast switching speed





Absolute Maximum Ratings T_A=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V _{DSS}	Drain-Source Voltage		100	V
V _{GSS}	Gate-Source Voltage		± 20	V
ID	Drain Current – Continuous	(Note 1a)	2.6	A
	– Pulsed		20	
P _D	Maximum Power Dissipation	(Note 1a)	1.6	W
		(Note 1b)	0.8	
T _J , T _{STG}	Operating and Storage Junction Temperature Range		-55 to +150	°C
Therma	I Characteristics			
D	Thormal Posistanco Junction to Ambio	nt (Nata 1a)	79	°C/M

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	(Note 1a)	78	°C/W
R _{θJC}	Thermal Resistance, Junction-to-Case	(Note 1)	30	°C/W

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape width	Quantity
.362	FDC3612	7"	8mm	3000 units

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FDC3612

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
-				176	шах	Unite
	Durce Avalanche Ratings (Note				90	
W _{DSS}	Drain-Source Avalanche Energy Drain-Source Avalanche Current	Single Pulse, V_{DD} = 50 V, I_D =2.6 A			2.6	mJ A
I _{AR}					2.0	A
	acteristics				1	
BV _{DSS}	Drain–Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	100		-	V
<u>ΔBV_{DSS}</u> ΔT _J	Breakdown Voltage Temperature Coefficient	I_D = 250 µA, Referenced to 25°C		99		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 80 V, V_{GS} = 0 V			10	μA
I _{GSSF}	Gate–Body Leakage, Forward	V_{GS} = 20 V, V_{DS} = 0 V			100	nA
I _{GSSR}	Gate–Body Leakage, Reverse	V_{GS} = -20 V, V_{DS} = 0 V			-100	nA
On Char	acteristics (Note 2)					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \ \mu A$	2	2.3	4	V
$\frac{\Delta V_{GS(th)}}{\Delta T_{J}}$	Gate Threshold Voltage Temperature Coefficient	$I_D = 250 \ \mu$ A, Referenced to 25°C		- 6		mV/°C
R _{DS(on)}	Static Drain–Source On Resistance	$V_{GS} = 10 \text{ V}, \text{ I}_D = 2.6 \text{ A}$ $V_{GS} = 6.0 \text{ V}, \text{ I}_D = 2.5 \text{ A}$ $V_{GS} = 10 \text{ V}, \text{ I}_D = 2.6 \text{ A}; \text{T}_J = 125^{\circ}\text{C}$		86 91 157	125 135 240	mΩ
I _{D(on)}	On–State Drain Current	$V_{GS} = 10 \text{ V}, V_{DS} = 5 \text{ V}$	10	-		Α
g _{FS}	Forward Transconductance	V _{DS} = 10 V, I _D = 2.6 A		10		S
Dynamic	Characteristics	-				
C _{iss}	Input Capacitance	$V_{DS} = 50 V$, $V_{GS} = 0 V$,		660		pF
Coss	Output Capacitance	f = 1.0 MHz		55		pF
C _{rss}	Reverse Transfer Capacitance			40		pF
Switchin	g Characteristics (Note 2)					
t _{d(on)}	Turn–On Delay Time	$V_{pp} = 50 V$ $I_p = 1 A$		6	11	ns
t _r	Turn–On Rise Time	$V_{DD} = 50 V,$ $I_D = 1 A,$ $V_{GS} = 10 V,$ $R_{GEN} = 6 Ω$		3.5	7	ns
t _{d(off)}	Turn–Off Delay Time	-		23	37	ns
t _f	Turn–Off Fall Time	-		3.7	7.4	ns
Q _g	Total Gate Charge	$V_{\rm DS} = 50 \text{ V}, \qquad I_{\rm D} = 2.6 \text{ A},$		14	20	nC
Q _{qs}	Gate–Source Charge	V _{GS} = 10 V		2.3		nC
Q _{gd}	Gate–Drain Charge	-		3.6		nC
-	ource Diode Characteristics	and Maximum Patings		L		
	Maximum Continuous Drain–Source				1.3	А
V _{SD}	Drain–Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, \text{ I}_{S} = 1.3 \text{ A}$ (Note 2)		0.76	1.2	V
t _{rr}	Diode Reverse Recovery Time	I _F = 2.6 A		31		nS
Q _{rr}	Diode Reverse Recovery Charge	$d_{iF}/d_t = 100 \text{ A}/\mu \text{s}$ (Note 2)	<u> </u>	56	<u> </u>	nC

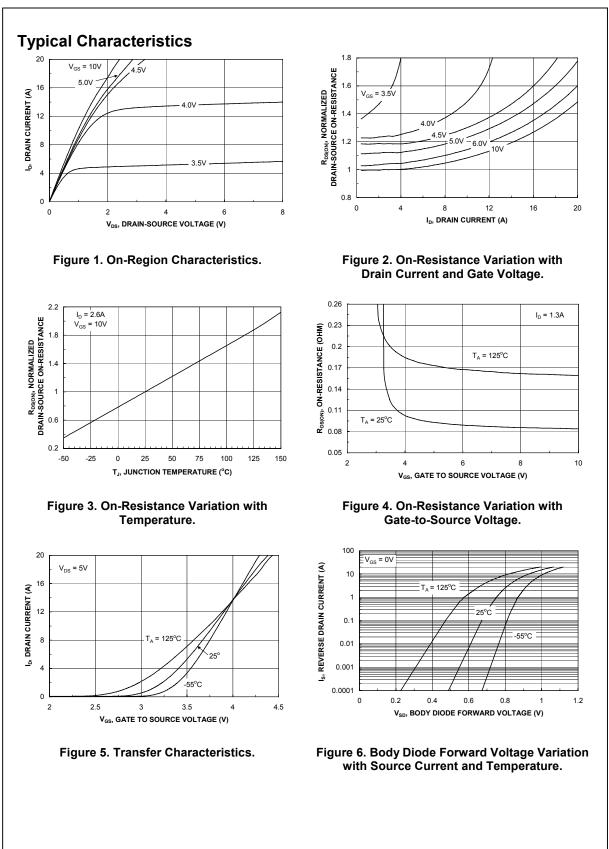
Notes:

1. $R_{_{0JA}}$ is the sum of the junction-to-case and case-to-ambient resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{_{0JC}}$ is guaranteed by design while $R_{_{0CA}}$ is determined by the user's board design.

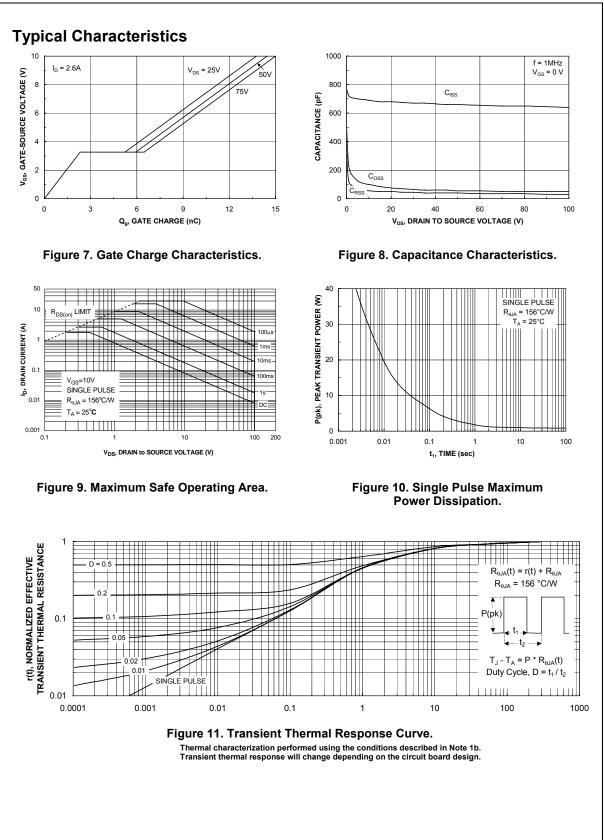
a. 78°C/W when mounted on a 1in² pad of 2oz copper on FR-4 board.

b. 156°C/W when mounted on a minimum pad.

2. Pulse Test: Pulse Width \leq 300 $\mu s,$ Duty Cycle \leq 2.0%



FDC3612



FDC3612 Rev B4

FDC3612



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