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					April 2007	
FDC6	331L					
ntegrat	ed Load	Switch				
General Des	scription		Feature	es		
This device is particularly suited for compact power management in portable electronic equipment where 2.5V to 8V input and 2.8A output current capability are needed. This load switch integrates a small N-Channel power MOSFET (Q1) that drives a large PChannel power MOSFET (Q2) in one tiny SuperSOT <sup>TM</sup> -6 package.		rer are nel nel • Contro	$\begin{array}{l} -2.8 \text{ A}, -8 \text{ V}.  R_{\text{DS(ON)}} = 55 \text{ m}\Omega @ \text{V}_{\text{GS}} = -4.5 \text{ V} \\ R_{\text{DS(ON)}} = 70 \text{ m}\Omega @ \text{V}_{\text{GS}} = -2.5 \text{ V} \\ R_{\text{DS(ON)}} = 100 \text{ m}\Omega @ \text{V}_{\text{GS}} = -1.8 \text{ V} \end{array}$ $\begin{array}{l} \text{Control MOSFET (Q1) includes Zener protection for ESD ruggedness (>6KV Human body model)} \end{array}$			
Load swit Power ma	ch		• High p low R <sub>c</sub>	erformance trench technolo	ogy for extremely	
	ıperSOT™-			Equivalen		
			C unless otherwise noted	Potingo	Unite	
Symbol V <sub>IN</sub>	Maximum	Parameter Input Voltage		Ratings ± 8	Units V	
V <sub>ON/OFF</sub>		ON/OFF voltage range			v	
Load	-	Load Current – Continuous (No		2.8	A	
		– Pulsed		9		
PD	Maximum	Power Dissipation	(Note 1)	0.7	W	
T <sub>J</sub> , T <sub>STG</sub>	Operating	Operating and Storage Junction Temperature Range -55 to +150			°C	
Therma	al Charac	teristics				
R <sub>0JA</sub>		ermal Resistance, Junction-to-Ambient (Note 1) 180		°C/M		
R <sub>0JC</sub>	Thermal R	esistance, Junction-to-Cas	e (Note 1)	60	60 °C/V	
Packao	le Markin	ig and Ordering I	nformation			
	Marking	Device	Reel Size	Tape width	Quantity	
	331	FDC6331L	7"	8mm	3000 units	
			· · ·			

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FDC6331L Rev D

# FDC6331L

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Cha	racteristics				•	
BV <sub>IN</sub>	Vin Breakdown Voltage	$V_{ON/OFF} = 0 V, I_D = -250 \mu A$	8			V
ILoad	Zero Gate Voltage Drain Current	V <sub>IN</sub> = 6.4 V, V <sub>ON/OFF</sub> = 0 V			-1	μA
I <sub>FL</sub>	Leakage Current, Forward	V <sub>ON/OFF</sub> = 0 V, V <sub>IN</sub> = 8 V			-100	nA
I <sub>RL</sub>	Leakage Current, Reverse	$V_{ON/OFF} = 0 V, V_{IN} = -8 V$			100	nA
On Cha	racteristics (Note 2)					
V <sub>ON/OFF (th)</sub>	Gate Threshold Voltage	$V_{IN} = V_{ON/OFF}$ , $I_D = -250 \ \mu A$	0.4	0.9	1.5	V
R <sub>DS(on)</sub>	Static Drain–Source On–Resistance (Q2)	$ \begin{array}{ll} V_{GS} = -4.5 \ V, & I_D = -2.8 A \\ V_{GS} = -2.5 \ V, & I_D = -2.5 \ A \\ V_{GS} = -1.8 \ V, & I_D = -2.0 \ A \end{array} $		34 45 64	55 70 100	mΩ
R <sub>DS(on)</sub>	Static Drain–Source On–Resistance (Q1)	$V_{GS} = 4.5 V, I_D = 0.4A$ $V_{GS} = 2.7 V, I_D = 0.2 A$		3.1 3.8	4 5	Ω

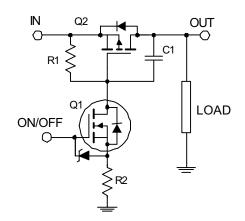
### Drain–Source Diode Characteristics and Maximum Ratings

ls	Maximum Continuous Drain–Source Diode Forward Current			-0.6	Α
V <sub>SD</sub>	Drain–Source Diode Forward Voltage	$V_{ON/OFF} = 0 V, I_S = -0.6 A$ (Note 2)		-1.2	V

Notes: 1. R<sub>0JA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R<sub>0JC</sub> is guaranteed by design while R<sub>0JA</sub> is determined by the user's board design.

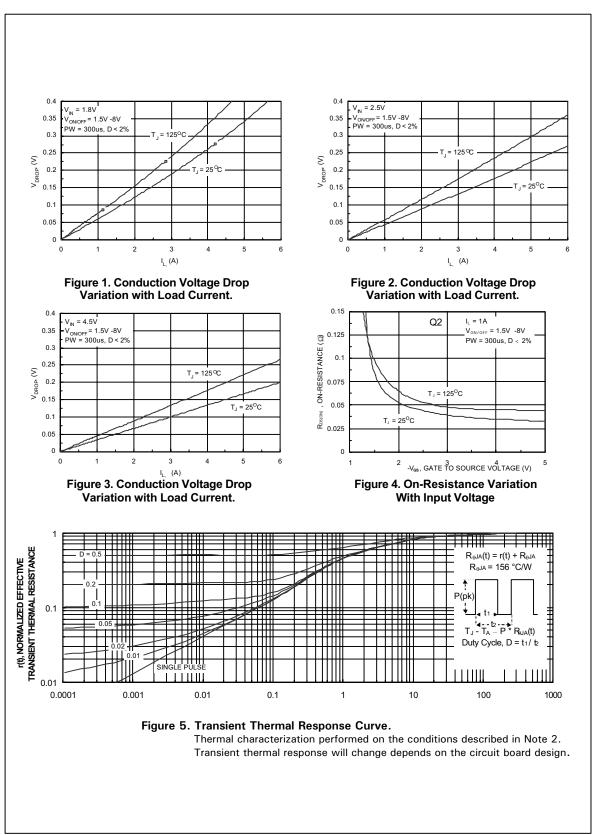
2. Pulse Test: Pulse Width < 300µs, Duty Cycle < 2.0%.

### FDC6331L Load Switch Application Circuit



External Component Recommendation: For additional in-rush current control, R2 and C1 can be added. For more information, see application note AN1030. FDC6331L

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