FAIRCHILD

SEMICONDUCTOR

FDD3672_F085 N-Channel UltraFET Trench MOSFET

100V, 44A, 28m Ω

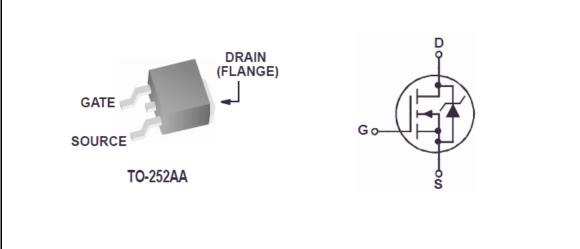
Features

- Typ $r_{DS(on)}$ = 24m Ω at V_{GS} = 10V, I_D = 44A
- Typ Q_{g(10)} = 24nC at V_{GS} = 10V
- Low Miller Charge
- Low Q_{rr} Body Diode
- Optimized efficiency at high frequencies
- UIS Capability (Single Pulse and Repetitive Pulse)
- Qualified to AEC Q101
- RoHS Compliant

Applications

- DC/DC converters and Off-Line UPS
- Distributed Power Architectures and VRMs
- Primary Switch for 24V and 48V Systems
- High Voltage Synchronous Rectifier





March 2011

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MOSFET Maximum Ratings $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter		Ratings	Units
V _{DSS}	Drain to Source Voltage		100	V
V _{GS}	Gate to Source Voltage		±20	V
1	Drain Current Continuous (T _C < 30 ^o C, V _{GS} = 10V)		44	A
D	Pulsed		See Figure 4	A
E _{AS}	Single Pulse Avalanche Energy (N	ote 1)	73	mJ
Р	Power Dissipation		144	W
PD	Derate above 25°C		0.96	W/ºC
T _J , T _{STG}	Operating and Storage Temperature		-55 to +175	°C

Thermal Characteristics

R_{\thetaJC}	Maximum Thermal Resistance Junction to Case	1.04	°C/W
R_{\thetaJA}	Maximum Thermal Resistance Junction to Ambient TO-263,1in ² copper pad area	52	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDD3672	FDD3672_F085	TO-252AA	330mm	16mm	2500 units

Electrical Characteristics $T_J = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Cha	racteristics					
B _{VDSS}	Drain to Source Breakdown Voltage	$I_{D} = 250 \mu A, V_{GS} = 0 V$	100	-	-	V
1	Zero Gate Voltage Drain Current	$V_{DS} = 80V, V_{GS} = 0V$	-	-	1	
DSS	Zero Gale voltage Dialit Guiterit	$T_J = 150^{\circ}C$	-	-	250	μA

 $V_{GS} = \pm 20V$

On Characteristics

 I_{GSS}

Gate to Source Leakage Current

V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250 \mu A$	2	3	4	V
		I _D = 44A, V _{GS} = 10V	-	0.024	0.028	Ω
r _{DS(on)} Drain to	Drain to Source On Resistance	I _D = 21A, V _{GS} = 6V,	-	0.028	0.047	Ω
	- I I I I I I I I I I I I I I I I I I I	$I_D = 44A, V_{GS} = 10V, T_J = 175^{\circ}C$	-	0.063	0.074	Ω

Dynamic Characteristics

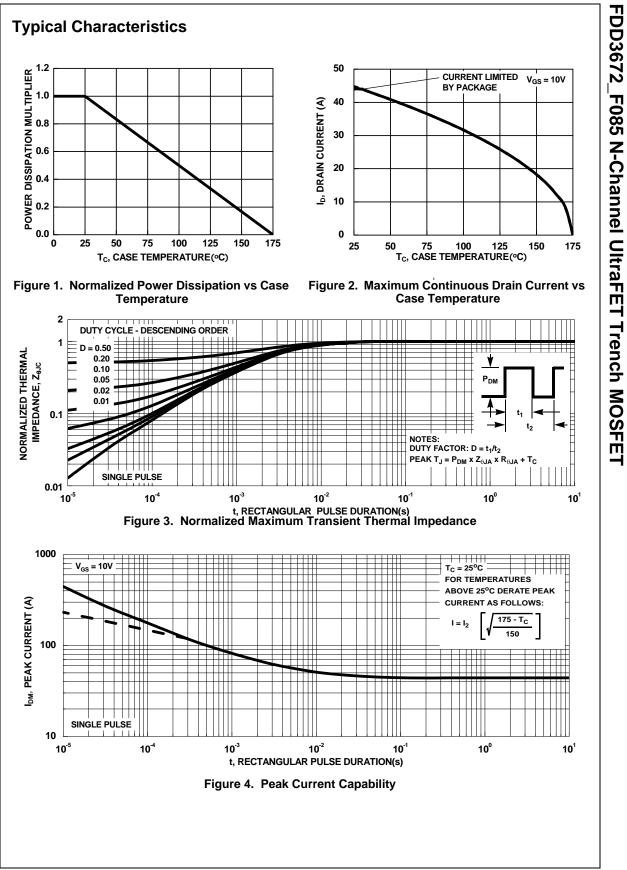
C _{iss}	Input Capacitance	— V _{DS} = 25V, V _{GS} = 0V, f = 1MHz		-	1635	-	pF
C _{oss}	Output Capacitance			-	240	-	pF
C _{rss}	Reverse Transfer Capacitance			-	60	-	pF
Q _{g(TOT)}	Total Gate Charge at 10V	$V_{GS} = 0$ to 10V		-	24	36	nC
Q _{g(TH)}	Threshold Gate Charge	$V_{GS} = 0$ to 2V	V _{DD} = 50V	-	3	4.5	nC
Q _{gs}	Gate to Source Gate Charge		I _D = 44A	-	8.3	-	nC
Q _{gs2}	Gate Charge Threshold to Plateau		l _g = 1.0mA	-	5.3	-	nC
Q _{gd}	Gate to Drain "Miller" Charge			-	5.8	-	nC

±100

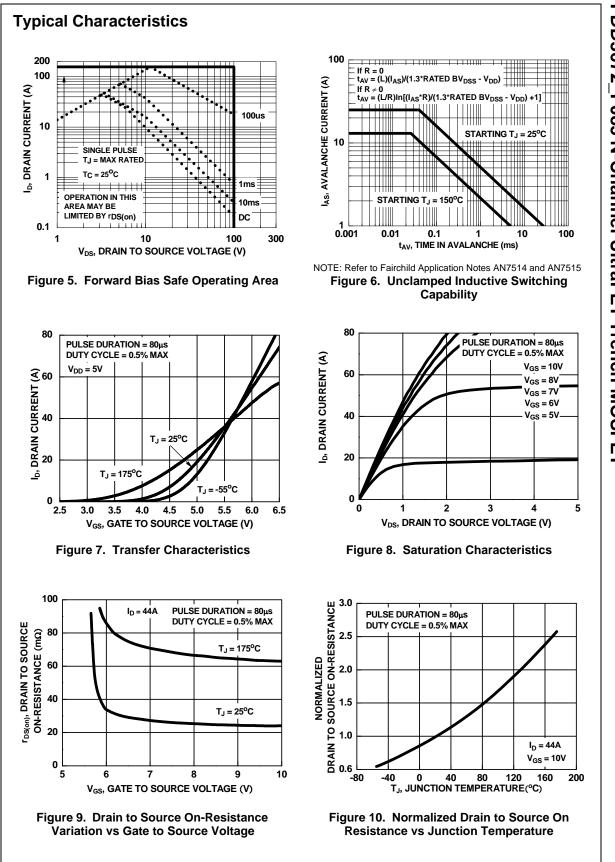
nA

Turn-Off Fall Time - 44 - t _{off} Turn-Off Time - 70	ns ns ns ns ns ns
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	ns ns ns ns
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	ns ns ns
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$\begin{array}{c c} c_{\text{off}} & \hline \text{Turn-Off Fall Time} & & - & 44 & - \\ \hline t_{\text{off}} & \hline \text{Turn-Off Time} & & - & - & 70 \end{array}$	ns
Turn-Off Fall Time - 44 - t _{off} Turn-Off Time - 70	
	ns
Drain-Source Diode Characteristics	
t _{rr} Reverse Recovery Time In = 44A dlop/dt = 100A/us - 44 57	ns
V_{SD} Source to Drain Diode Voltage $\frac{I_{SD} = 44A}{I_{SD} = 21A}$ - 0.9 1.25	V
$\frac{l_{rrr}}{Q_{rrr}} = \frac{Reverse Recovery finite}{Reverse Recovery Charge} = \frac{l_F}{44} = \frac{1}{57}$	nC
	ne

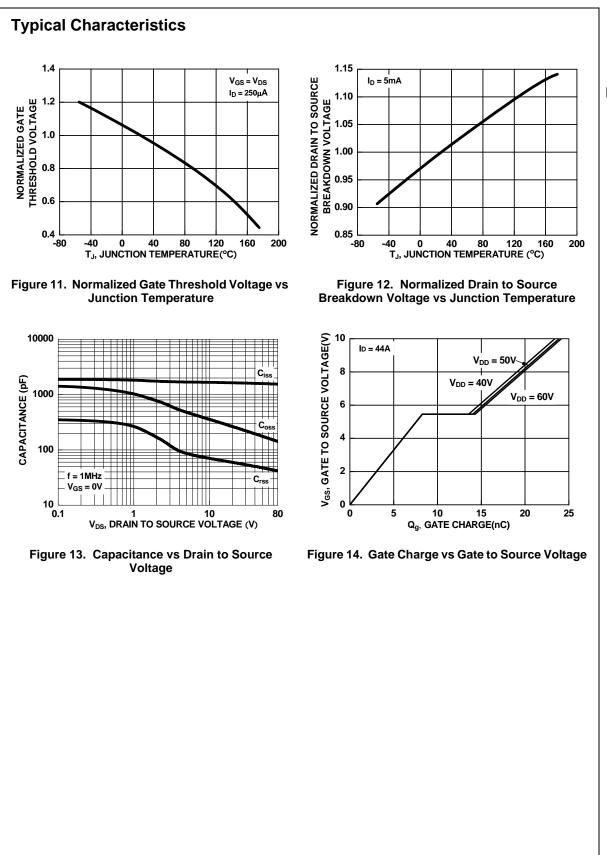
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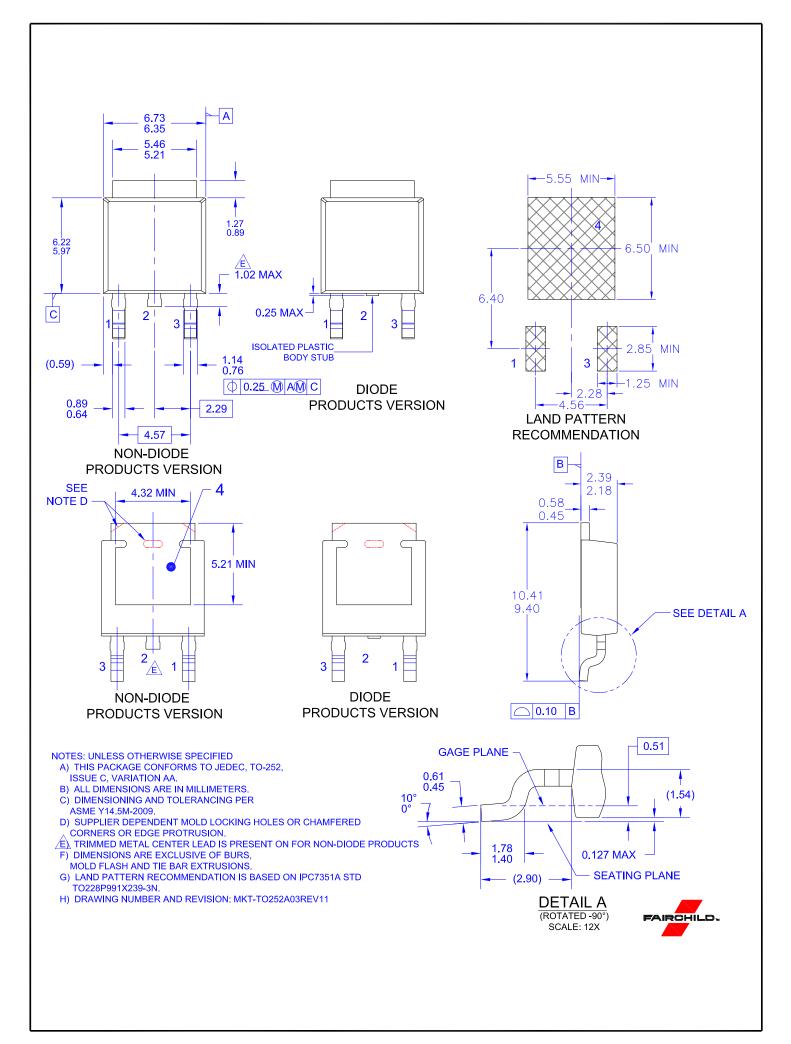
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