

Symbol	Parameter		Ratings	Units
V _{DSS}	Drain to Source Voltage		-40	V
V _{GS}	Gate to Source Voltage	±20	V	
1	Drain Current Continuous (T _C < 130°C, V _{GS} = 10V)		-14	Α
I _D	Pulsed		See Figure 4	A
E _{AS}	Single Pulse Avalanche Energe	(Note 1)	84	mJ
D	Power Dissipation		50	W
P _D	Dreate above 25°C		0.34	W/ºC
T _J , T _{STG}	Operating and Storage Temperature		-55 to +175	°C

Thermal Characteristics

$R_{ ext{ heta}JC}$	Maximum Thermal Resistance Junction to Case	3	°C/W
R_{\thetaJA}	Maximum Thermal Resistance Junction to Ambient TO-252, 1in ² copper pad area	40	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDD4243	FDD4243_F085	TO252	13"	12mm	2500 units
Note:					

1. A suffix as "...F085P" has been temporarily introduced in order to manage a double source strategy as Fairchild has officially announced in Aug 2014.

Electrical Characteristics T_J = 25°C unless otherwise noted

Symb	I Parameter	Test Conditions	Min	Тур	Max	Units
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Off Characteristics

BV _{DSS}	Drain to Source Breakdown Voltage	$I_{D} = -250 \mu A, V_{GS} = 0V$	-40	-	-	V
$\Delta BV_{DSS} \Delta T_J$	Breakdown Voltage Temperature Coefficient ID = -250μA, referenced to 25°C		-	-32	-	mV/°C
	Zero Gate Voltage Drain Current	V _{DS} = -32V	-	-	-1	μA
DSS		$T_J = 125^{\circ}C$	-	-	-100	μΑ
I _{GSS}	Gate to Source Leakage Current V _{GS} = ±20V		-	-	±100	nA

On Characteristics

V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = -250 \mu A$	-1.4	-1.6	-3.0	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	ID = -250μ A, referenced to 25° C	-	4.7	-	mV/°C
		I _D = -6.7A, V _{GS} = -10V	-	36	44	
r _{DS(on)} Drain to Source On Resistance	Drain to Source On Resistance	I _D = -5.5A, V _{GS} = -4.5V	-	48	64	mΩ
	$I_D = -6.7A, V_{GS} = -10V,$ $T_J = 150^{\circ}C$	-	57	70	1115.2	
9 _{FS}	Forward Transconductance	I _D = -6.7A, V _{DS} = -5V,	-	23	-	S

Dynamic Characteristics

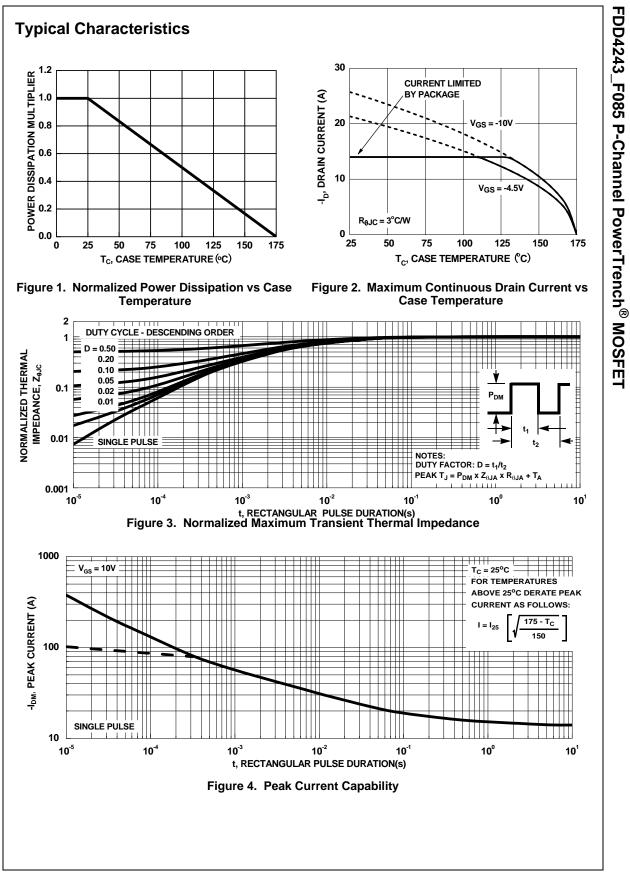
C _{iss}	Input Capacitance		-	1165	1550	pF
C _{oss}	Output Capacitance	V _{DS} = -20V, V _{GS} = 0V, f = 1MHz		165	220	pF
C _{rss}	Reverse Transfer Capacitance	T = 110112	-	90	135	pF
R _G	Gate Resistance	f = 1MHz		4	-	Ω
Q _{g(TOT)}	Total Gate Charge		-	21	29	nC
Q _{gs}	Gate to Source Gate Charge $V_{DD} = -20V, V_{GS} = -10V$		-	3.4	-	nC
Q _{gd}	Gate to Drain "Miller" Charge	$I_{\rm D} = -6.7 {\rm A}$	-	4	-	nC

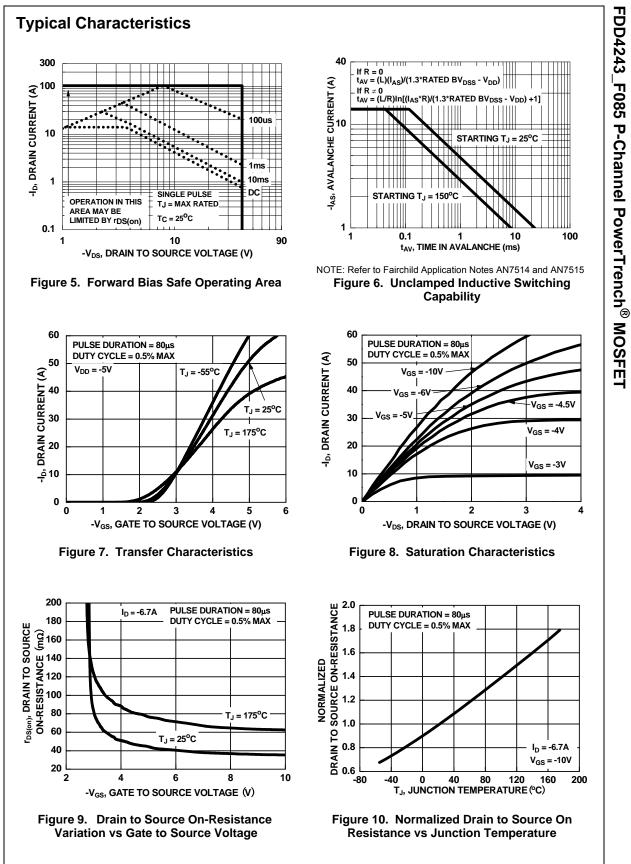
Symbol	nbol Parameter Test Conditions Min Typ Max U					
Switch	ing Characteristics					
t _{d(on)}	Turn-On Delay Time		-	6	12	ns
t _r	Rise Time	V_{DD} = -20V, I _D = -6.7A V _{GS} = -10V, R _{GEN} = 6Ω	-	15	26	ns
t _{d(off)}	Turn-Off Delay Time		-	22	35	ns
t _f	Fall Time		-	7	14	ns
Drain-So	Source Diode Characteristics	I _{SD} = -6.7A, V _{GS} =0V	-	-0.86	-1.2	V
t _{rr}	Reverse Recovery Time	I = 6.70 dI / dt = 1000 / up	-	29	43	ns
	Reverse Recovery Charge	—— I _{SD} = -6.7A, dI _{SD} /dt = 100A/μs		30	44	nC

2. Starting T_J = 25°C, L = 3mH, I_{AS} = 7.5A, V_{GS} = 10V, V_{DD} = 40V during the inductor charging time and 0V during the time in avalanche.

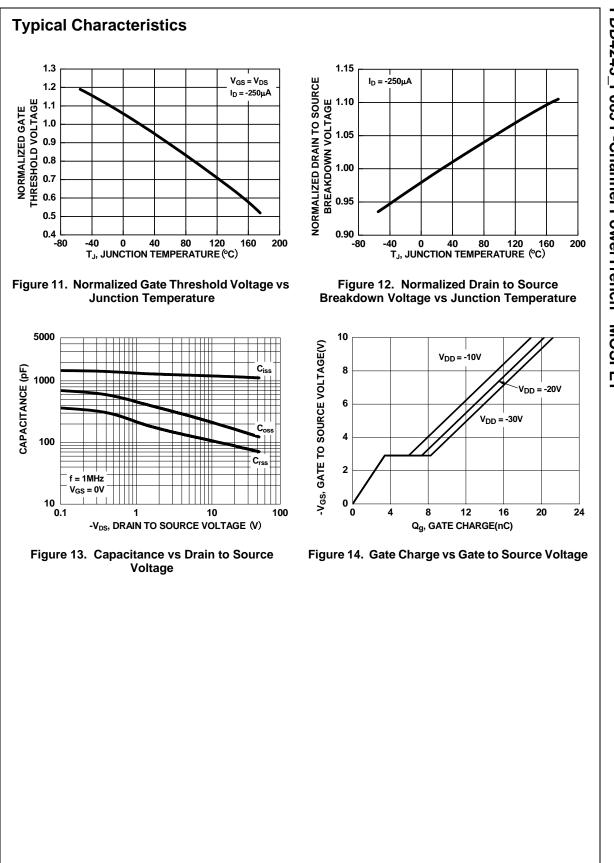
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