

MOSFET Maximum Ratings T_C = 25 °C unless otherwise noted

Symbol	Parameter			Ratings	Units	
V _{DS}	Drain to Source Voltage			150	V	
V _{GS}	Gate to Source Voltage			±20	V	
	Drain Current -Continuous	T _C = 25 °C		27		
I _D	-Continuous	T _A = 25 °C	(Note 1a)	5	Α	
	-Pulsed (Note 4)			30		
E _{AS}	Single Pulse Avalanche Energy		(Note 3)	72	mJ	
P _D	Power Dissipation $T_{\rm C} = 25 ^{\circ}{\rm C}$			89	w	
	Power Dissipation	T _A = 25 °C	(Note 1a)	3.1	VV	
T _J , T _{STG}	Operating and Storage Junction Temperature Range			-55 to +150	°C	

Thermal Characteristics

$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case		1.4	°C/W
R_{\thetaJA}	Thermal Resistance, Junction to Ambient	(Note 1a)	40	C/VV

Package Marking and Ordering Information

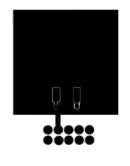
Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDD86252	FDD86252	D-PAK(TO-252)	13 "	16 mm	2500 units

1

Units	[FDD86252
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Symbol	Parameter	Test Conditions	Min	Тур	Max	Units	
Off Chara	cteristics						
BV _{DSS}	Drain to Source Breakdown Voltage	I _D = 250 μA, V _{GS} = 0 V	150			V	
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I_D = 250 μ A, referenced to 25 °C		104		mV/°C	
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 120 V, V _{GS} = 0 V			1	μA	
I _{GSS}	Gate to Source Leakage Current	V _{GS} = ±20 V, V _{DS} = 0 V			±100	nA	
On Chara	cteristics						
V _{GS(th)}	Gate to Source Threshold Voltage	V _{GS} = V _{DS} , I _D = 250 μA	2.0	3.1	4.0	V	
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	$I_D = 250 \ \mu$ A, referenced to 25 °C		-10		mV/°C	
0		V _{GS} = 10 V, I _D = 5 A		41	52		
r _{DS(on)}	Static Drain to Source On Resistance	$V_{GS} = 6 V, I_D = 4 A$		49	72	mΩ	
00(01)		V _{GS} = 10 V, I _D = 5 A,T _J = 125 °C		81	103		
9 _{FS}	Forward Transconductance	$V_{DS} = 10 \text{ V}, I_D = 5 \text{ A}$		15		S	
C _{iss} C _{oss} C _{rss}	Input Capacitance Output Capacitance Reverse Transfer Capacitance	— V _{DS} = 75 V, V _{GS} = 0 V, f = 1 MHz		741 78 4.2	985 130 10	pF pF pF	
R _g	Gate Resistance			0.4		Ω	
Switching	g Characteristics						
t _{d(on)}	Turn-On Delay Time			8.3	17	ns	
t _r	Rise Time	V _{DD} = 75 V, I _D = 5 A,		1.8	10	ns	
t _{d(off)}	Turn-Off Delay Time	V _{GS} = 10 V, R _{GEN} = 6 Ω		14	25	ns	
t _f	Fall Time			3	10	ns	
Qg	Total Gate Charge	V _{GS} = 0 V to 10 V		11.3	16	nC	
Qg	Total Gate Charge	$V_{GS} = 0 V \text{ to } 5 V V_{DD} = 75 V,$		6.3	9	nC	
Q _{gs}	Gate to Source Charge	I _D = 5 A		3.4		nC	
Q _{gd}	Gate to Drain "Miller" Charge			2.6		nC	
Drain-Sou	urce Diode Characteristics						
	Source-Drain Diode Forward Voltage	$V_{GS} = 0 V, I_S = 5 A$ (Note 2)		0.80	1.3	V	
V _{SD}		V _{GS} = 0 V, I _S = 2.6 A (Note 2)		0.77	1.2		
t _{rr}	Reverse Recovery Time	I _F = 5 A, di/dt = 100 A/μs		60	97	ns	
Q _{rr}	Reverse Recovery Charge	$\mu_{\rm F} = 5 \text{A}, \text{u}/\text{u} = 100 \text{A}/\mu \text{S}$		72	115	nC	

Notes: 1: R_{0JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{0JC} is guaranteed by design while R_{0JA} is determined by the user's board design.

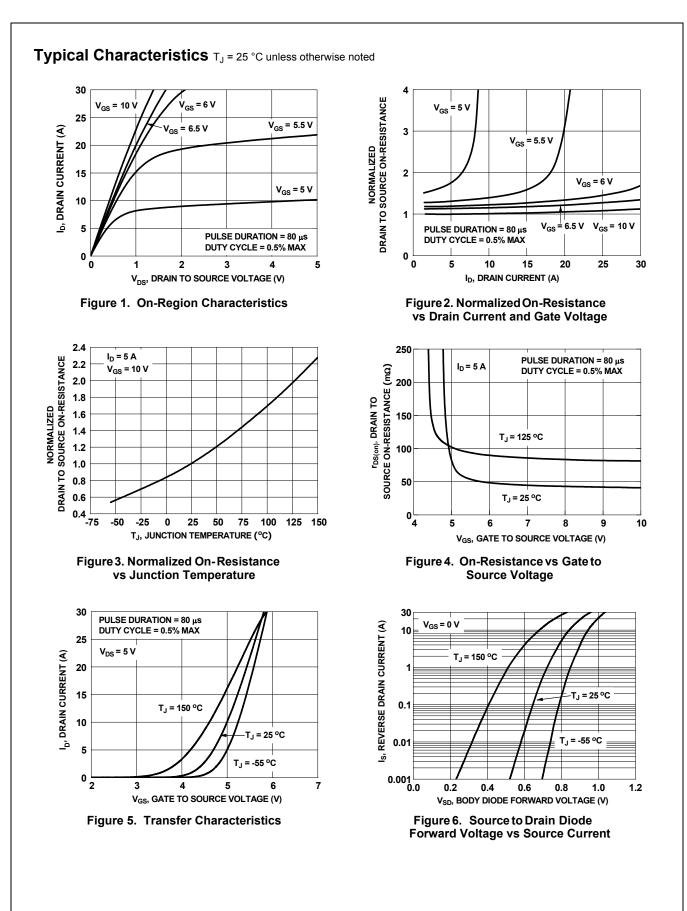


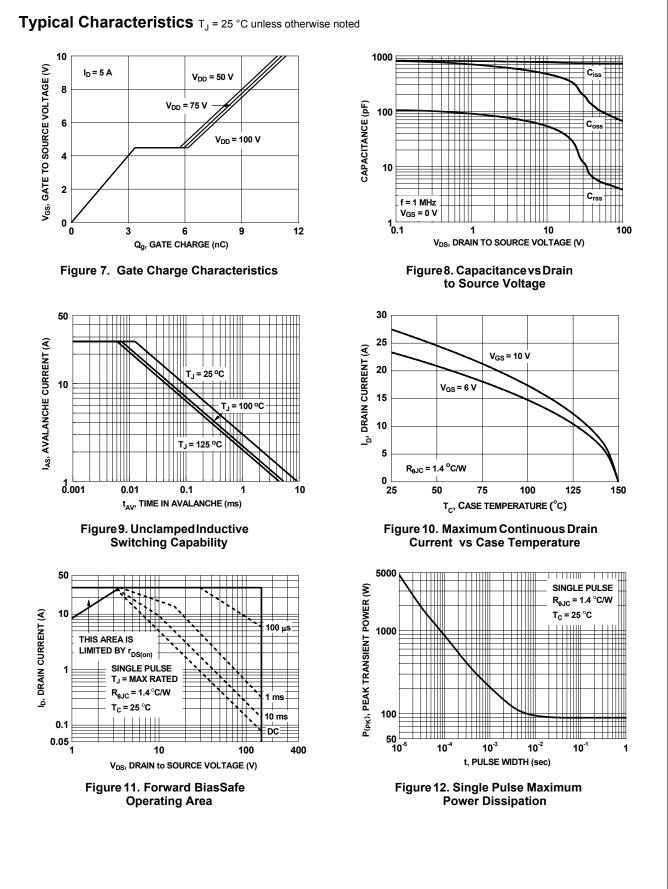
a) 40 °C/W when mounted on a 1 in² pad of 2 oz copper



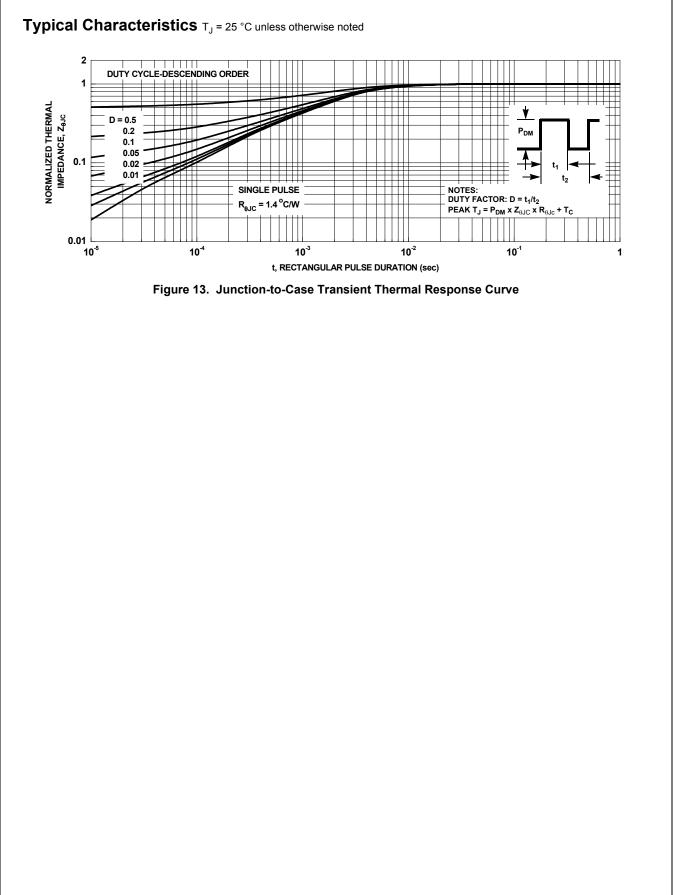
b) 96 °C/W when mounted on a minimum pad

Pulse Test: Pulse Width < 300 µs, Duty cycle < 2.0%.
 Starting T_J = 25 °C, L = 1 mH, I_{AS} = 12 A, V_{DD} = 135 V, V_{GS} = 10 V.
 Pulsed Drain current is tested at 300 µs with 2% duty cycle. For repetitive pulses, the pulse width is limited by the maximum junction temperature.





FDD86252 N-Channel Shielded Gate PowerTrench[®] MOSFET



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