

MOSFET Maximum Ratings T₁ = 25°C unless otherwise noted.

Symbol	Parameter		Ratings	Units	
V _{DSS}	Drain to Source Voltage		60	V	
V _{GS}	Gate to Source Voltage		±20	V	
I _D	Drain Current - Continuous (V_{GS} =10) (Note 1) T_C = 25°C		35	•	
	Pulsed Drain Current	T _C = 25°C	See Figure4	Α	
E _{AS}	Single Pulse Avalanche Energy	(Note 2)	82	mJ	
P _D	Power Dissipation		75	W	
	Derate above 25°C		0.5	W/ ^o C	
T _J , T _{STG}	Operating and Storage Temperature		-55 to + 175	°C	
$R_{\theta JC}$	Thermal Resistance, Junction to Case		2.0	°C/W	
$R_{\theta JA}$	Maximum Thermal Resistance, Junction to Ambient	(Note 3)	50	°C/W	

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDMS5361L	FDMS5361L_F085	Power 56	13"	12mm	3000 units

Notes:

1: Current is limited by junction temperature.

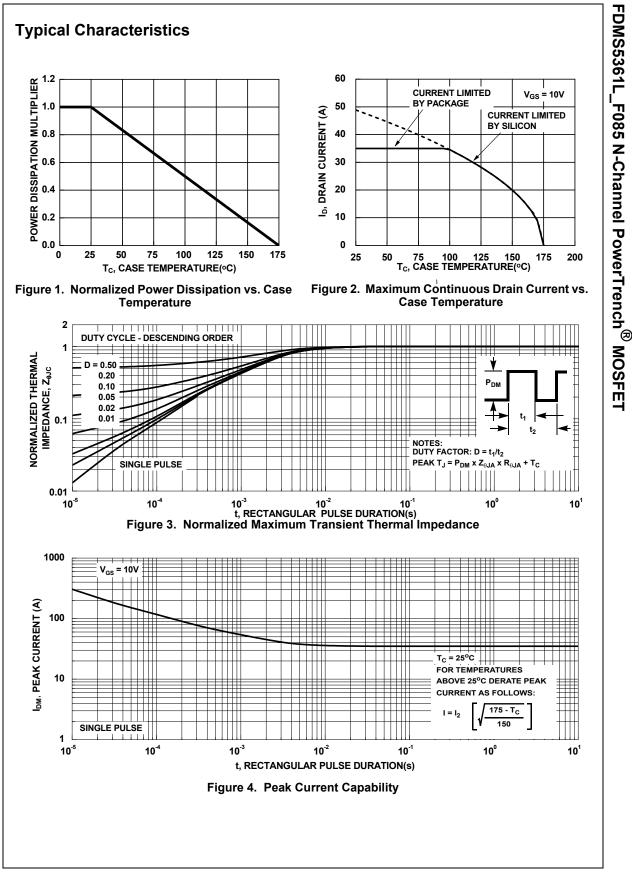
2: Starting T_J = 25°C, L = 0.21mH, I_{AS} = 28A, V_{DD} = 60V during inductor charging and V_{DD} = 0V during time in avalanche. 3: $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder ⁴⁰⁰ mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design while $R_{\theta JA}$ is determined by the user's board design. The maximum rating presented here is based on mounting on a 1 in² pad of 2oz copper.

Symbol	Parameter	Test	Conditions	Min.	Тур.	Max.	Units
Off Cha	aracteristics						
B _{VDSS}	Drain to Source Breakdown Voltage	I _D = 250μA, V	/ _{GS} = 0V	60	-	-	V
		V _{DS} =60V,	$T_{.1} = 25^{\circ}C$	-	-	1	μA
DSS	Drain to Source Leakage Current	$V_{GS} = 0V$	$T_{.1} = 175^{\circ}C(Note 4)$	-	-	1	mA
GSS	Gate to Source Leakage Current	$V_{GS} = \pm 20V$	3		-	±100	nA
On Cha	aracteristics						
V _{GS(th)}	Gate to Source Threshold Voltage	Vcc = Vcc lr	V _{GS} = V _{DS} , I _D = 250μA		1.84	3.0	V
- GS(III)		I _D = 16.5A,	$T_{.1} = 25^{\circ}C$	1.0 -	11.7	15	mΩ
_		V _{GS} = 10V	$T_{\rm J} = 175^{\rm o} {\rm C}({\rm Note} 4)$	-	24.5	30	mΩ
R _{DS(on)}	Drain to Source On Resistance	I _D = 16.5A,	$T_{,l} = 25^{\circ}C$	-	14.6	18	mΩ
		V _{GS} = 4.5V	$T_{\rm J} = 175^{\rm o} {\rm C}({\rm Note} \ 4)$	-	29.5	34	mΩ
Coss	Output Capacitance	V _{DS} = 25V, V f = 1MHz	──V _{DS} = 25V, V _{GS} = 0V, f = 1MHz		176	-	pF
C _{iss}	Input Capacitance		V _{DS} = 25V, V _{GS} = 0V,		1980	-	pF
C _{rss}	Reverse Transfer Capacitance	T = TMHZ	_	-	93	-	pF
۲ _g	Gate Resistance	f = 1MHz	f = 1MHz		1.6	-	Ω
Q _{g(ToT)}	Total Gate Charge at 10V	V _{GS} = 0 to 10)V V _{DD} = 30V	-	37	44	nC
$Q_{g(th)}$	Threshold Gate Charge	V _{GS} = 0 to 2\		-	3.6	5	nC
Q _{gs}	Gate to Source Gate Charge		-		5.5	-	nC
Q _{gd}	Gate to Drain "Miller" Charge				7.3	-	nC
witch	ing Characteristics			-	-	30	ns
t _{on}	Turn-On Delay		-	-	16	-	ns
t _{d(on)}	Rise Time	1/2 = -301/1	- 16 5 4	-	10	-	ns
	Turn-Off Delay	$v_{DD} = 30V, I$ $V_{CS} = 10V F$	V_{DD} = 30V, I _D = 16.5A, V_{GS} = 10V, R _{GEN} = 6 Ω		52	-	ns
d(off)	Fall Time	- 65 - 60,1			8	-	ns
f f	Turn-Off Time				-	67	ns
off				-	-	07	115
)rain_S	Source Diode Characteristics						
	Source to Drain Diode Voltage	I _{SD} = 16.5A,	V _{GS} = 0V	-	-	1.25	V
V _{SD} t _{rr}	Source to Drain Diode Voltage Reverse Recovery Time		$V_{GS} = 0V$ dI _{SD} /dt = 100A/µs,	-	- 28	1.25 32	V ns

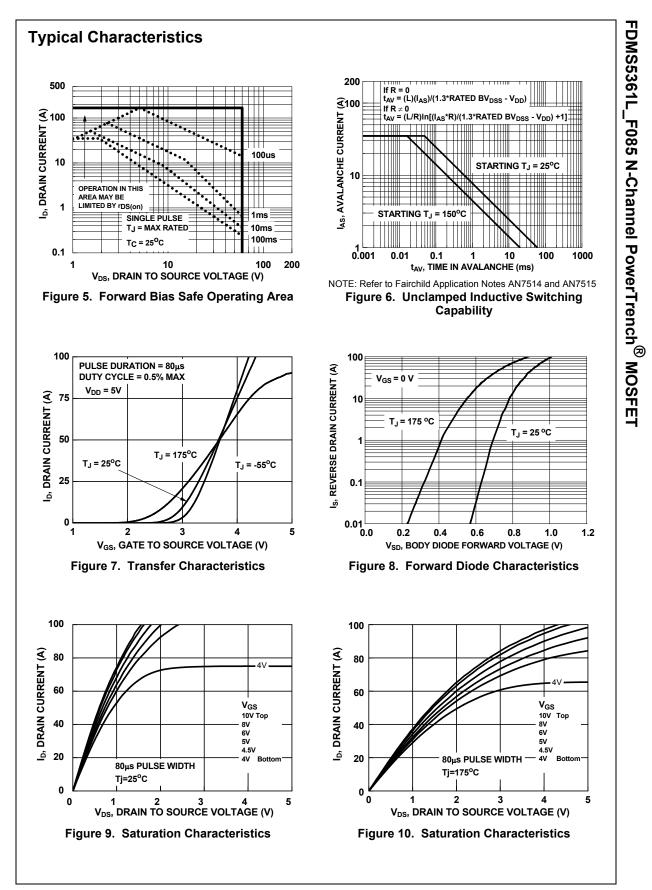
Note:

4: The maximum value is specified by design at T_J = 175°C. Product is not tested to this condition in production.

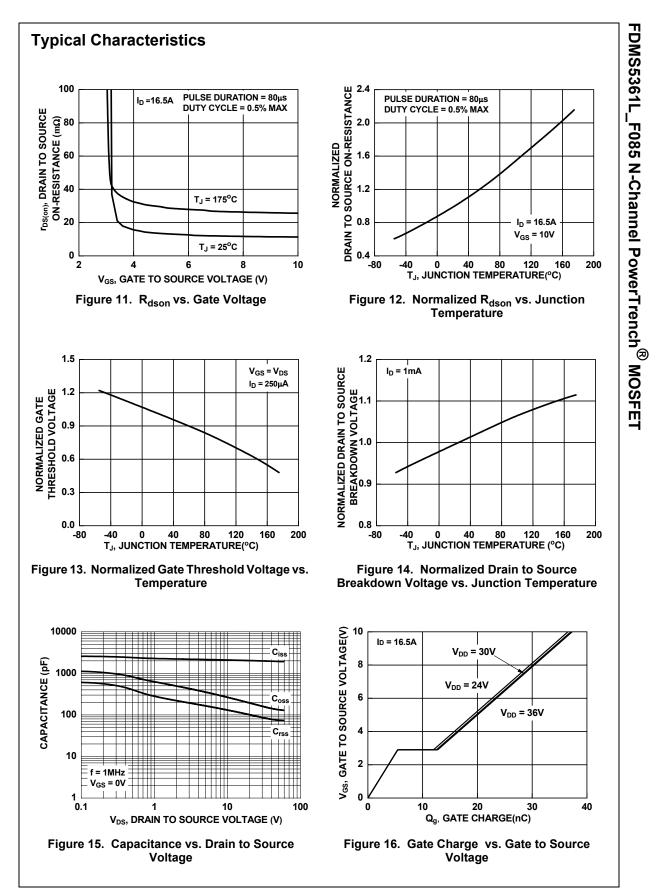
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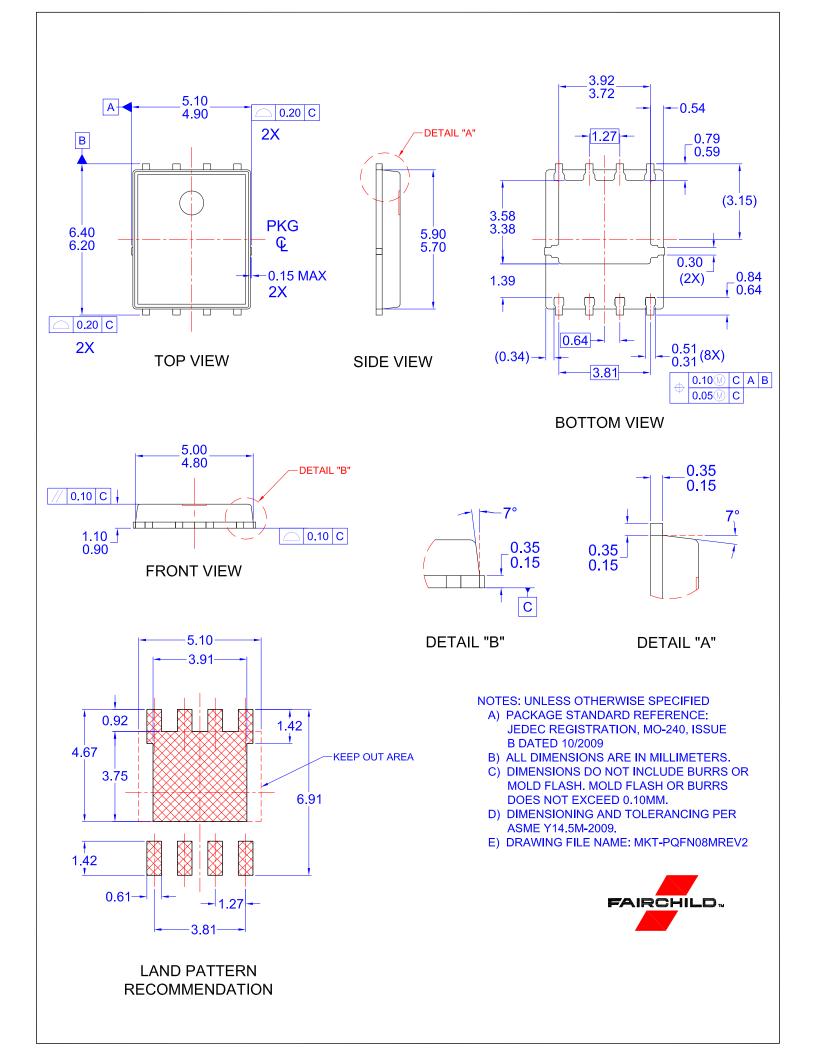
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