



## FDP8030L/FDB8030L

# N-Channel Logic Level PowerTrench® MOSFET

### **General Description**

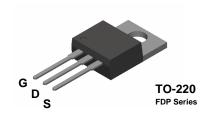
This N-Channel Logic level MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers.

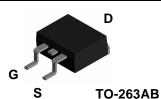
These MOSFETS feature faster switching and lower gate charge than other MOSFETS with comparable  $R_{\text{DS(on)}}$  specifications.

The result is a MOSFET that is easy and safer to drive (even at very high frequencies), and DC/DC power supply designs with higher overall efficiency.

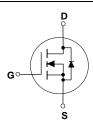
### **Features**

- 80 A, 30 V.  $R_{DS(ON)} = 0.0035 \ \Omega \ @ \ V_{GS} = 10 \ V$   $R_{DS(ON)} = 0.0045 \ \Omega \ @ \ V_{GS} = 4.5 \ V$
- Critical DC electrical parameters specified at elevated temperature
- Rugged internal source-drain diode can eliminate the need for an external Zener diode transient suppressor
- High performance trench technology for extremely low Rescont
- 175°C maximum junction temperature rating





**FDB Series** 



Absolute Maximum Ratings T<sub>A</sub>=25°C unless otherwise noted

Symbol	Parameter	Ratings	Units
V <sub>DSS</sub>	Drain-Source Voltage	30	V
V <sub>GSS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Drain Current - Continuous (Note 1)	80	А
	- Pulsed (Note 1)	300	
P <sub>D</sub>	Total Power Dissipation @ T <sub>C</sub> = 25°C	187	W
	Derate above 25°C	1.25	W°C
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-65 to +175	°C
TL	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	275	°C

### **Thermal Characteristics**

$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	0.8	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5	°C/W

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Drain-Sc	ource Avalanche Ratings (Note	1)				•
W <sub>DSS</sub>	Single Pulse Drain-Source Avalanche Energy	$V_{DD} = 20 \text{ V}, \qquad I_D = 80 \text{ A}$			1500	mJ
I <sub>AR</sub>	Maximum Drain-Source Avalanche Current				80	Α
Off Char	acteristics	_				
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	30			V
ΔBV <sub>DSS</sub> ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	$I_D = 250 \mu\text{A}$ , Referenced to 25°C		23		mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = 24 \text{ V}, \qquad V_{GS} = 0 \text{ V}$			10	μΑ
I <sub>GSSF</sub>	Gate-Body Leakage, Forward	$V_{GS} = 20 \text{ V}, \qquad V_{DS} = 0 \text{ V}$			100	nA
I <sub>GSSR</sub>	Gate-Body Leakage, Reverse	$V_{GS} = -20 \text{ V}$ $V_{DS} = 0 \text{ V}$			-100	nA
On Char	acteristics (Note 2)					
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	1	1.5	2	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	$I_D$ = 250 $\mu$ A, Referenced to 25°C		<b>-</b> 5		mV/°C
R <sub>DS(on)</sub>	Static Drain–Source On–Resistance	$V_{GS} = 10 \text{ V}, \qquad I_{D} = 80 \text{ A} $ $T_{J} = 125 ^{\circ}\text{C}$		3.1 4.0	3.5 5.6	mΩ
		$V_{GS} = 4.5 \text{ V}, \qquad I_{D} = 70 \text{ A}$		3.6	4.5	
$I_{D(on)}$	On–State Drain Current	$V_{GS} = 10 \text{ V}, \qquad V_{DS} = 10 \text{ V}$	60			Α
<b>g</b> FS	Forward Transconductance	$V_{DS} = 10 \text{ V}, \qquad I_{D} = 80 \text{ A}$		170		S
Dynamic	: Characteristics					
Ciss	Input Capacitance	$V_{DS} = 15 \text{ V}, \qquad V_{GS} = 0 \text{ V},$		10500		pF
Coss	Output Capacitance	f = 1.0 MHz		2700		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			1650		pF
Switchin	g Characteristics (Note 2)					
t <sub>D(on)</sub>	Turn-On Delay Time	$V_{DD} = 15 \text{ V}, \qquad I_D = 50 \text{ A},$		20	35	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{GS} = 4.5 \text{ V}, \qquad R_{GEN} = 10 \Omega$		185	225	ns
t <sub>D (off)</sub>	Turn-Off Delay Time	$R_{GS} = 10 \Omega$		160	200	ns
t <sub>f</sub>	Turn-Off Fall Time	1		200	240	ns
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> = 15 V,		120	170	nC
$Q_{gs}$	Gate-Source Charge	$I_D = 80 \text{ A}, V_{GS} = 5 \text{ V}$		27		nC
$Q_{gd}$	Gate-Drain Charge			48		nC
Drain-Se	ource Diode Characteristics	and Maximum Ratings				
Is	Maximum Continuous Drain-Source				80	Α
I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode	e Forward Current (Note 1)			300	Α
$V_{SD}$	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V},  I_S = 80 \text{ A} \text{ (Note 1)}$		1	1.3	V

### Notes:

<sup>1.</sup> Pulse Test: Pulse Width <  $300\mu s,$  Duty Cycle < 2.0%

### **Typical Characteristics**

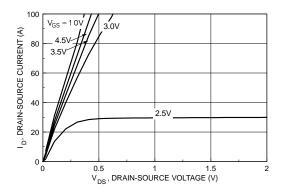
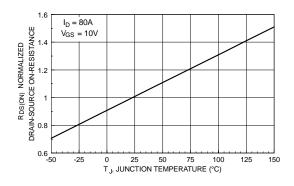


Figure 1. On-Region Characteristics.

Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.



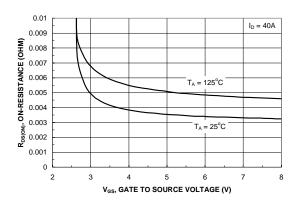
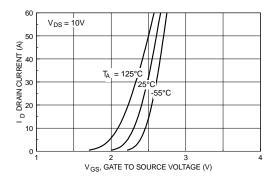


Figure 3. On-Resistance Variation with Temperature.

Figure 4. On-Resistance Variation with Gate-to-Source Voltage.



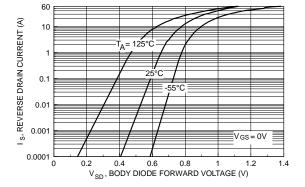
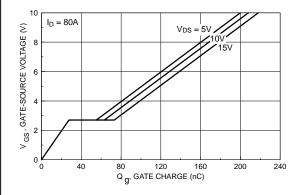


Figure 5. Transfer Characteristics.

Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

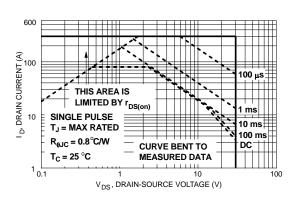
### **Typical Characteristics**



18000 10000 10000 10000 10000 V<sub>GS</sub> = 0V 10000 V<sub>DS</sub>, DRAIN TO SOURCE VOLTAGE (V)

Figure 7. Gate Charge Characteristics.





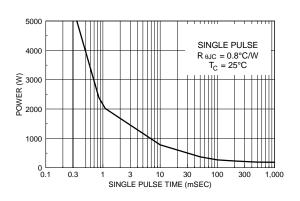


Figure 9. Maximum Safe Operating Area.

Figure 10. Single Pulse Maximum Power Dissipation.

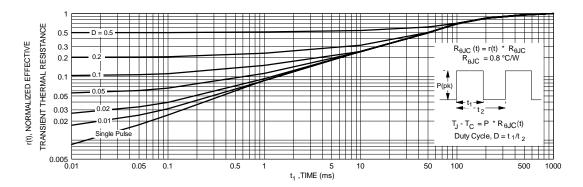


Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1c. Transient thermal response will change depending on the circuit board design.





#### TRADEMARKS

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

2Cool™ FPS™ AccuPower™ F-PFS™ FRFET® AX-CAP® BitSiC™ Global Power Resource<sup>SM</sup> Green Bridge™ Build it Now™  $\mathsf{CorePLUS^{\mathsf{TM}}}$ Green FPS™ Green FPS™ e-Series™ Gmax™ CorePOWER™  $CROSSVOLT^{rM}$  $\mathsf{GTO^{\mathsf{TM}}}$ IntelliMAX $^{\text{TM}}$ Current Transfer Logic™

ISOPLANAR™ DEUXPEED® Dual Cool™ EcoSPARK<sup>®</sup> and Better™ EfficentMax™ MegaBuck™ **ESBC™** MICROCOUPLER™

Fairchild<sup>®</sup> Fairchild Semiconductor® FACT Quiet Series™ FACT FAST® FastvCore™ FETBench™

 $(1)_{\mathbb{R}}$ PowerTrench® PowerXS™ Programmable Active Droop™ **OFET**  $OS^{TM}$ 

Quiet Series™ RapidConfigure™

Marking Small Speakers Sound Louder Saving our world, 1mW/W/kW at a time™ SignalWise™ SmartMax™ SMART START™

Solutions for Your Success™ STEALTH™ SuperFET® SuperSOT™-3 SuperSOT™-6 SuperSOT™-8 SupreMOS® SyncFET™

SYSTEM®\*

GENERAL

TipyBoost™ TinyBoost TinyBuck™ TinyCalc™ TinyLogic<sup>®</sup> TINYOPTO™ TinyPower™ TinyPWM™ TinyWire™ TranSiC® TriFault Detect™ TRUECURRENT®\* uSerDes™

Sync-Lock™

UHC<sup>®</sup> Ultra FRFET™ UniFET™ VCX™ VisualMax™ VoltagePlus™ XS™

\*Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

MicroFET™

MicroPak™ MicroPak2™

MillerDrive™

MotionMax™

 $mWSaver^{\scriptscriptstyle\mathsf{TM}}$ 

OPTOLOGIC®

OPTOPLANAR®

OptoHiT™

#### DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY
FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used here in:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

#### ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.Fairchildsemi.com, under Sales Support.

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufactures of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed application, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handing and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address and warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

#### PRODUCT STATUS DEFINITIONS **Definition of Terms**

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

Rev. 164

# **Mouser Electronics**

**Authorized Distributor** 

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

FDP8030L