

# FDPF44N25T N-Channel UniFET<sup>TM</sup> MOSFET 250 V, 44 A, 69 mΩ

### Features

- $R_{DS(on)}$  = 69 m $\Omega$  (Max.) @ V<sub>GS</sub> = 10 V, I<sub>D</sub> = 22 A
- Low Gate Charge (Typ. 47 nC)
- Low C<sub>rss</sub> (Typ. 60 pF)

### **Applications**

- PDP TV
- Lighting
- Uninterruptible Power Supply
- AC-DC Power Supply



FDPF44N25T — N-Channel UniFET<sup>TM</sup> MOSFET

# Description

UniFET<sup>TM</sup> MOSFET is Fairchild Semiconductor's high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.



### Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted.

Symbol		Parameter	FDPF44N25T FDPF44N25TRDTU	Unit
V <sub>DSS</sub>	Drain-Source Voltage		250	V
ID	Drain Current	- Continuous (T <sub>C</sub> = 25°C) - Continuous (T <sub>C</sub> = 100°C)	44* 26.4*	A A
I <sub>DM</sub>	Drain Current	- Pulsed (Note 1)	176*	Α
V <sub>GSS</sub>	Gate-Source voltage		± 30	V
E <sub>AS</sub>	Single Pulsed Avalance	che Energy (Note 2)	2055	mJ
I <sub>AR</sub>	Avalanche Current	(Note 1)	44	А
E <sub>AR</sub>	Repetitive Avalanche	Energy (Note 1)	30.7	mJ
dv/dt	Peak Diode Recovery	dv/d (Note 3)	4.5	V/ns
P <sub>D</sub>	Power Dissipation	(T <sub>C</sub> = 25°C) - Derate Above 25°C	38 0.3	W W/°C
T <sub>J,</sub> T <sub>STG</sub>	Operating and Storag	e Temperature Range	-55 to +150	°C
TL	Maximum Lead Tempe	erature for Soldering, 1/8" from Case for 5 Seconds	300	°C

\*Drain current limited by maximum junction temperature.

# **Thermal Characteristics**

Symbol	Parameter	FDPF44N25T FDPF44N25TRDTU	Unit	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	3.3	°C/W	
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	C/W	

N-Channel UniFET <sup>TM</sup> MOS	FDPF44N25T
UniFET <sup>TM</sup> N	Innel
	UniFET <sup>TM</sup> N

Part N	Part Number Top Mark		Package	Package Packing Method Reel Size		e Tape Width		Qu	Quantity	
FDPF44N25TRDTU		FDPF44N25T	TO-220F	Tube	N/A		N/A	50	) units	
FDPF44N	25TRDTU	FDPF44N25T	TO-220F (LG-formed)	Tube	N/A		N/A	50	units	
Electric	al Char	acteristics T <sub>c</sub> = 2	25°C unless oth	erwise noted.						
Symbol		Parameter		Conditions		Min.	Тур.	Max.	Unit	
Off Charac	teristics							•	•	
BV <sub>DSS</sub>	Drain-Sou	rce Breakdown Voltage	V <sub>GS</sub> = 0	V, $I_D = 250 \ \mu A$ , $T_J =$	25°C	250			V	
$\Delta BV_{DSS}$ / $\Delta T_{J}$	Breakdow Coefficien	n Voltage Temperature t	I <sub>D</sub> = 250	μA, Referenced to 2	5°C		0.25		V/°C	
I <sub>DSS</sub>	Zero Gate	Voltage Drain Current		50 V, V <sub>GS</sub> = 0 V 00 V, T <sub>C</sub> = 125°C				1 10	μΑ μΑ	
I <sub>GSSF</sub>	Gate-Body	y Leakage Current, Forw	ard V <sub>GS</sub> = 3	0 V, V <sub>DS</sub> = 0 V				100	nA	
I <sub>GSSR</sub>	Gate-Body	y Leakage Current, Reve	erse V <sub>GS</sub> = -3	30 V, V <sub>DS</sub> = 0 V				-100	nA	
On Charac	teristics									
V <sub>GS(th)</sub>	Gate Thre	shold Voltage	V <sub>DS</sub> = V	<sub>GS</sub> , I <sub>D</sub> = 250 μA		3.0		5.0	V	
R <sub>DS(on)</sub>	Static Drai On-Resist		V <sub>GS</sub> = 1	0 V, I <sub>D</sub> = 22 A			0.058	0.069	Ω	
9 <sub>FS</sub>	Forward T	ransconductance	V <sub>DS</sub> = 4	0 V, I <sub>D</sub> = 22 A			32		S	
Dynamic C	haracteris	tics								
C <sub>iss</sub>	Input Capa	but Capacitance $V_{DS} = 25 \text{ V}, \text{ V}_{GS} = 0 \text{ V},$					2210	2870	pF	
C <sub>oss</sub>	Output Ca	pacitance	f = 1.0 N	f = 1.0 MHz			450	585	pF	
C <sub>rss</sub>	Reverse T	ransfer Capacitance					60	90	pF	
Switching	Characteris	stics								
t <sub>d(on)</sub>	Turn-On D	elay Time		V <sub>DD</sub> = 125 V, I <sub>D</sub> = 44 A,			53	117	ns	
t <sub>r</sub>	Turn-On F	Rise Time	R <sub>G</sub> = 25	$R_{G} = 25 \Omega$			402	814	ns	
t <sub>d(off)</sub>	Turn-Off D	elay Time					85	179	ns	
t <sub>f</sub>	Turn-Off F	all Time		(Note 4)		/	112	234	ns	
Qg	Total Gate	Charge		$V_{DS} = 200 \text{ V}, \text{ I}_{D} = 44 \text{ A},$ $V_{GS} = 10 \text{ V}$ (Note 4)			47	61	nC	
Q <sub>gs</sub>	Gate-Sour	ce Charge	V <sub>GS</sub> = 1				18		nC	
Q <sub>gd</sub>	Gate-Drai	n Charge					24		nC	
Drain-Sou	rce Diode C	haracteristics and Max	kimum Ratings							
I <sub>S</sub>	Maximum	Continuous Drain-Sourc	e Diode Forwar	d Current				44	А	
I <sub>SM</sub>	Maximum	Pulsed Drain-Source Die	ode Forward Cu	irrent				176	А	
$V_{SD}$	Drain-Sou	rce Diode Forward Volta	ge V <sub>GS</sub> = 0	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 44 A				1.4	V	
t <sub>rr</sub>	Reverse F	Recovery Time		V, I <sub>S</sub> = 44 A,			195		ns	
Q <sub>rr</sub>	Reverse F	Recovery Charge	dl <sub>F</sub> /dt =1	100 A/μs			1.8		μC	

#### Notes:

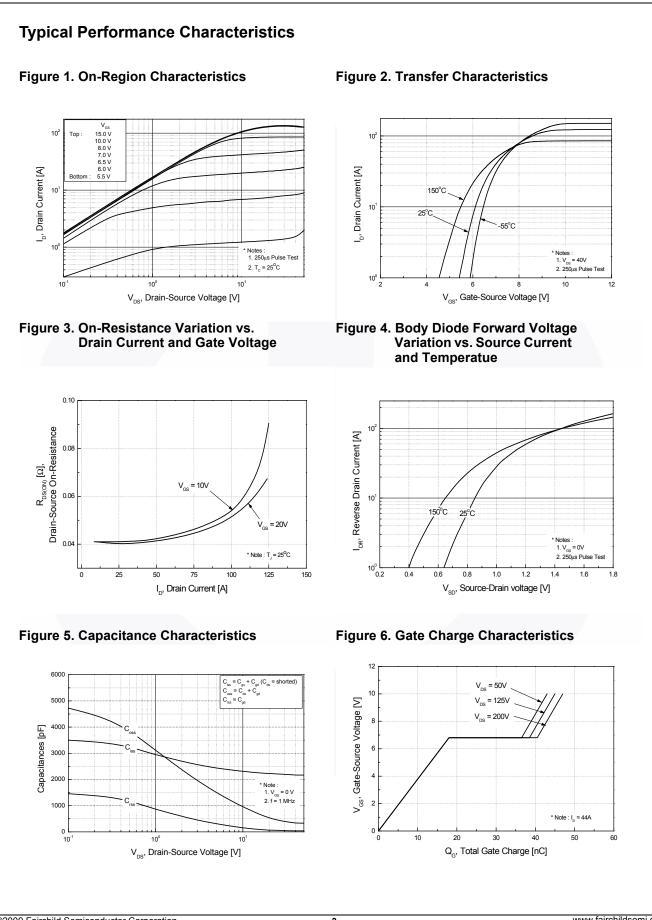
1. Repetitive rating: pulse-width limited by maximum junction temperature.

Package Marking and Ordering Information

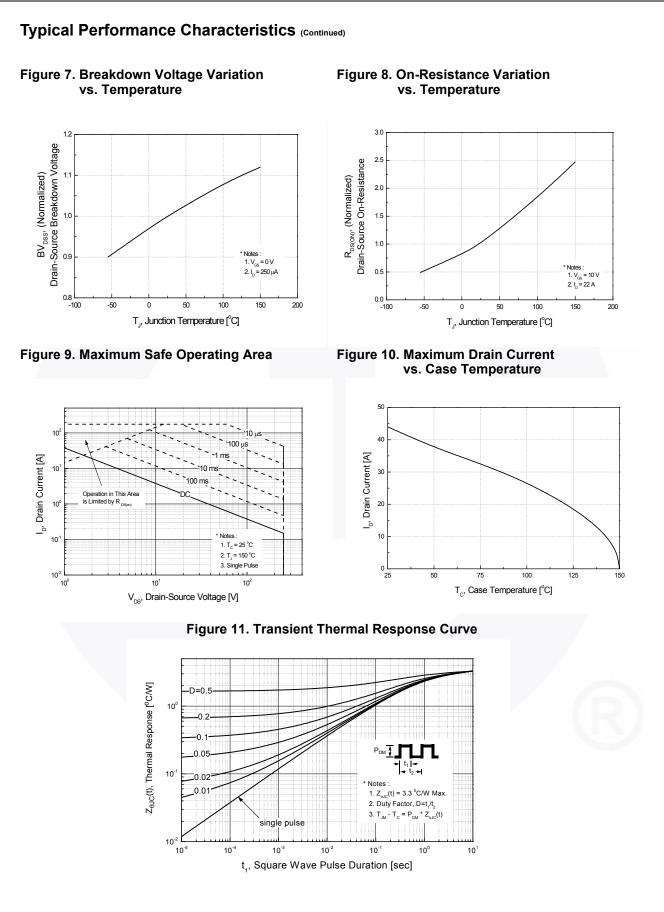
2. L = 1.7 mH, I\_{AS} = 44 A, V\_{DD} = 50 V, R\_G = 25  $\Omega$ , starting T\_J = 25°C.

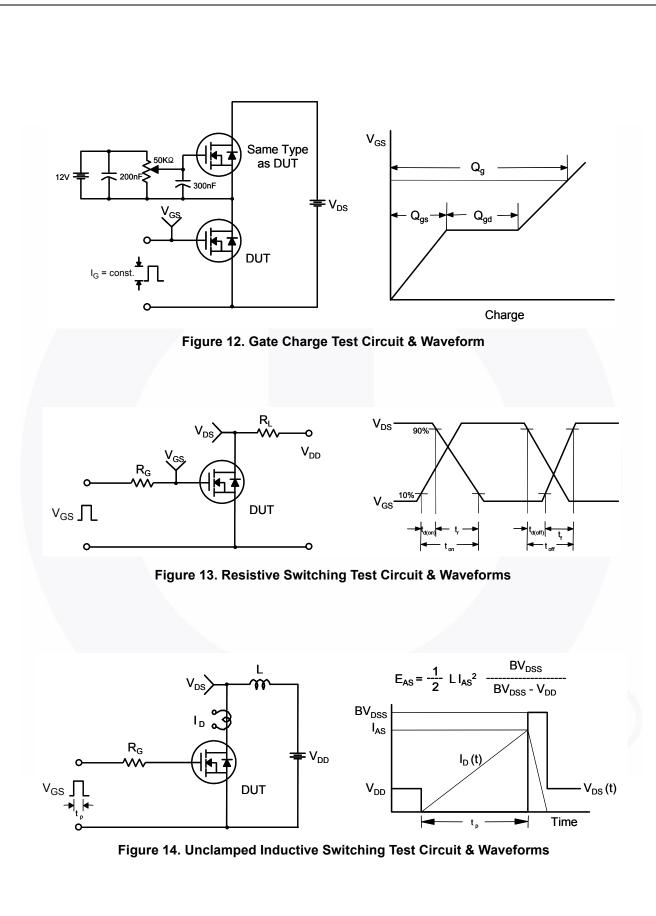
3. I\_{SD}  $\leq$  44 A, di/dt  $\leq$  200 A/µs, V\_{DD}  $\leq$  BV\_{DSS}, starting T\_J = 25°C.

4. Essentially independent of operating temperature typical characteristics.



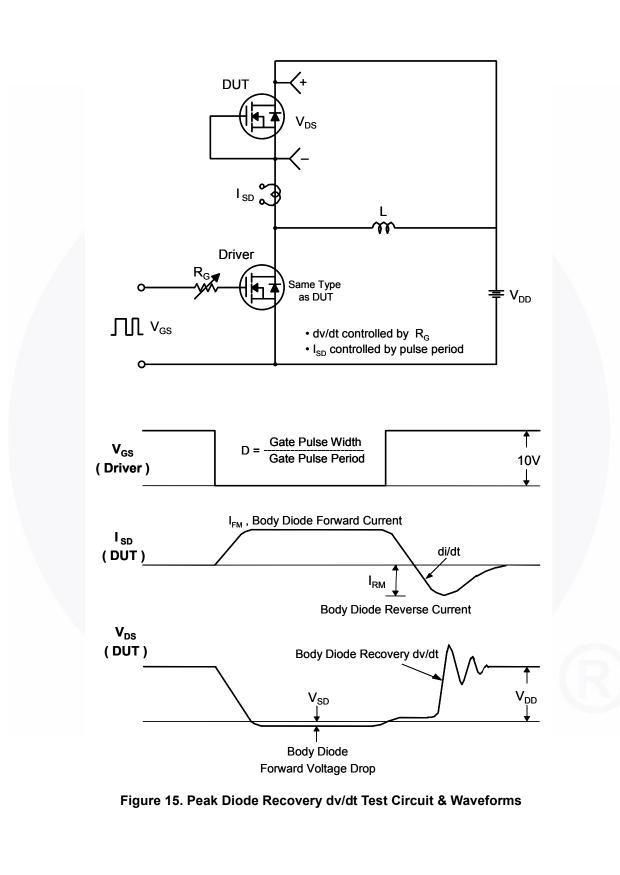
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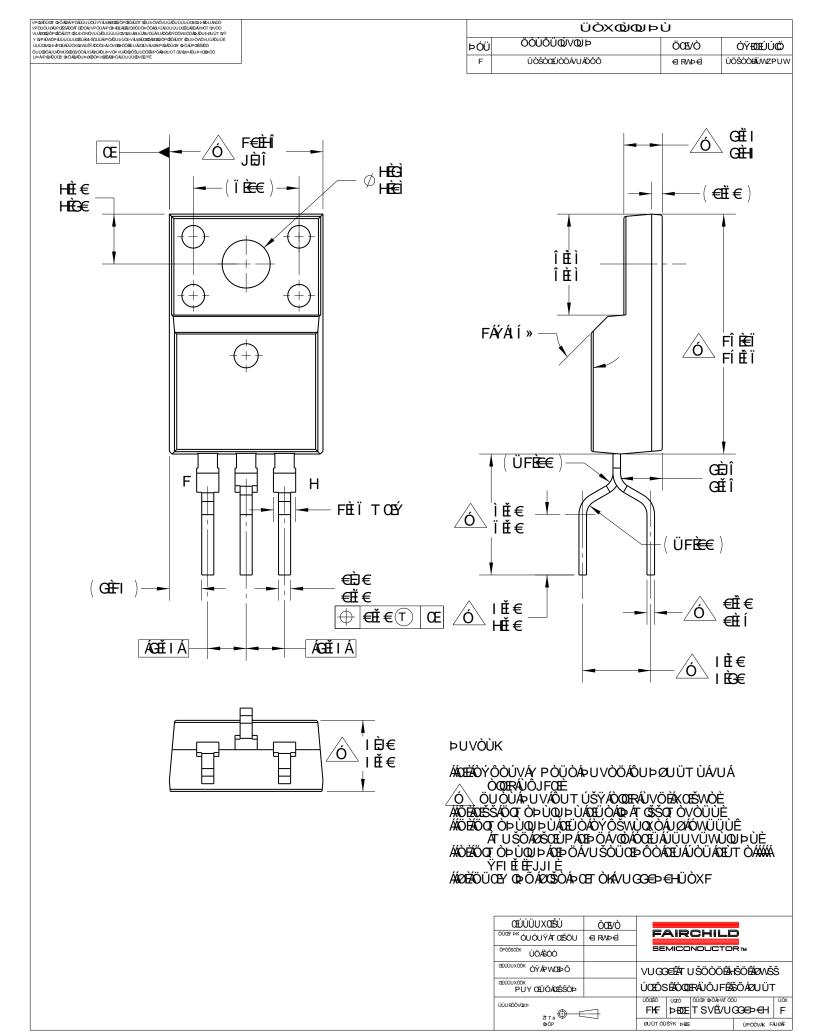


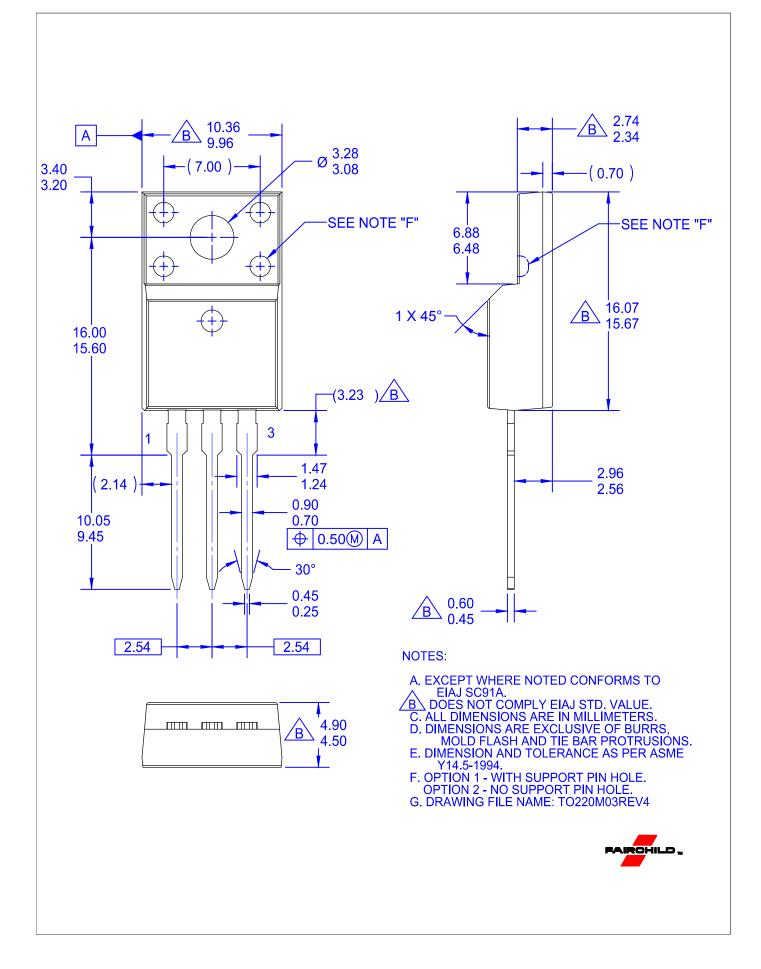


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