

June 2013

FDS4675_F085

40V P-Channel PowerTrench MOSFET

General Description

This P.Channel MOSFET is a rugged gate version of Fairchild Semiconductor's advanced PowerTrench process. It has been optimized for power management applications requiring a wide range of gave drive voltage ratings (4.5V-20V).

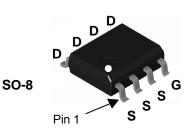
Applications

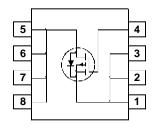
- · Power management
- Load switch
- · Battery protection

Features

- -11 A, -40 V $R_{DS(ON)} = 0.013 \Omega @V_{GS} = -10 V$ $R_{DS(ON)} = 0.017 \Omega @V_{GS} = -4.5 V$
- · Fast switching speed
- High performance trench technology for extremely low $R_{\text{DS}(\text{ON})}$
- · High power and current handling capability
- Qualified to AEC Q101
- RoHS Compliant







Absolute Maximum Ratings T_A=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V _{DSS}	Drain-Source Voltage		-40	V
V _{GSS}	Gate-Source Voltage		±20	V
l _D	Drain Current - Continuous	(Note 1a)	-11	Α
	– Pulsed		-50	
P _D	Power Dissipation for Single Operation	(Note 1a)	2.4 (steady state)	W
		(Note 1b)	1.4	
		(Note 1c)	1.2	
T _J , T _{STG}	Operating and Storage Junction Temperature Range		-55 to +150	°C

Thermal Characteristics

R₀JA	Thermal Resistance, Junction-to-Ambient	(Note 1a)	62.5 (steady state), 50 (10 sec)	°C/W
R _{0JA}	Thermal Resistance, Junction-to-Ambient	(Note 1c)	125	°C/W
R _{0JC}	Thermal Resistance, Junction-to-Case	(Note 1)	25	°C/W

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape width	Quantity
FDS4675	FDS4675_F085	13"	12mm	2500 units

Electrical Characteristics T _A = 25°C unless otherwise noted							
Symbol	Parameter	Test Conditions	Min	Тур	Max	Units	
Off Characteristics							
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-4 0			V	
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I _D = –250 μA, Referenced to 25°C		-34		mV/°C	
l _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -32 \text{ V}, V_{GS} = 0 \text{ V}$			-1	μΑ	
I _{GSSF}	Gate–Body Leakage, Forward	$V_{GS} = 20 \text{ V}, \qquad V_{DS} = 0 \text{ V}$			100	nA	
I _{GSSR}	Gate–Body Leakage, Reverse	$V_{GS} = -20 \text{ V}$ $V_{DS} = 0 \text{ V}$			-100	nA	
On Char	acteristics (Note 2)						
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	– 1	-1.4	-3	V	
$\Delta V_{GS(th)} \over \Delta T_J$	Gate Threshold Voltage Temperature Coefficient	I_D = –250 μA, Referenced to 25°C		4.6		mV/°C	
R _{DS(on)}	Static Drain–Source On–Resistance	$V_{GS} = -10 \text{ V}, I_D = -11 \text{ A}$ $V_{GS} = -4.5 \text{ V}, I_D = -9.5 \text{ A}$		10 13	13 17	mΩ	
	on redictand	V _{GS} =-10 V, I _D =-11 A, T _J =125°C		15	21		
g FS	Forward Transconductance	$V_{DS} = -5 \text{ V}, \qquad I_D = -11 \text{ A}$		44		S	
Dynamic	Dynamic Characteristics						
Ciss	Input Capacitance	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V},$		4350		pF	
Coss	Output Capacitance	f = 1.0 MHz		622		pF	
C _{rss}	Reverse Transfer Capacitance			290		pF	
Switchin	g Characteristics (Note 2)						
t _{d(on)}	Turn-On Delay Time	$V_{DD} = -20 \text{ V}, \qquad I_D = -1 \text{ A},$		20	36	ns	
t _r	Turn-On Rise Time	$V_{GS} = -4.5 \text{ V}, R_{GEN} = 6 \Omega$		29	46	ns	
t _{d(off)}	Turn-Off Delay Time			95	152	ns	
t _f	Turn-Off Fall Time			60	96	ns	
Qg	Total Gate Charge	$V_{DS} = -20 \text{ V}, I_{D} = -11 \text{ A},$		40	56	nC	
Q _{gs}	Gate-Source Charge	V _{GS} = -4.5 V		11		nC	
Q _{gd}	Gate-Drain Charge]		13		nC	
Drain-So	ource Diode Characteristics	and Maximum Ratings					
Is	Maximum Continuous Drain-Source	<u> </u>			-2.1	Α	
V_{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_S = -2.1 \text{ A} \text{(Note 2)}$		-0.7	-1.2	V	

Notes

 R_{BJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{BJC} is guaranteed by design while R_{BCA} is determined by the user's board design.



a) 50°C/W when mounted on a 1in² pad of 2 oz copper



b) 105°C/W when mounted on a .04 in² pad of 2 oz copper



c) 125°C/W when mounted on a minimum pad.

Scale 1 : 1 on letter size paper

2. Pulse Test: Pulse Width < 300µs, Duty Cycle < 2.0%

Typical Characteristics

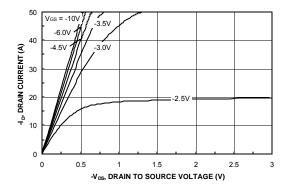


Figure 1. On-Region Characteristics.

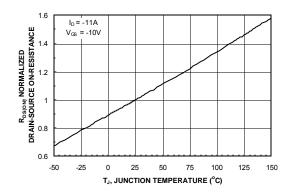


Figure 3. On-Resistance Variation with Temperature.

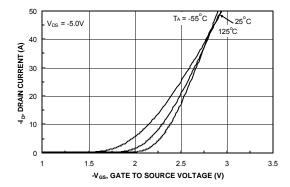


Figure 5. Transfer Characteristics.

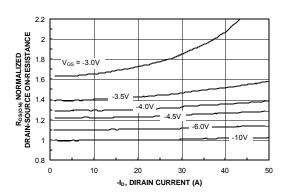


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

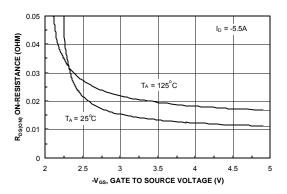


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

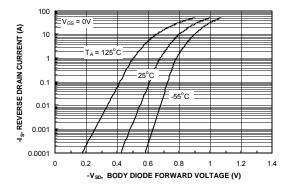
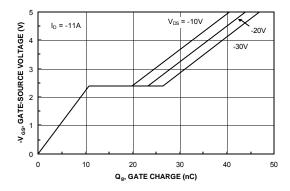


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

Typical Characteristics



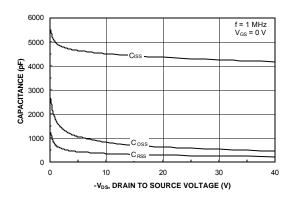
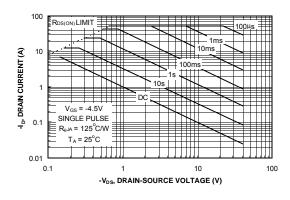


Figure 7. Gate Charge Characteristics.

Figure 8. Capacitance Characteristics.



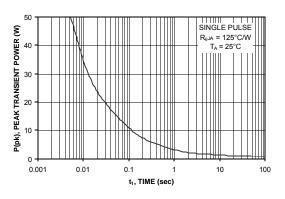


Figure 9. Maximum Safe Operating Area.

Figure 10. Single Pulse Maximum Power Dissipation.

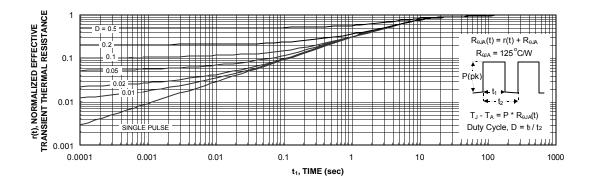


Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1c. Transient thermal response will change depending on the circuit board design.





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