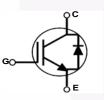


February 2016

## **Features**

- Maximum Junction Temperature : T<sub>J</sub> = 175°C
- Positive Temperaure Co-efficient for Easy Parallel Operating
- High Current Capability
- Low Saturation Voltage:  $V_{CE(sat)} = 1.4 \text{ V} (\text{Typ.}) @ I_C = 40 \text{ A}$
- 100% of the Parts tested for I<sub>LM</sub>(1)
- High Input Impedance
- Tighten Parameter Distribution
- RoHS Compliant





Using Fairchild's proprietary trench design and advanced field

stop IGBT technology, 650V field stop offers superior conduction and switching performance and easy parallel operation.

This device is well suited for the resonant or soft switching appli-

**General Description** 

Applications

Induction Heating, MWO

cation such as induction heating and MWO.

## **Absolute Maximum Ratings**

Symbol	Description		FGA40S65SH	Unit
V <sub>CES</sub>	Collector to Emitter Voltage		650	V
V <sub>GES</sub>	Gate to Emitter Voltage		± 20	V
	Transient Gate to Emitter Voltage		± 30	V
1.	Collector Current	@ T <sub>C</sub> = 25 <sup>o</sup> C	80	A
I <sub>C</sub>	Collector Current	@ T <sub>C</sub> = 100 <sup>o</sup> C	40	A
I <sub>LM</sub> (1)	Pulsed Collector Current @ $T_{C} = 25^{\circ}C$		120	A
I <sub>CM</sub> (2)	Pulsed Collector Current		120	A
IF	Diode Forward Current	@ T <sub>C</sub> = 25 <sup>o</sup> C	40	A
	Diode Forward Current	@ T <sub>C</sub> = 100°C	20	A
I <sub>FM</sub>	Pulsed Diode Maximum Forward Cu	240	A	
P <sub>D</sub>	Maximum Power Dissipation	@ T <sub>C</sub> = 25 <sup>o</sup> C	268	W
. D	Maximum Power Dissipation	@ T <sub>C</sub> = 100°C	134	W
TJ	Operating Junction Temperature	-55 to +175	°C	
T <sub>stg</sub>	Storage Temperature Range		-55 to +175	°C
TL	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C

## **Thermal Characteristics**

Symbol	Parameter	FGA40S65SH	Unit	
$R_{\thetaJC}$ (IGBT)	Thermal Resistance, Junction to Case, Max.	0.56	°C/W	
$R_{ ext{ heta}JA}$	Thermal Resistance, Junction to Ambient, Max.	40	°C/W	

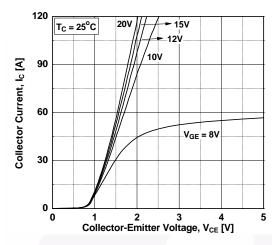
#### Notes:

1. V<sub>CC</sub> = 400 V, V<sub>GE</sub> = 15 V, I<sub>C</sub> = 120 A, R<sub>G</sub> = 35  $\Omega$ , Inductive Load 2. Repetitive rating: Pulse width limited by max. junction temperature

<b>Device Ma</b>	Device MarkingDeviceFFGA40S65SHFGA40S65SH		Package	Reel Size	Таре	Width	Qty p	er Tube
FGA40S6			TO-3PN	-		-	30	
Electrica	I Chara	acteristics of the	<b>IGBT</b> T <sub>C</sub> = 25°	C unless otherwise noted	·			
Symbol		Parameter	Test	Conditions	Min.	Тур.	Max.	Unit
Off Characte	eristics							
BV <sub>CES</sub>	Collector to	Emitter Breakdown Voltage	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 1 mA		650	-	-	V
ΔBV <sub>CES</sub>	Temperatu Voltage	re Coefficient of Breakdown	$V_{GE} = 0 \text{ V}, \text{ I}_{C} = 1 \text{ mA}$		-	0.65	-	V/ºC
I <sub>CES</sub>	Collector C	ut-Off Current	V <sub>CE</sub> = V <sub>CES</sub> , V <sub>GE</sub> = 0 V		-	-	250	μA
I <sub>GES</sub>	G-E Leaka	ge Current	$V_{GE} = V_{GES},$	$V_{CE} = 0 V$	-	-	± 400	nA
On Characte	eristics							
V <sub>GE(th)</sub>	G-E Thres	nold Voltage	I <sub>C</sub> = 40 mA, \	$V_{CE} = V_{GE}$	4.0	5.3	7.5	V
			$I_{C} = 40 \text{ A}, V_{GE} = 15 \text{ V}$		-	1.40	1.81	V
V <sub>CE(sat)</sub>	Collector to Emitter Saturation Voltage			I <sub>C</sub> = 40 A, V <sub>GE</sub> = 15 V,		1.65	-	V
			I <sub>F</sub> = 20 A, T <sub>C</sub>	= 25°C	-	1.45	1.95	V
V <sub>FM</sub> I	Diode Forward Voltage		I <sub>F</sub> = 20 A, T <sub>C</sub>	= 175°C	-	1.65	-	V
C <sub>oes</sub>	Input Capa Output Cap	citance pacitance	V <sub>CE</sub> = 30 V, V f = 1 MHz	′ <sub>GE</sub> = 0 V,	-	2012 49	-	pF pF
C <sub>res</sub> I	Reverse T	ansfer Capacitance				26	-	pF
Switching C	haracteris	tics						
T <sub>d(on)</sub>	Turn-On D	elay Time			-	19.2	-	ns
	Rise Time					65.6	-	ns
	Turn-Off D	elay Time	V <sub>CC</sub> = 400 V, I <sub>C</sub> = 40 A,		-	68.8	-	ns
	Fall Time		R <sub>G</sub> = 6 Ω, V <sub>G</sub>	$R_G = 6 \Omega$ , $V_{GE} = 15 V$ , Resistive Load, $T_C = 25^{\circ}C$		96.8	-	ns
E <sub>on</sub>	Turn-On S	witching Loss	Resistive Loa			194	-	uJ
	Turn-Off S	witching Loss			-	388	- /	uJ
E <sub>ts</sub>	Total Switc	hing Loss			-	592	-	uJ
T <sub>d(on)</sub>	Turn-On D	elay Time			-	19.2	-	ns
	Rise Time			$V_{CC} = 400 \text{ V}, \text{ I}_{C} = 40 \text{ A},$ $R_{G} = 6 \Omega, \text{ V}_{GE} = 15 \text{ V},$ ResistiveLoad, $T_{C} = 175^{\circ}\text{C}$	-	87.2	-	ns
T <sub>d(off)</sub>	Turn-Off D	elay Time	V <sub>CC</sub> = 400 V,		-	75.2	-	ns
	Fall Time		R <sub>G</sub> = 6 Ω, V <sub>G</sub>		-	158	-	ns
E <sub>on</sub>	Turn-On S	witching Loss	ResistiveLoa		-	292	-	uJ
E <sub>off</sub>	Turn-Off Sv	witching Loss			-	633	-	uJ
E <sub>ts</sub>	Total Switc	hing Loss			-	925	-	uJ
Q <sub>g</sub>	Total Gate	Charge	N/ 100 Y	1 10.4	-	73	-	nC
Q <sub>ge</sub>	Gate to En	nitter Charge	V <sub>CE</sub> = 400 V, V <sub>GE</sub> = 15 V	$I_{\rm C} = 40  {\rm A},$	-	13	-	nC
90								

## **Typical Performance Characteristics**

### **Figure 1. Typical Output Characteristics**





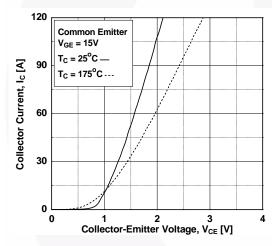


Figure 5. Saturation Voltage vs. V<sub>GE</sub>

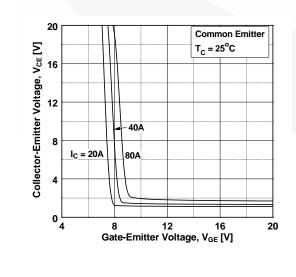
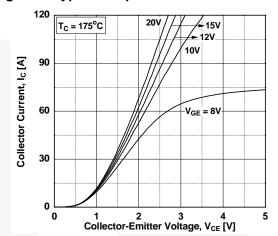
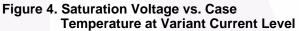


Figure 2. Typical Output Characteristics





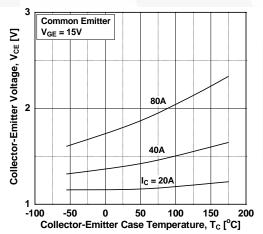
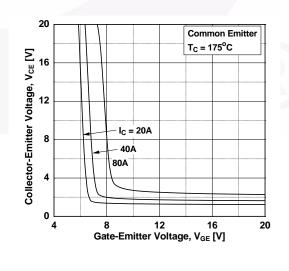
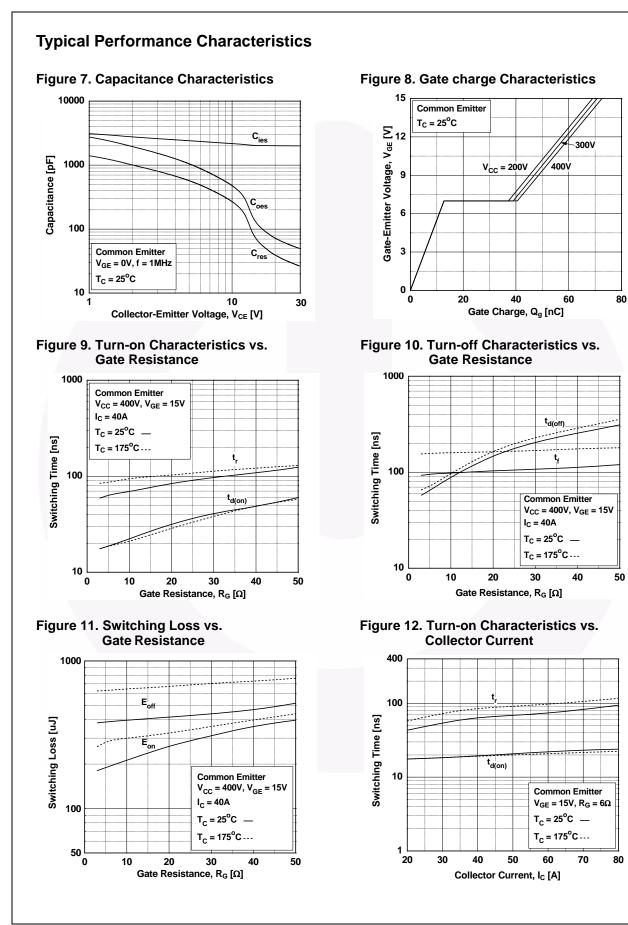
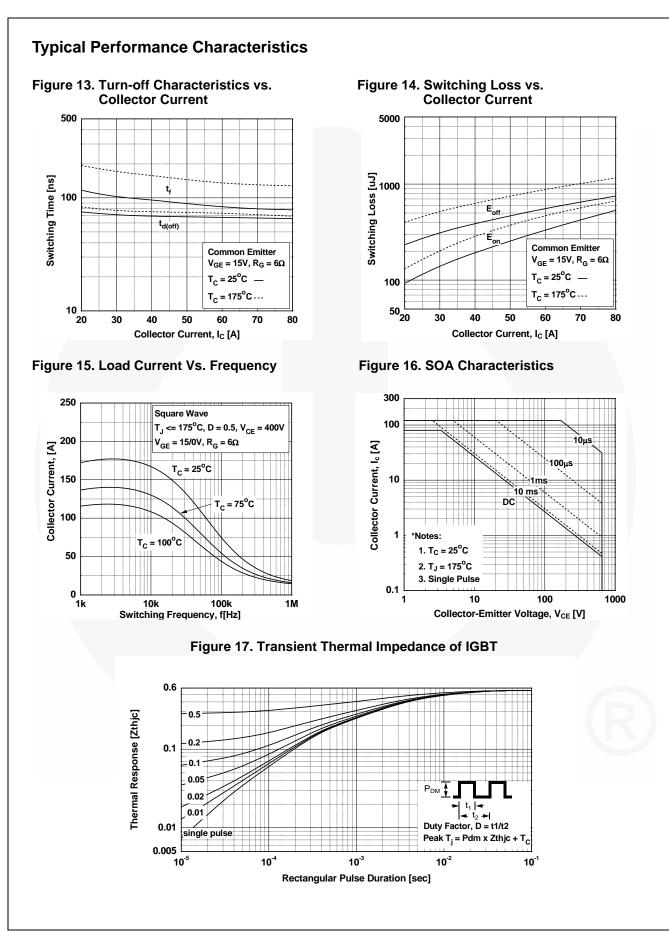


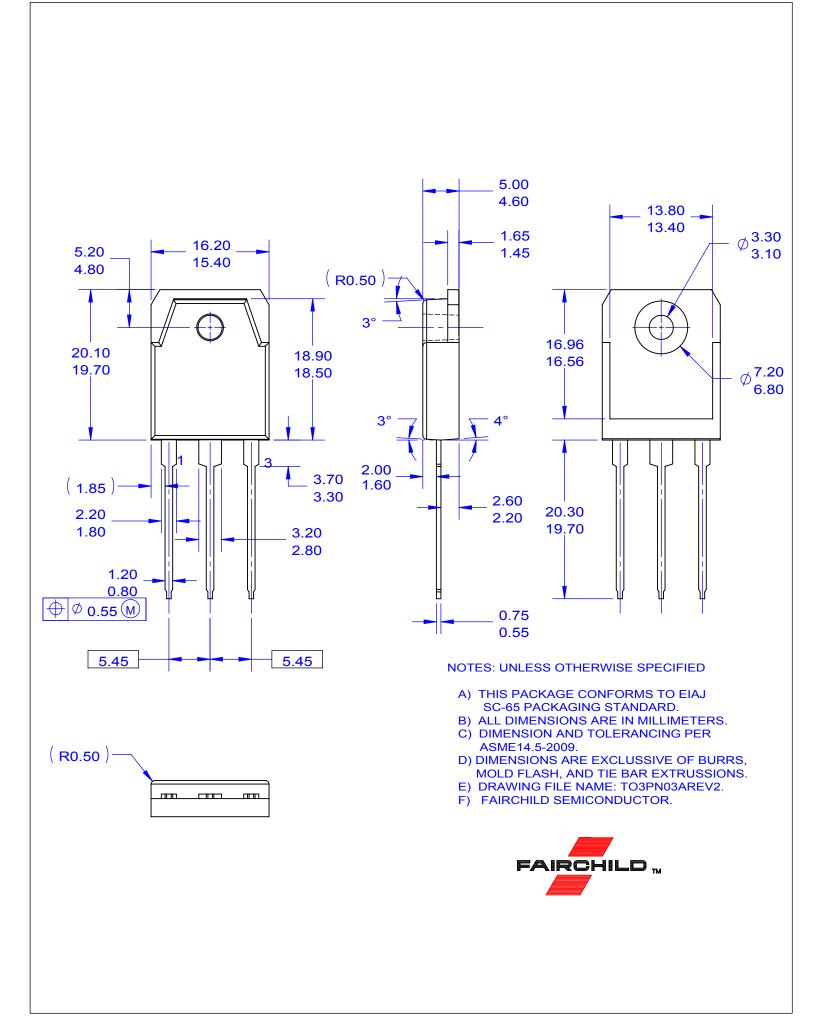
Figure 6. Saturation Voltage vs. V<sub>GE</sub>













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