

October 2015

FJN4303R PNP Epitaxial Silicon Transistor with Bias Resistor

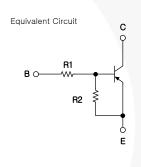
Features

- 100 mA Output Current Capability
- Built-in Bias Resistor (R₁ = 22 kΩ, R₂ = 22 kΩ)

Applications

- Switching, Interface, and Driver Circuits
- Inverters
- Digital Applications in Industrial Segments





Transistors with built-in resistors can be excellent

space- and cost-saving solutions by reducing compo-

nent count and simplifying circuit design.

Description

Ordering Information

Part Number	Top Mark	Package	Packing Method
FJN4303RTA	R4303	TO-92 3L	Ammo

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit	
V _{CBO}	Collector-Base Voltage	-50	V	
V _{CEO}	Collector-Emitter Voltage	-50	V	
V _{EBO}	Emitter-Base Voltage	-10	V	
۱ _C	Collector Current	-100	mA	
TJ	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	-55 to 150	°C	

Thermal Characteristics⁽¹⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Parameter	Value	Unit
Power Dissipation	300	mW
Derate Above T _A = 25°C	2.4	mW/°C
Thermal Resistance, Junction to Ambient	416	°C/W
	Power Dissipation Derate Above T _A = 25°C	Power Dissipation300Derate Above $T_A = 25^{\circ}C$ 2.4

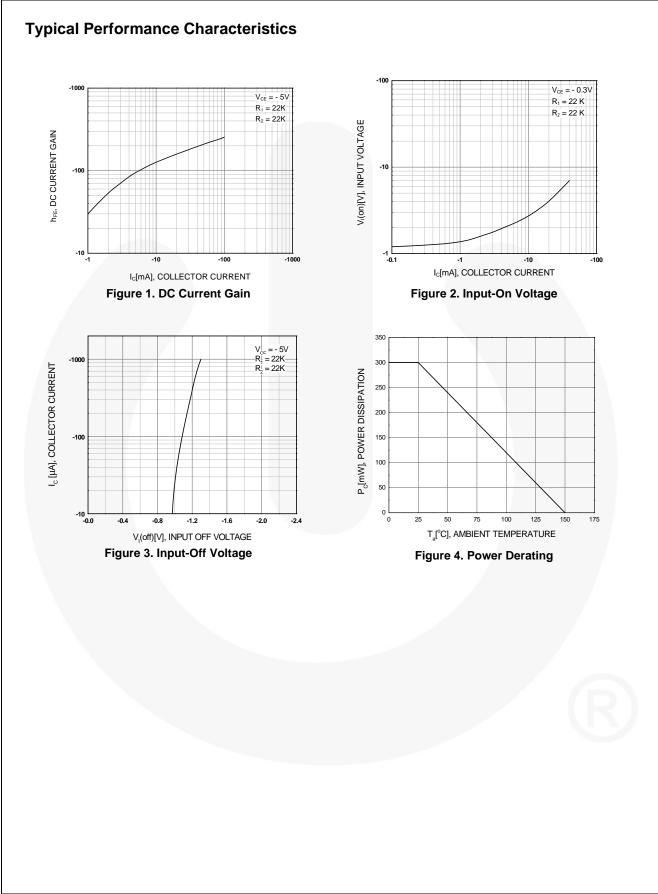
Note:

1. PCB size: FR-4 76 x 114 x 0.6T mm³ (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

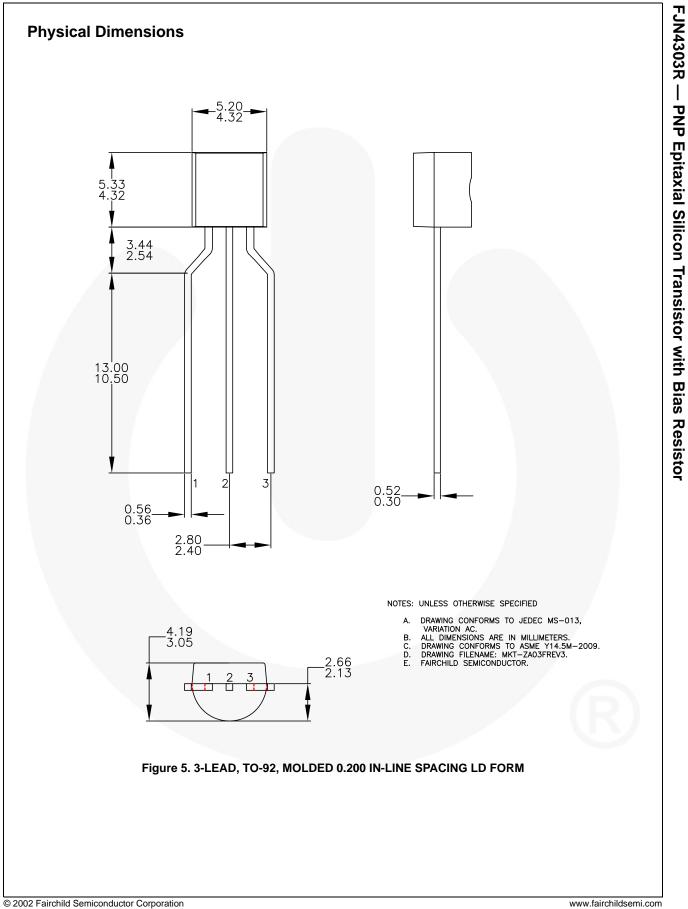
Electrical Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{CBO}	Collector-Base Breakdown Voltage	$I_{C} = -10 \ \mu A, I_{E} = 0$	-50			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_{\rm C} = -100 \ \mu \text{A}, \ I_{\rm B} = 0$	-50			V
I _{CBO}	Collector Cut-Off Current	$V_{CB} = -40 \text{ V}, I_{E} = 0$			-0.1	μΑ
h _{FE}	DC Current Gain	$V_{CE} = -5 \text{ V}, \text{ I}_{C} = -5 \text{ mA}$	56			
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = -10 mA, I _B = -0.5 mA			-0.3	V
f _T	Current Gain Bandwidth Product	$V_{CE} = -10 \text{ V}, I_{C} = -5 \text{ mA}$		200		MHz
C _{ob}	Output Capacitance	$V_{CB} = -10 \text{ V}, I_E = 0,$ f = 1.0 MHz		5.5		pF
V _I (off)	Input-Off Voltage	$V_{CE} = -5 \text{ V}, \text{ I}_{C} = -100 \mu\text{A}$			-0.5	V
V _l (on)	Input-On Voltage	$V_{CE} = -0.3 \text{ V}, I_{C} = -5 \text{ mA}$	-3.0			V
R ₁	Input Resistor		15	22	29	kΩ
R_1/R_2	Resistor Ratio		0.9	1.0	1.1	



FJN4303R — PNP Epitaxial Silicon Transistor with Bias Resistor



FJN4303R Rev. 1.4

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