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FPAB30BH60 PFC SPM® 3 Series for Single-Phase Boost PFC



December 2013

FPAB30BH60 PFC SPM[®] 3 Series for Single-Phase Boost PFC

Features

- UL Certified No. E209204 (UL1557)
- 600 V 30 A Single-Phase Boost PFC with Integral Gate Driver and Protection
- Very Low Thermal Resistance Using Al₂O₃ DBC Substrate
- Full-Wave Bridge Rectifier and High-Performance Output Diode
- Built-in NTC Thermistor for Temperature Monitoring
- Optimized for 20kHz Switching Frequency
- Isolation Rating: 2500 Vrms/min.

Applications

Single-Phase Boost PFC Converter

Related Source

- AN-9090 PFC SPM 3 Series User's Guide
- AN-9091 Boost PFC Inductor Design Guide

General Description

The FPAB30BH60 is a PFC SPM[®] 3 module providing a fully-featured, high-performance Boost PFC (Power Factor Correction) input power stage for consumer, medical, and industrial applications. These modules integrate optimized gate drive of the built-in IGBT to minimize EMI and losses. while also providing multiple on-module protection features including under-voltage lockout, over-current shutdown, thermal monitoring, and fault reporting. These modules also feature a full-wave rectifier, and high-performance output diode for additional space savings and mounting convenience

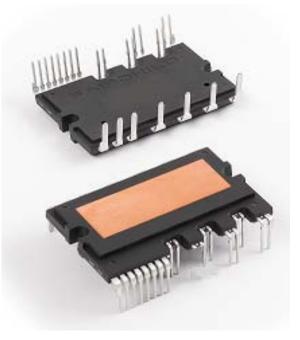


Figure 1. Package Overview

Package Marking & Ordering Information

Device	Device Marking	Package	Packing Type	Quantity
FPAB30BH60	FPAB30BH60	SPMIA-027	Rail	10

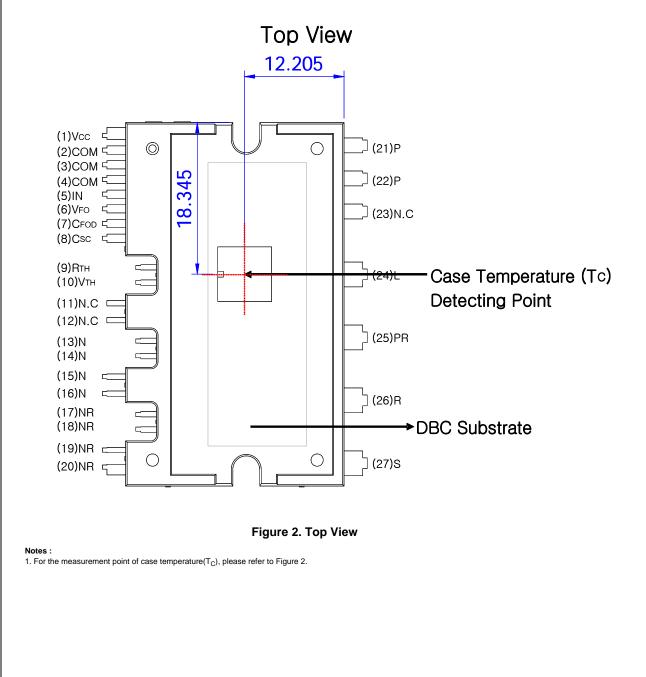
Integrated Power Functions

• PFC converter for single-phase AC / DC power conversion (please refer to Figure 3)

Integrated Drive, Protection, and System Control Functions

- For IGBTs: gate drive circuit, Over-Current Protection (OCP), control supply circuit Under-Voltage Lock-Out (UVLO) Protection
- Fault signal: corresponding to OC and UV fault
- Built-in thermistor: temperature monitoring
- Input interface: active-HIGH interface, works with 3.3 / 5 V logic, Schmitt-trigger input

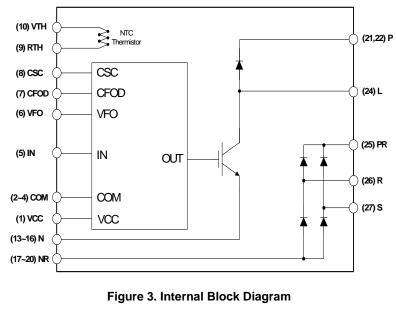
Pin Configuration



Pin Number	Pin Name	Pin Description	
1	V _{CC}	Common Bias Voltage for IC and IGBT Driving	
2,3,4	COM	Common Supply Ground	
5	IN	Signal Input for IGBT	
6	V _{FO}	Fault Output	
7	C _{FOD}	Capacitor for Fault Output Duration Selection	
8	C _{SC}	Capacitor (Low-Pass Filter) for Over-Current Detection	
9	R _(TH)	Series Resistor for The Use of Thermistor	
10	V _(TH)	Thermistor Bias Voltage	
11,12	N.C	No Connection*	
13~16	Ν	IGBT Emitter	
17~20	N _R	Negative DC-Link of Rectifier	
21,22	Р	Positive Rail of DC-Link	
23	N.C	No Connection	
24	L	Reactor Connection Pin	
25	P _R	Positive DC-Link of Rectifier	
26	R	AC Input for R-Phase	
27	S	AC Input for S-Phase	

* 11th and 12th pins are cut. Please refer to package outline drawings for more detail.

Internal Equivalent Circuit and Input/Output Pins



FPAB30BH60 PFC SPM® 3 Series for Single-Phase Boost PFC

Absolute Maximum Ratings ($T_J = 25^{\circ}C$, unless otherwise specified.) Converter Part

Symbol	Item	Condition	Rating	Unit
V _i	Supply Voltage	Applied between R - S	264	V _{rms}
V _{i(Surge)}	Supply Voltage (Surge)	Applied between R - S	500	V
V _{PN}	Output Voltage	Applied between P - N		V
V _{PN(Surge)}	Output Voltage (Surge)	Applied between P - N	500	V
V _{CES}	Collector - Emitter Voltage		600	V
I _{FSM}	Peak Forward Surge Current	Single Half Sine-Wave	250	А
l _i	Input Current (100% Load)	$T_{C} < 95^{\circ}C, V_{i} = 220 V, V_{PN} = 390 V, V_{PWM} = 20 \text{ kHz}$	25	А
I _{i(125%)}	Input Current (125% Load)	5% Load) $T_{C} < 95^{\circ}C, V_{i} = 220 V, V_{PN} = 390 V, V_{PWM} = 20 \text{ kHz},$ 1 Minite Non-Repetitive		A
P _C	Collector Dissipation	$T_{C} = 25^{\circ}C$	169	W
Τ _J	Operating Junction Temperature		-20 ~ 150	°C

Notes:

The maximum junction temperature rating of the power chips integrated within the PFC SPM[®] product is 150 °C(@T_C \leq 100°C). However, to insure safe operation of the PFC SPM product, the average junction temperature should be limited to T_{J(ave)} \leq 125°C (@T_C \leq 100°C)

Control Part

Symbol	ltem	Condition	Rating	Unit
V _{CC}	Control Supply Voltage	Applied between V _{CC} - COM	20	V
V _{IN}	Input Signal Voltage	Applied between IN - COM	-0.3 ~ V _{CC} +0.3	V
V _{FO}	Fault Output Supply Voltage	Applied between V _{FO} - COM	-0.3 ~ V _{CC} +0.3	V
I _{FO}	Fault Output Current	Sink Current at V _{FO} Pin	5	mA
V _{SC}	Current Sensing Input Voltage	Applied between C _{SC} - COM	-0.3 ~ V _{CC} +0.3	V

Total System

Symbol	ltem	Item Condition		Unit
т _с	Module Case Operating Temperature		-20 ~ 100	°C
T _{STG}	Storage Temperature		-40 ~ 125	°C
V _{ISO}	Isolation Voltage	60 Hz, Sinusoidal, AC 1 Minute, Connect Pins to Heat Sink Plate	2500	V _{rms}

Thermal Resistance

Symbol	ltem	Condition	Min.	Тур.	Max.	Unit
$R_{\theta(j\text{-}c)Q}$	Junction to Case Thermal Resistance	IGBT	-	-	0.74	°C/W
$R_{\theta(j\text{-}c)F}$		FRD	-	-	1.44	°C/W
$R_{\theta(j\text{-}c)R}$		Rectifier (per 1 / 4 module)	-	-	2.07	°C/W

Notes:

2. For the measurement point of case temperature(T_C), please refer to Figure 2.

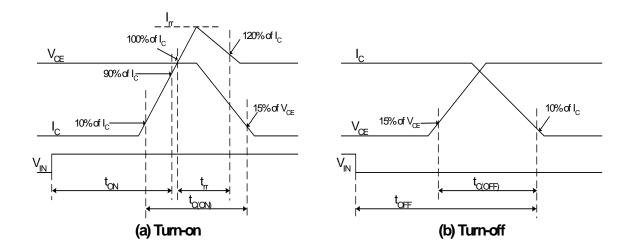
Electrical Characteristics (T_J = 25°C, Unless Otherwise Specified.)

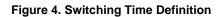
Converter Part

Symbol	ltem	Condition	Min.	Тур.	Max.	Unit
V _{CE(SAT)}	IGBT Saturation Voltage	$V_{CC} = 15 \text{ V}, \text{ V}_{IN} = 5 \text{ V}, \text{ I}_{C} = 30 \text{ A}$	-	2.0	2.8	V
V _{FF}	FRD Forward Voltage	I _F = 30 A	-	1.8	2.5	V
$V_{\sf FR}$	Rectifier Forward Voltage	I _F = 30 A	-	1.2	1.5	V
t _{ON}	Switching Times	$V_{PN} = 400 \text{ V}, V_{CC} = 15 \text{V}, I_{C} = 30 \text{ A}$	-	650	-	ns
t _{C(ON)}		$V_{IN} = 0 V \leftrightarrow 5 V$, Inductive Load (Note 3)	-	400	-	ns
t _{OFF}			-	620	-	ns
t _{C(OFF)}			-	200	-	ns
t _{rr}			-	60	-	ns
l _{rr}			-	3.5	-	А
I _{CES}	Collector - Emitter Leakage Current	$V_{CE} = V_{CES}$	-	-	250	μA

Notes:

3. t_{ON} and t_{OFF} include the propagation delay time of the internal drive IC. t_{C(ON)} and t_{C(OFF)} are the switching time of IGBT itself under the given gate driving condition internally. For the detailed information, please see Figure 4.



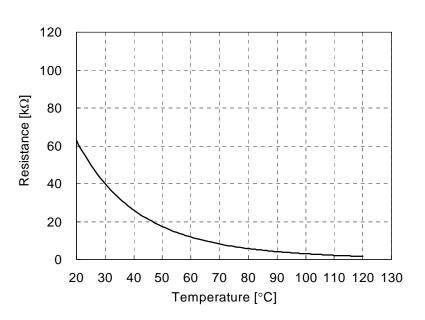


Symbol	ltem	Co	ondition	Min.	Тур.	Max.	Unit
I _{QCCL}	Quiescent V _{CC} Supply Current	V_{CC} = 15 V, IN = 0 V	V _{CC} - COM	-	-	26	mA
V _{FOH}	Fault Output Voltage	$V_{SC} = 0 \text{ V}, \text{ V}_{FO} \text{ Circuit: } 4.7 \text{ k}\Omega \text{ to } 5 \text{ V} \text{ Pull-up}$		4.5	-	-	V
V _{FOL}		V_{SC} = 1 V, V_{FO} Circuit: 4.7 k Ω to 5 V Pull-up		-	-	0.8	V
V _{SC(ref)}	Over-Current Trip Level	V _{CC} = 15 V		0.45	0.5	0.55	V
UV _{CCD}	Supply Circuit Under-Voltage	Detection Level		10.7	11.9	13.0	V
UV _{CCR}	Protection	Reset Level		11.2	12.4	13.2	V
t _{FOD}	Fault-Out Pulse Width	C _{FOD} = 33 nF (Note 3	3)	1.4	1.8	2.0	ms
V _{IN(ON)}	ON Threshold Voltage	Applied between IN -	СОМ	2.8	-	-	V
V _{IN(OFF)}	OFF Threshold Voltage			-	-	0.8	V
R _{TH}	Resistance of Thermistor	at T _{TH} = 25°C (Note 4, Figure 5)		-	50	-	kΩ
		at T _{TH} = 100°C (Note	4, Figure 5)	-	2.99	-	kΩ

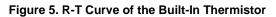
Notes:

3. The fault-out pulse width t_{FOD} depends on the capacitance value of C_{FOD} according to the following approximate equation : $C_{FOD} = 18.3 \times 10^{-6} \times t_{FOD}[F]$

4. T_{TH} is the temperature of know case temperature(T_C), please make the experiment considering your application.



R-T Graph



Recommended Operating Condition

Symbol	Item	Condition	Min.	Тур.	Max.	Unit
Vi	Input Supply Voltage	Applied between R - S	187	220	253	V _{rms}
V _{PN}	Output Voltage	Applied between P - N	-	380	400	V
V _{CC}	Control Supply Voltage	Applied between V _{CC(L)} - COM	13.5	15.0	16.5	V
dV _{CC} /dt	Control Supply Variation		-1	-	1	V/µs
f _{PWM}	PWM Input Frequency	$T_{J} \le 150^{\circ}C$	-	20	-	kHz
li	Allowable Input Current	T _C < 90°C, V _i = 220 V, V _{PN} = 380 V V _{PWM} = 20 kHz	-	-	30	A _{peak}

Mechanical Characteristics and Ratings

ltem	Co	Condition			Max.	Unit
Mounting Torque	Mounting Screw: M3	Recommended 0.62 N•m	0.51	0.62	0.72	N∙m
Device Flatness	See Figure 6		0	-	+120	μm
Weight			-	15.00	-	g

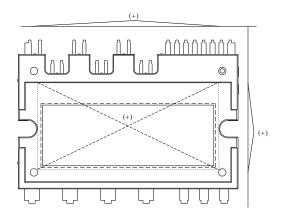
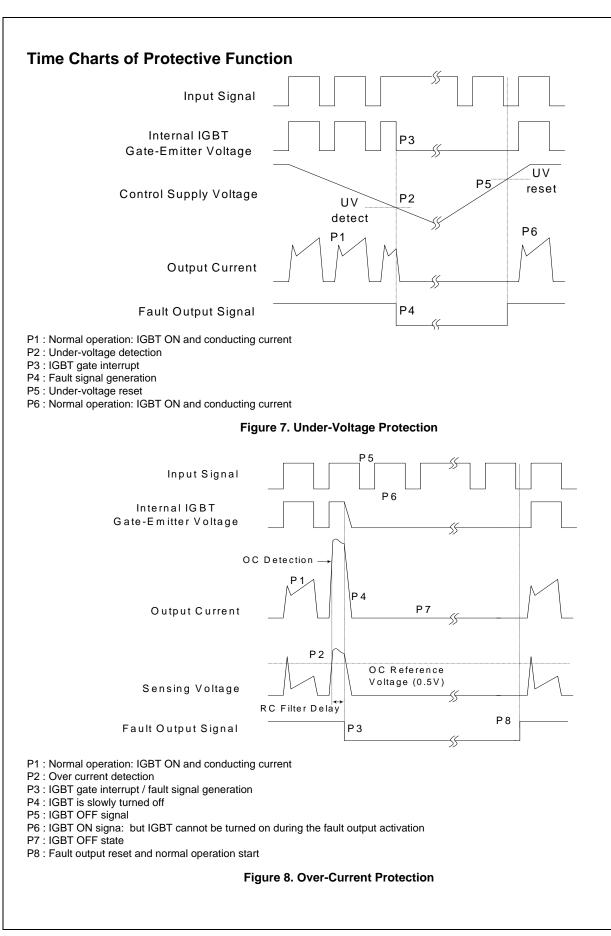
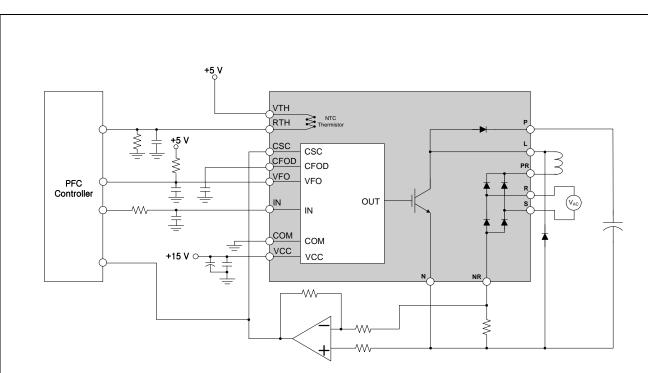


Figure 6. Flatness Measurement Position

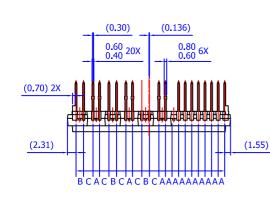




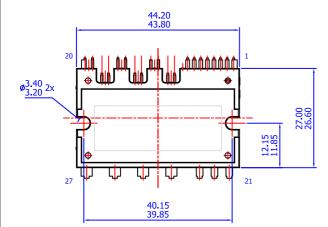
Notes:

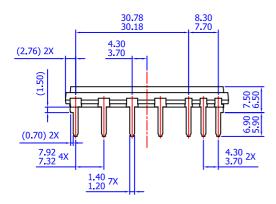
Figure 9. Application Example

5. Each capacitors should be located as close to PFC SPM[®] product pins as possible. 6. It's recommended that anti-parallel diode should be connected with IGBT.



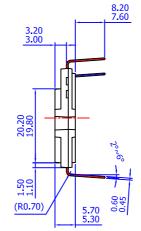
LEAD PITCH (TOLERANCE : ±0.30) A:1.778 B:2.050 C:2.531

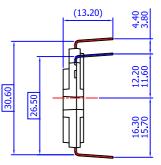


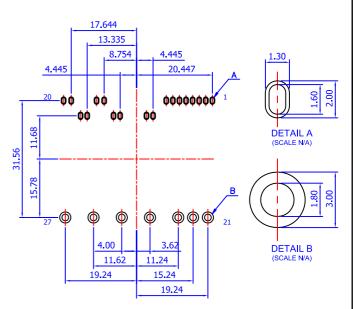


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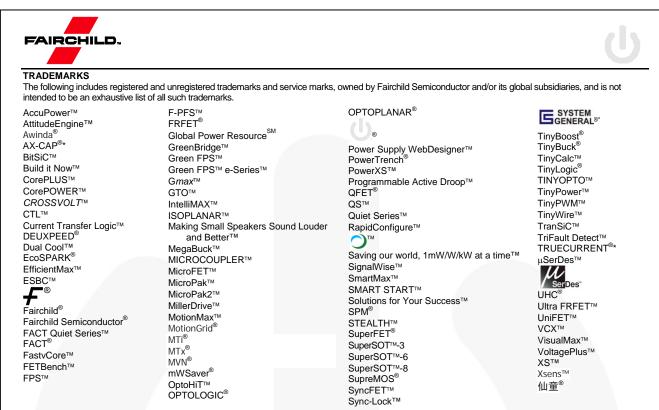






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