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October 2012

FPF1007-FPF1009 IntelliMAX™ Advanced Load Products

Features

- 1.2 to 5.5 V Input Voltage Range
- Typical $R_{ON} = 30 \text{ m}\Omega$ at $V_{IN} = 5.5 \text{ V}$
- Typical $R_{ON} = 40 \text{ m}\Omega$ at $V_{IN} = 3.3 \text{ V}$
- Fixed Three Different Turn-on Rise Time 10 µs / 80 µs / 1 ms
- Low < 10 µA at V_{IN} = 3.3 V Quiescent Current
- Internal ON Pin Pull Down
- Output Discharge Function
- ESD Protection above 8000 V HBM and 2000 V CDM
- RoHS Compliant

Applications

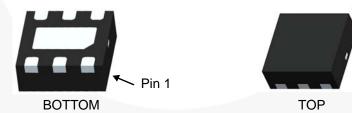
- PDAs
- Cell Phones
- GPS Devices
- MP3 Players
- Digital Cameras
- Peripheral Ports
- Hot-Swap SuppliesNotebook Computers



General Description

The FPF1007/8/9 are low R_{DS} P-Channel MOSFET load switches offered in a selection of 10 μ s, 80 μ s, and 1 ms slew rate turn-on options for transient / in-rush current control. To support trends in mobile application requirements, the minimum operating input voltage has been reduced down to 1.2 V, the input current leakage has been minimized to extend battery life, and the ESD-protection has been designed to withstand a minimum of 8 kV (HBM) and 2 kV (CDM).

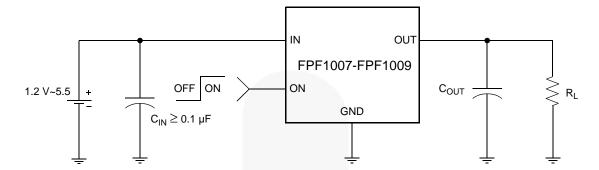
The switch is controlled by an active-high logic input (ON pin), allowing direct interface with a low-voltage control signal. An internal ON pin pull-down resistor protects against unintentional device turn-on in the initial state. An on-chip pull-down resistor on the output is enabled when the switch is turned-off and provides quick, robust discharge of the output load.



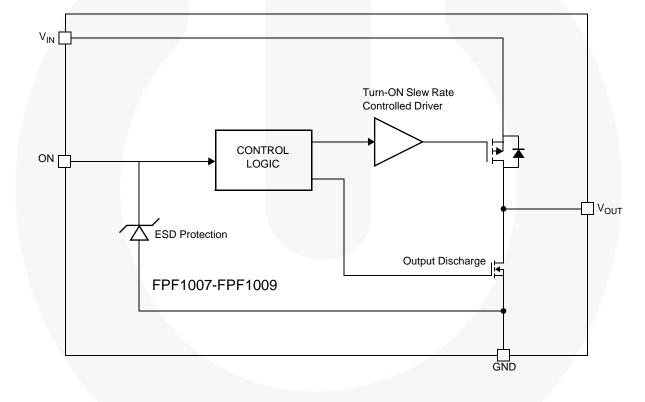
Ordering Information

Part	Switch R _{ON} at 5.5 V [Typ.]	Rise Time [Typ.]	Output Discharge [Typ.]	ON Pin Activity
FPF1007	30 m $Ω$, PMOS	10 µs	60 Ω	Active HIGH
FPF1008	30 m $Ω$, PMOS	80 µs	60 Ω	Active HIGH
FPF1009	30 m $Ω$, PMOS	1 ms	60 Ω	Active HIGH

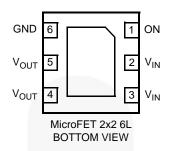
Typical Application Circuit



Functional Block Diagram



Pin Configuration



Pin Description

Pin	Name	Function
4, 5	V _{OUT}	Switch Output: Output of the power switch
2, 3	V _{IN}	Supply Input: Input to the power switch and the supply voltage for the IC
6	GND	Ground
1	ON	ON/OFF Control Input

Absolute Maximum Ratings

Parameter	Min.	Max.	Unit	
V _{IN} , V _{OUT} , ON to GND	-0.3	6.0	V	
Maximum Continuous Switch Current		1.5	А	
Power Dissipation at T _A = 25°C ⁽¹⁾		1.2	W	
Storage Junction Temperature	-65	+150	°C	
Operating Temperature Range	-40	+85	°C	
Thermal Resistance, Junction to Ambient		86	°C/W	
Electrostatic Discharge Protection	НВМ	8000		V
Electrostatic Discharge Protection	CDM	2000		V

Note:

Package power dissipation on 1-square inch pad, 2 oz. copper board.

Recommended Operating Range

Parameter	Min.	Max.	Unit
V _{IN}	1.2	5.5	V
Ambient Operating Temperature, T _A	-40	+85	°C

Electrical Characteristics

 $V_{IN} = 1.2\,V \text{ to } 5.5\,V, T_A = -40 \text{ to } +85^{\circ}C \text{ unless otherwise noted.} \text{ Typical values are at } V_{IN} = 3.3\,V \text{ and } T_A = 25^{\circ}C.$

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units	
Basic Operation	<u>'</u>			ı			
Operating Voltage	V _{IN}		1.2		5.5	V	
0 :		I _{OUT} = 0 mA, V _{IN} = 3.3 V, V _{ON} = Enabled		8		μΑ	
Quiescent Current	IQ	I _{OUT} = 0 mA, V _{IN} = 5.5 V, V _{ON} = Enabled			15		
Off Supply Current	I _Q (off)	V _{ON} = GND, V _{OUT} = OPEN			1	μΑ	
Off Switch Current	I _{SD} (off)	V _{ON} = GND, V _{OUT} = GND		0.1	1.0	μΑ	
		V _{IN} = 5.5 V, I _{OUT} = 200 mA, T _A = 25°C		30	40	mΩ	
		V _{IN} = 3.3 V, I _{OUT} = 200 mA, T _A = 25°C		40	55		
On-Resistance	R _{ON}	V _{IN} = 1.5 V, I _{OUT} = 200 mA, T _A = 25°C		100	130		
On resistance	· ·ON	V _{IN} = 1.2 V, I _{OUT} = 200 mA, T _A = 25°C		175	250		
	1	$V_{IN} = 3.3 \text{ V}, I_{OUT} = 200 \text{ mA},$ $T_A = -40^{\circ}\text{C to } +85^{\circ}\text{C}$	20		65		
Output Pull Down Resistance	R _{PD}	V _{IN} = 3.3 V, V _{ON} = 0 V, T _A = 25°C		60		Ω	
ON Input Logic Low Voltage	V _{IL}	V _{IN} = 1.2 V to 5.5 V			0.4	V	
ON Input Logic High Voltage	V _{IH}	V _{IN} = 1.2 V to 5.5 V	1			V	
ON Input Leakage (On)		$V_{ON} = V_{IN} = 5.5 \text{ V}$			10	μA	
ON Input Leakage (Off)		V _{ON} = GND			1	μA	
Dynamic							
FPF1007							
Turn On	t _{ON}			12		μs	
Rise Time	t _R	$V_{IN} = 3.3 \text{ V}, R_L = 500 \Omega, R_{L_CHIP} = 60 \Omega,$		10		μs	
Turn Off	t _{OFF}	C _{OUT} = 0.1 μF, T _A = 25°C		40		μs	
Fall Time	t _F			15		μs	
FPF1008	<u>'</u>			ı			
Turn On	t _{ON}			125		μs	
Rise Time	t_R $V_{IN} = 3.3 \text{ V}, R_L = 500 \Omega, R_{L_CHIP} =$			80		μs	
Turn Off	t _{OFF}	C _{OUT} = 0.1 μF, T _A = 25°C		40		μs	
Fall Time	t _F	1		15		μs	
FPF1009				I	· /		
Turn On	t _{ON}			2	- y	ms	
Rise Time	t _R	V_{IN} = 3.3 V, R_{L} = 500 Ω , $R_{\text{L_CHIP}}$ = 60 Ω ,		1		ms	
Turn Off	t _{OFF}	C _{OUT} = 0.1 μF, T _A = 25°C		40		μs	
Fall Time	t _F			15		μs	

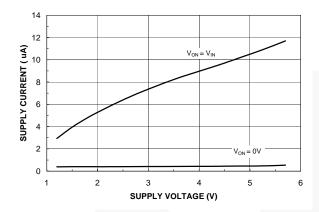


Figure 1. Quiescent Current vs. Input Voltage

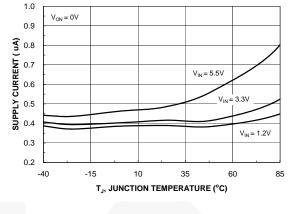


Figure 2. Quiescent Current vs. Temperature

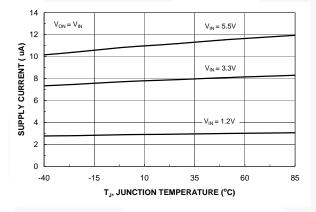


Figure 3. Quiescent Current vs. Temperature

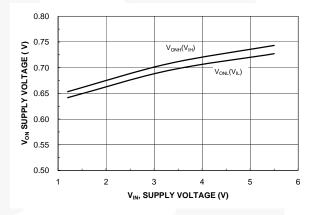


Figure 4. V_{ON} Voltage vs. Input Voltage

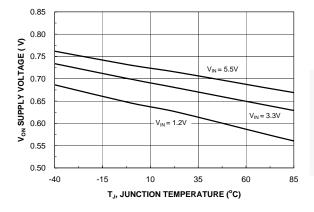


Figure 5. V_{ON} Low Voltage vs. Temperature

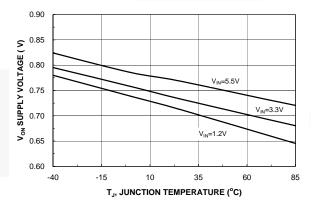


Figure 6. V_{ON} High Voltage vs. Temperature

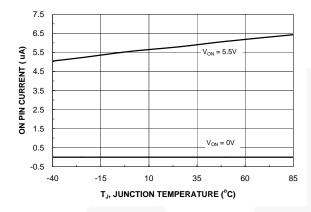


Figure 7. On Pin Current vs. Temperature

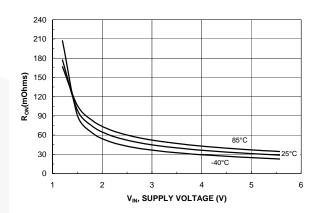


Figure 8. R_{ON} vs. V_{IN}

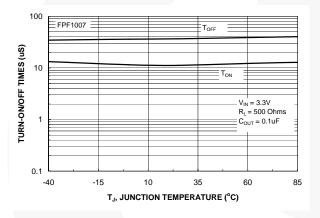


Figure 9. FPF1007 $t_{\mbox{ON}}$ / $t_{\mbox{OFF}}$ vs. Temperature

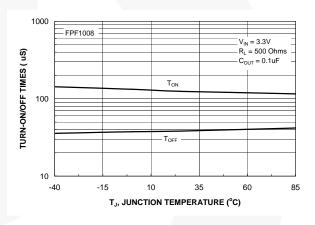


Figure 10. FPF1008 t_{ON} / t_{OFF} vs. Temperature

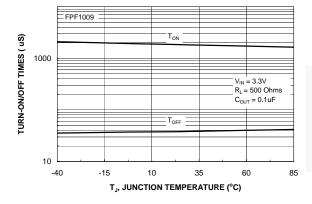


Figure 11. FPF1009 $t_{\mbox{\scriptsize ON}}\,/\,t_{\mbox{\scriptsize OFF}}$ vs. Temperature

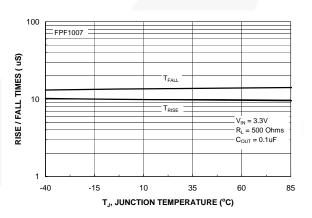


Figure 12. FPF1007 $t_{\mbox{RISE}}$ / $t_{\mbox{FALL}}$ vs. Temperature

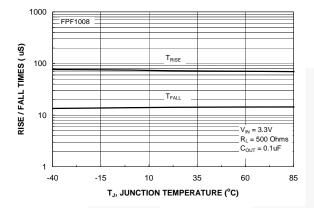


Figure 13. FPF1008 t_{RISE} / t_{FALL} vs. Temperature

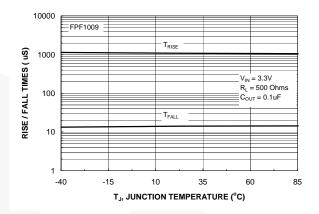


Figure 14. FPF1009 t_{RISE} / t_{FALL} vs. Temperature

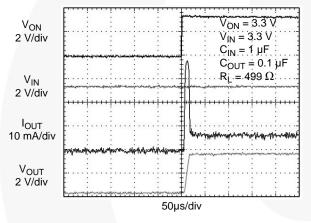


Figure 15. FPF1007 Turn-On Response

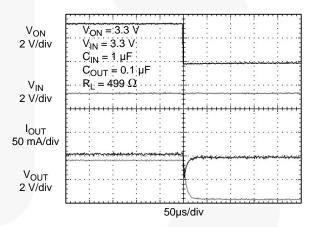


Figure 16. FPF1007 Turn-Off Response Load current discharged through on-chip output discharge resistor

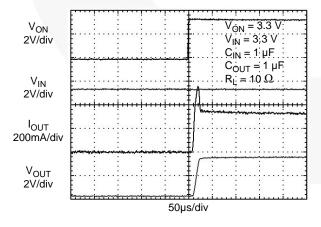


Figure 17. FPF1007 Turn-On Response (C_{OUT} = 1 μF)

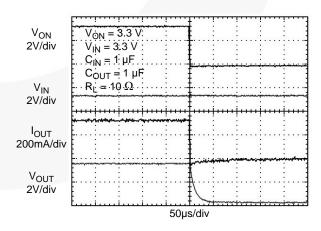
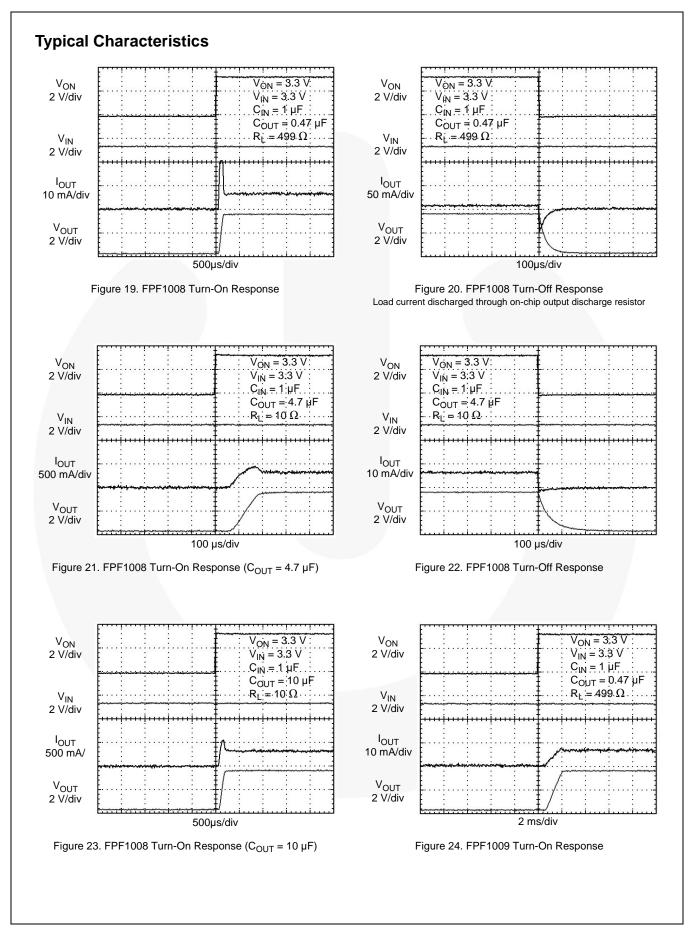


Figure 18. FPF1007 Turn-Off Response



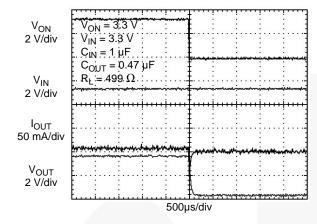


Figure 25. FPF1009 Turn-Off Response Load current discharged through on-chip output discharge resistor

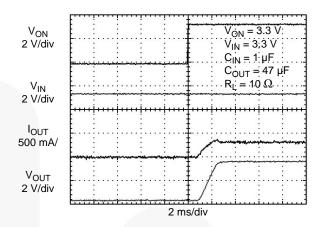


Figure 26. FPF1009 Turn-On Response ($C_{OUT} = 47 \mu F$)

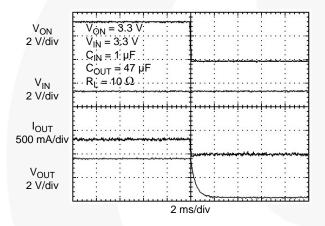


Figure 27. FPF1009 Turn-Off Response

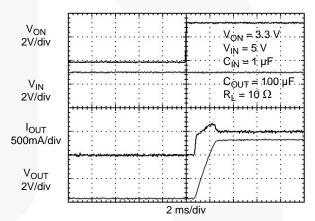
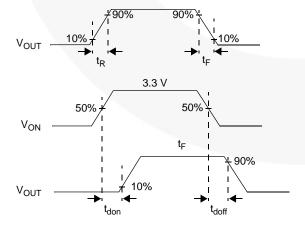


Figure 28. FPF1009 Turn-On Response $(C_{OUT} = 100 \mu F, V_{IN} = 5 V)$

Timing Diagram



where:

 t_{ON} = Turn-On Time = Turn-Off Time

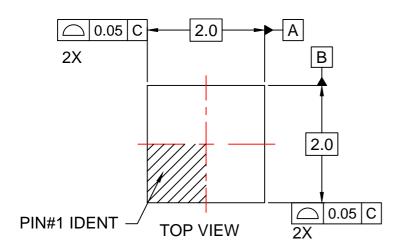
= Turn-On Delay Time = Turn-Off Delay Time

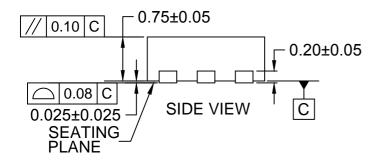
= Rise Time

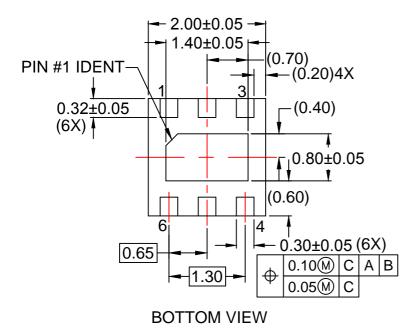
= V_{OUT} Fall Time

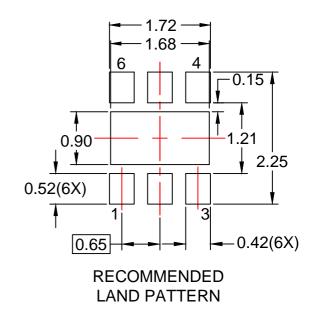
 $t_{ON} = t_{R} + t_{don}$

 $t_{OFF} = t_F + t_{doff}$









NOTES:

- A. PACKAGE DOES NOT FULLY CONFORM TO JEDEC MO-229 REGISTRATION
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