

FQP3N60C N-Channel QFET[®] MOSFET

600 V, 3.0 A, 3.4 Ω

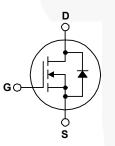
Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.

Features

- 3.0 A, 600 V, $R_{DS(on)}$ = 3.4 Ω (Max.) @ V_{GS} = 10 V, I_D = 1.5 A
- Low Gate Charge (Typ. 10.5 nC)
- Low Crss (Typ. 5.0 pF)
- 100% Avalanche Tested





Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol		Parameter		FQP3N60C	Unit
V _{DSS}	Drain-Source Volt	age		600	V
ID	Drain Current	- Continuous (T _C = 25°C) - Continuous (T _C = 100°C)		3 1.8	A A
I _{DM}	Drain Current	- Pulsed	(Note 1)	12	A
V _{GSS}	Gate-Source volta	age		±30	V
E _{AS}	Single Pulsed Avalanche Energy		(Note 2) 150		mJ
I _{AR}	Avalanche Current		(Note 1)	3	A
E _{AR}	Repetitive Avalanche Energy		(Note 1)	7.5	mJ
dv/dt	Peak Diode Recovery dv/dt (Note		(Note 3)	4.5	V/ns
P _D	Power Dissipation (T _C = 25°C) - Derate above 25°C			75 0.62	W W/°C
T _{J,} T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C	
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds			300	°C

Thermal Characteristics

Symbol	Parameter	FQP3N60C	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	1.67	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	-C/W

December 2013

=QP3N60C
- N-Channel
QFET®
MOSFET

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQP3N60C	FQP3N60C	TO-220	Tube	N/A	N/A	50 units

Electrical Characteristics T_C = 25°C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Charac	teristics					<u> </u>
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	600			V
ΔBV_{DSS} / ΔT_J	Breakdown Voltage Temperature Coefficient	$I_D = 250 \ \mu A$, Referenced to 25°C		0.6		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 600 V, V_{GS} = 0 V$ $V_{DS} = 480 V, T_{C} = 125^{\circ}C$			1 10	μΑ μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V_{GS} = 30 V, V_{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V	-		-100	nA
On Charac	teristics					
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	2.0		4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 1.5 A		2.8	3.4	Ω
9 _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 1.5 A		3.5		S
Dynamic C	Characteristics					
C _{iss}	Input Capacitance	$V_{DS} = 25 V, V_{GS} = 0 V,$		435	565	pF
C _{oss}	Output Capacitance	☐ f = 1.0 MHz		45	60	pF
C _{rss}	Reverse Transfer Capacitance			5	8	pF
	Characteristics					
t _{d(on)}	Turn-On Delay Time	V _{DD} = 300 V, I _D = 3 A		12	34	ns
t _r	Turn-On Rise Time	$R_{G} = 25 \Omega$		30	70	ns
t _{d(off)}	Turn-Off Delay Time			35	80	ns
t _f	Turn-Off Fall Time	(Note 4)		35	80	ns
Qg	Total Gate Charge	V _{DS} = 480 V, I _D = 3 A	-	10.5	14	nC
Q _{gs}	Gate-Source Charge	V _{GS} = 10 V (Note 4)		2.1		nC
Q _{gd}	Gate-Drain Charge			4.5		nC
Drain-Sou	rce Diode Characteristics and Maximur	n Ratings				<u>. </u>
I _S Maximum Continuous Drain-Source Diode Forward Current					3	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				12	Α
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 3 A			1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 3 A		260		ns
Q _{rr}	Reverse Recovery Charge	− dI _F /dt =100 A/μs		1.6		μC

Notes:

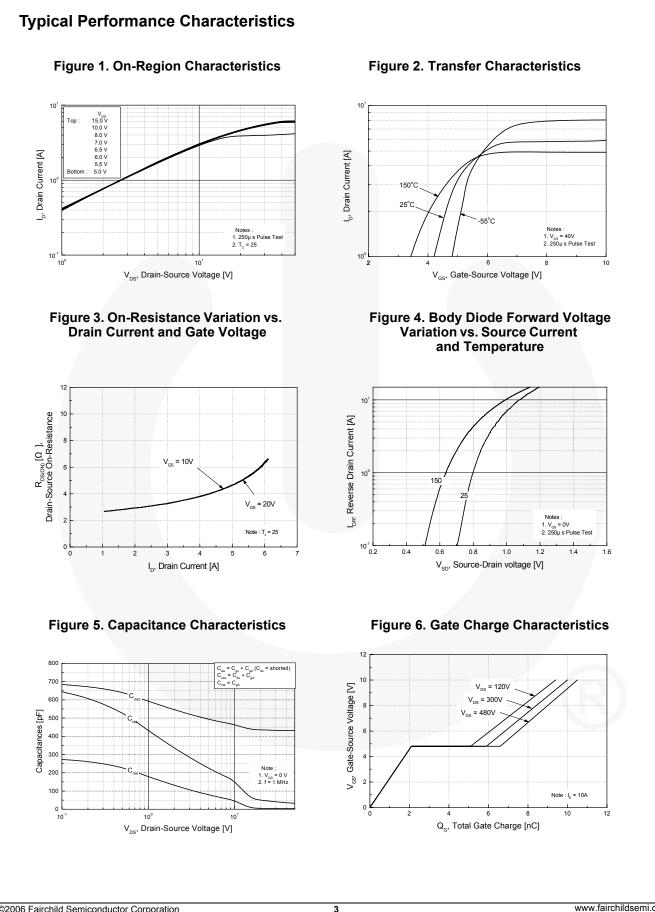
1. Repetitive rating : pulse-width limited by maximum junction temperature.

2. L= 30 mH, I_{AS} = 3 A, V_{DD} = 50 V, R_G = 25 $\Omega,$ starting $\mbox{ T}_{\mbox{J}}$ = 25°C.

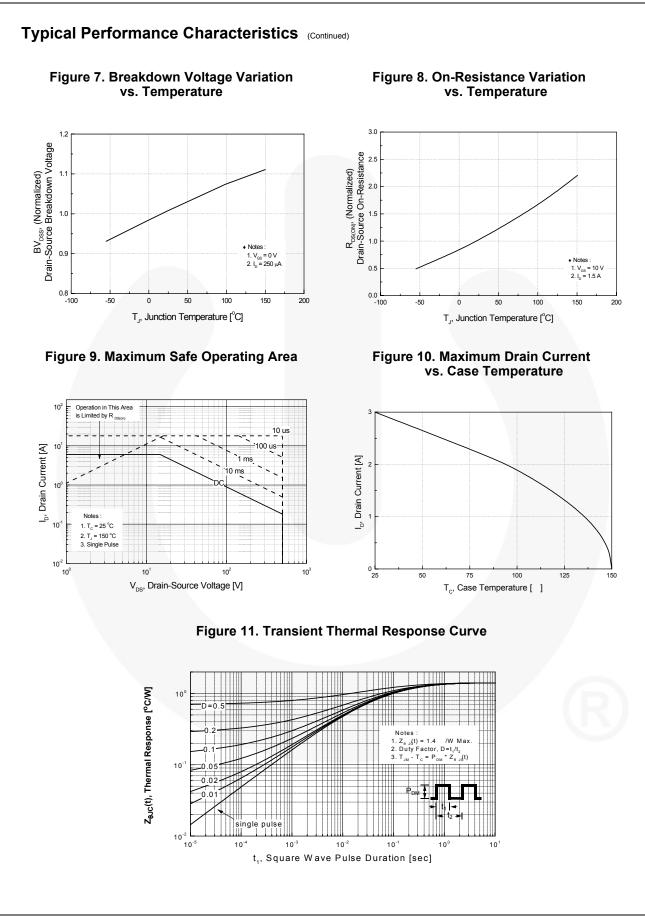
 $3.I_{SD} \leq 3 \text{ A, di/dt} \leq 200 \text{ A/}\mu\text{s, V}_{DD} \leq BV_{DSS,} \text{ Starting } \ \text{T}_{J} \text{ = } 25^{\circ}\text{C}.$

4. Essentially independent of operating temperature.

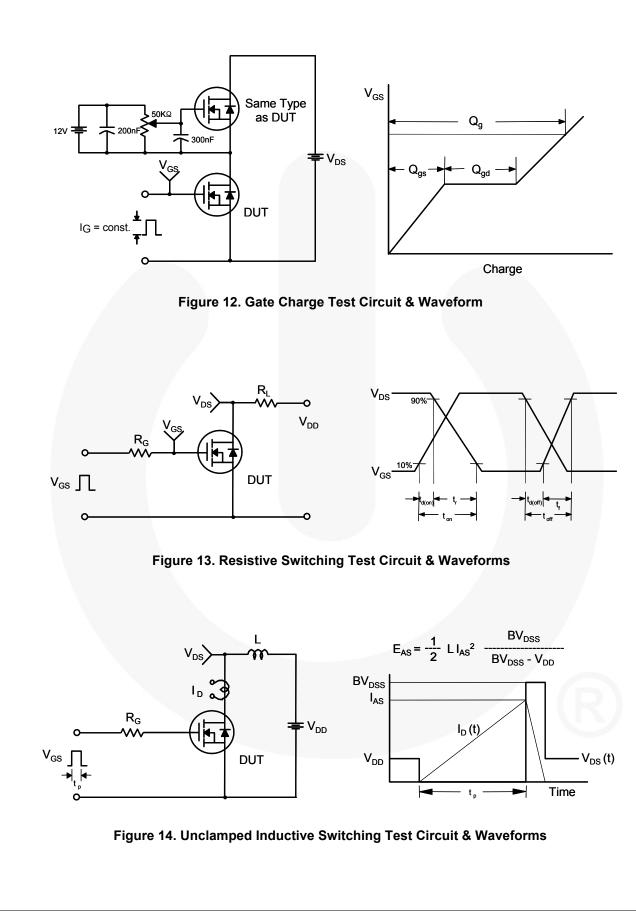
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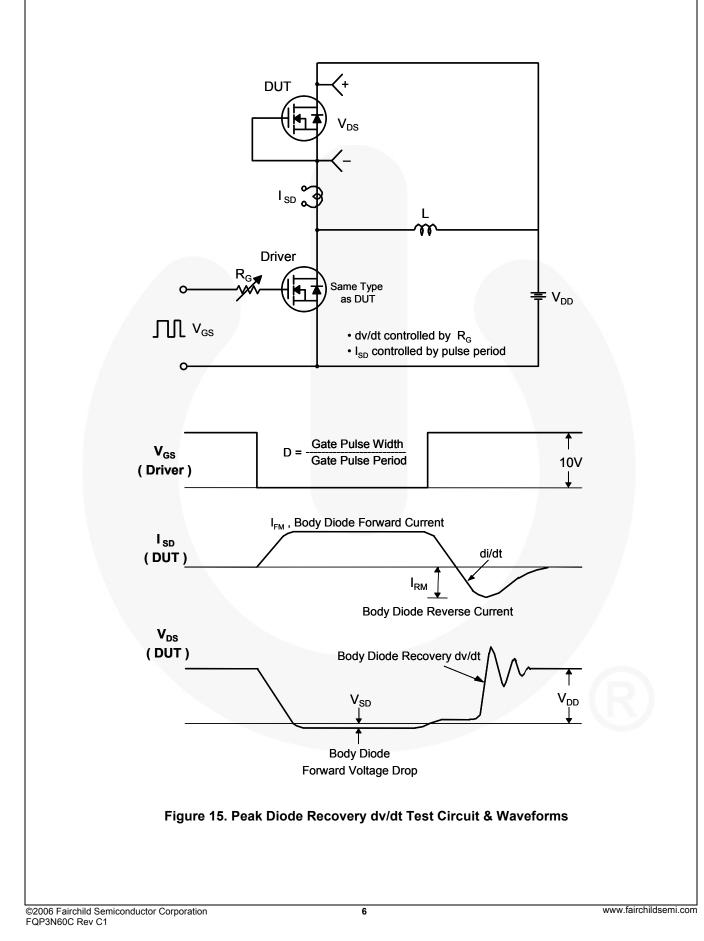
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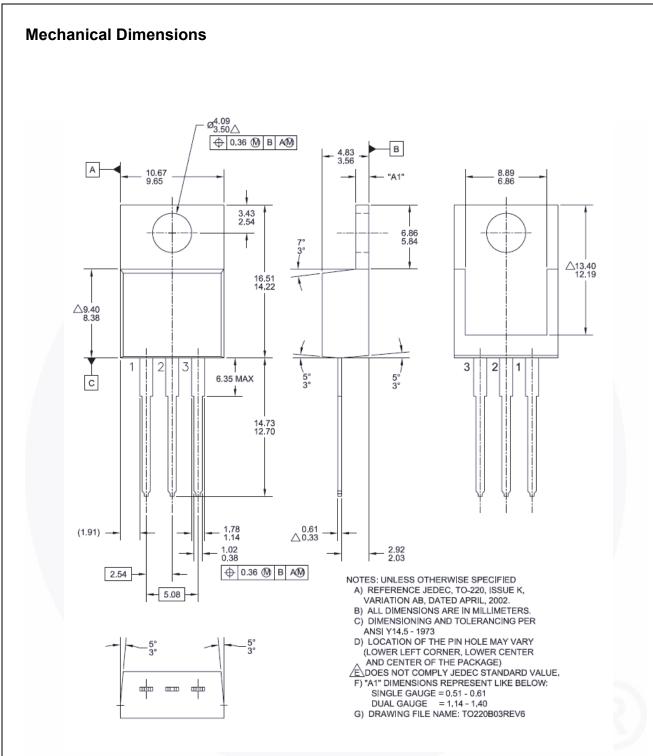


Figure 16. TO-220, Molded, 3-Lead, Jedec Variation AB

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