### FAIRCHILD

SEMICONDUCTOR®

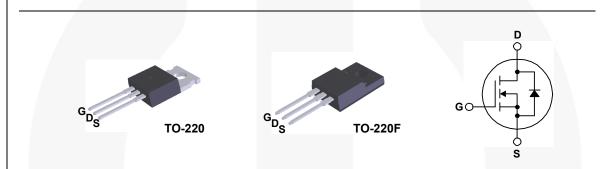
## FQP9N90C / FQPF9N90C **N-Channel QFET® MOSFET** 900 V, 8.0 A, 1.4 Ω

#### Description

This N-Channel enhancement mode power MOSFET is • 8 A, 900 V, R<sub>DS(on)</sub> = 1.4 Ω (Max.) @ V<sub>GS</sub> = 10 V, produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state • Low Gate Charge (Typ. 45 nC) resistance, and to provide superior switching performance . Low Crss (Typ. 14 pF) and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power • 100% Avalanche Tested factor correction (PFC), and electronic lamp ballasts.

#### Features

- $I_D = 4 A$



#### Absolute Maximum Ratings T<sub>c</sub> = 25°C unless otherwise noted.

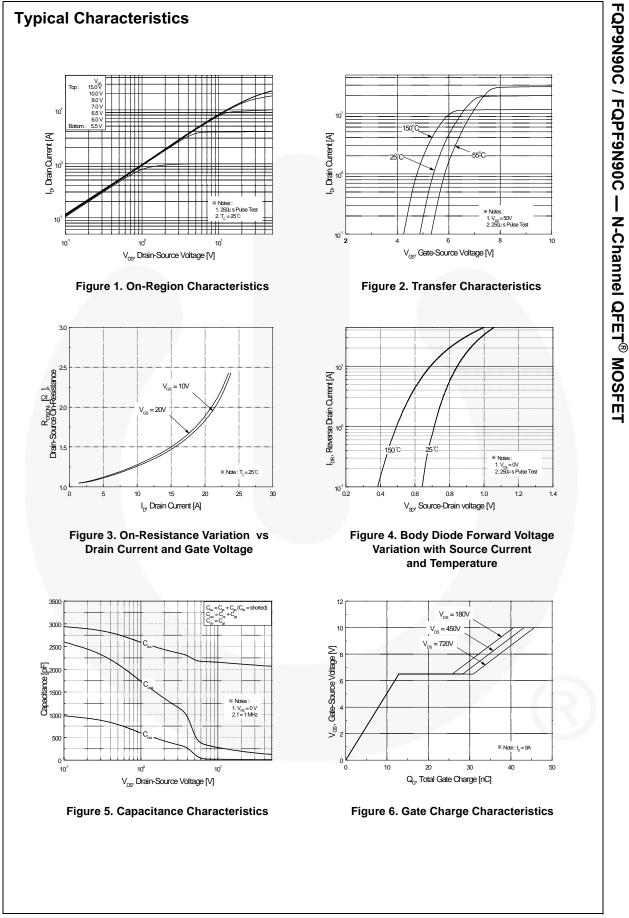
Symbol	Parameter		FQP9N90C FQPF9N90CT		Unit
V <sub>DSS</sub>	Drain-Source Voltage	g	V		
I <sub>D</sub>	Drain Current - Continuous ( $T_C = 25^{\circ}C$ )		8.0	8.0 *	А
	- Continuous (T <sub>C</sub> = 100°C)	-	2.8	2.8 *	А
DM	Drain Current - Pulsed	(Note 1)	32	32 *	А
V <sub>GSS</sub>	Gate-Source Voltage	±	V		
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2		9	mJ	
AR	Avalanche Current	(Note 1)	8.0		А
E <sub>AR</sub>	Repetitive Avalanche Energy (Note		2	mJ	
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.0		V/ns
P <sub>D</sub>	Power Dissipation ( $T_C = 25^{\circ}C$ )	205	68	W	
	- Derate above 25°C	1.64	0.54	W/°C	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range	-55 te	°C		
TL	Maximum lead temperature for soldering, 1/8" from case for 5 seconds	3	°C		

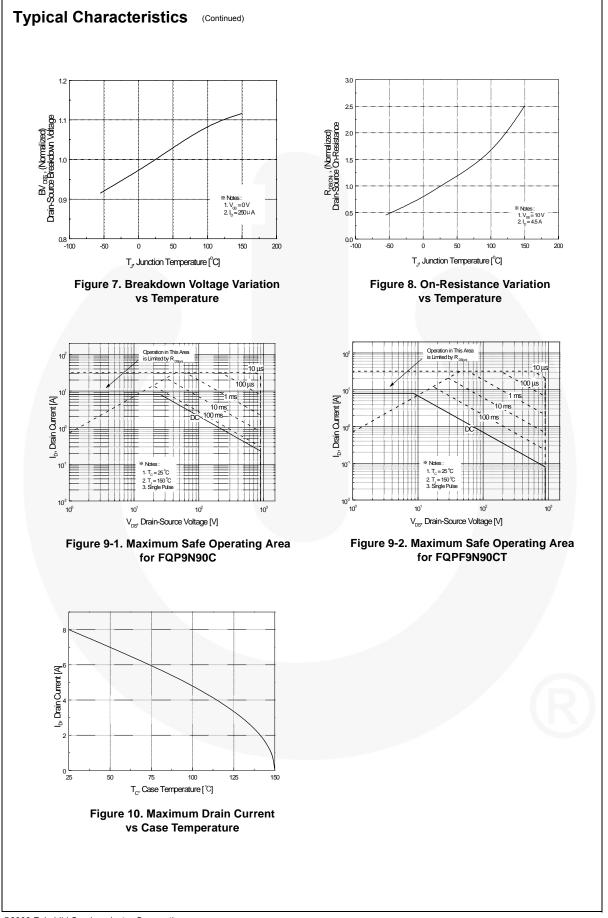
\* Drain current limited by maximum junction temperature

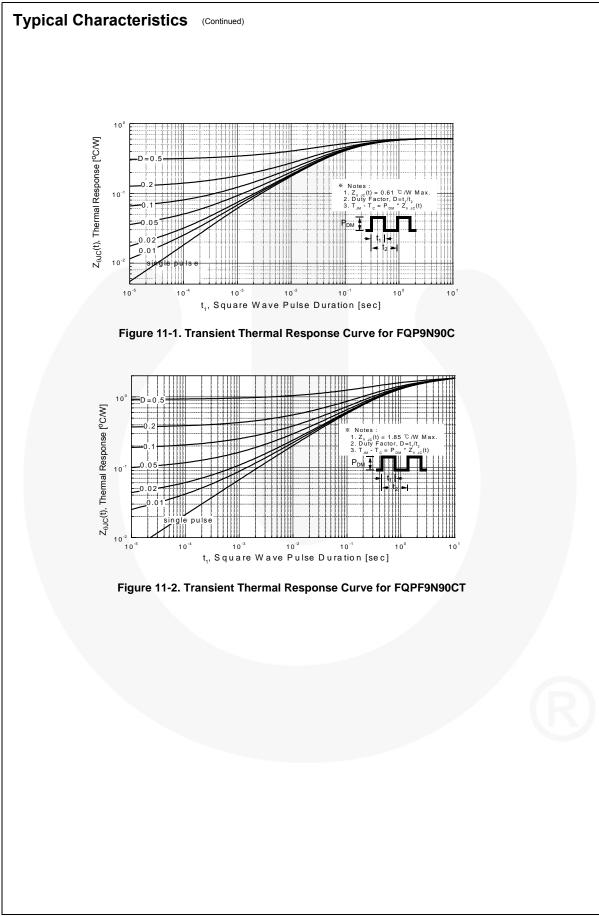
#### **Thermal Characteristics**

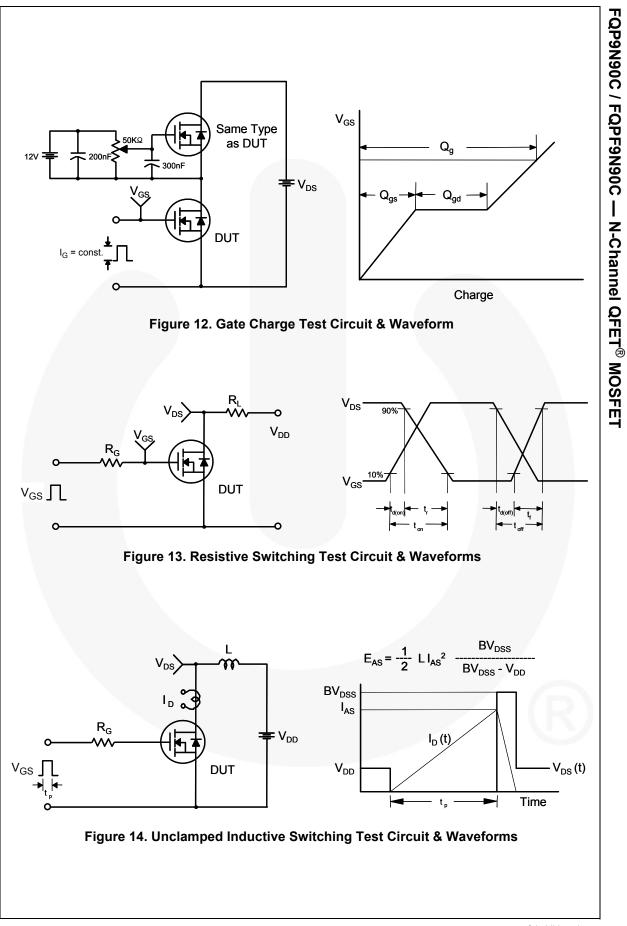
Symbol	Parameter	FQP9N90C	FQPF9N90CT	Unit °C/W	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	0.61	1.85		
$R_{\theta JS}$	Thermal Resistance, Case-to-Sink Typ, Max.	0.5		°C/W	
$R_{\thetaJA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	62.5	°C/W	

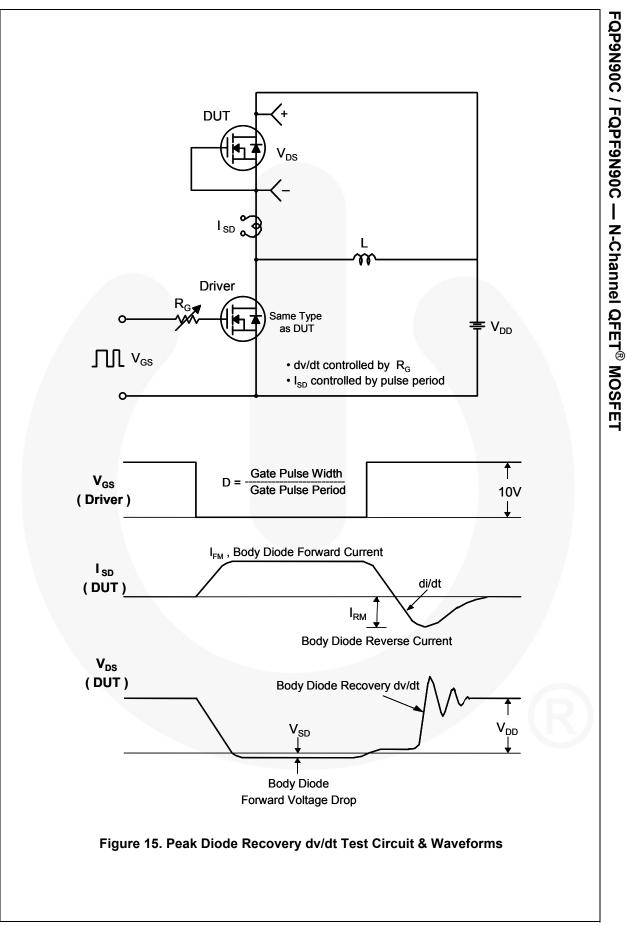
-		Top Mark	Package		Packing Method Reel		Size	Tape Width N/A		Quantity 50 units	
		FQP9N90C	TO-2	220 Tube N/A		ł					
		20F Tube N/		Ą	N/A		50 units				
	al Cha	racteristics	T <sub>C</sub> = 25°C	unless ot	herwise noted.			-		1	
Symbol		Parameter			Test Conditions		Min.	Тур.	Max.	Unit	
Off Cha	racteris	tics									
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage		V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA		900			V			
BV <sub>DSS</sub>	Breakdown Voltage Temperature										
$/\Delta T_J$	Coefficier	<b>o</b>		$I_D = 250 \ \mu$ A, Referenced to 25°C				0.99		V/°C	
DSS	1			V <sub>DS</sub> = 900 V, V <sub>GS</sub> = 0 V				10	μA		
200	Zero Gate Voltage Drain Current		V <sub>DS</sub> = 720 V, T <sub>C</sub> = 125°C					10	μA		
GSSF	Gate-Bod	Gate-Body Leakage Current, Forward		V <sub>GS</sub> = 30 V, V <sub>DS</sub> = 0 V					100	nA	
GSSR		Gate-Body Leakage Current, Reverse		$V_{GS} = -30 \text{ V}, V_{DS} = 0 \text{ V}$					-100	nA	
		••••									
Jn Cna / <sub>GS(th)</sub>	Gate Thre	eshold Voltage	-	Vpe =	• V <sub>GS</sub> , I <sub>D</sub> = 250 μA		3.0		5.0	V	
RDS(on)	Static Dra	iin-Source	-	_	= 10 V, I <sub>D</sub> = 4 A			1.12	1.4	Ω	
		On-Resistance Forward Transconductance		V <sub>DS</sub> = 40 V, I <sub>D</sub> = 4 A				9.2		S	
IFS	1 of Ward		_	103				0.2			
-	ic Chara	cteristics								-	
Piss	Input Cap	acitance	_	V <sub>DS</sub> =	= 25 V, V <sub>GS</sub> = 0 V,	= 0 V,		2100	2730	pF	
Soss	Output Ca	Capacitance		f = 1.0 MHz				175	230	pF	
Srss	Reverse	Transfer Capacitance	9				14	18	pF		
Switchi	ng Char	acteristics									
d(on)	Turn-On I	-On Delay Time					50	110	ns		
	Turn-On I	Rise Time			$V_{DD} = 450 \text{ V}, \text{ I}_{D} = 9.0 \text{ A},$ R <sub>G</sub> = 25 $\Omega$			120	250	ns	
d(off)	Turn-Off I	Delay Time		NG -	20 32			100	210	ns	
:	Turn-Off I	all Time				(Note 4)		75	160	ns	
λ <sub>g</sub>	Total Gate	e Charge		V <sub>De</sub> =	= 720 V, I <sub>D</sub> = 9.0A,			45	58	nC	
λ <sub>gs</sub>		rce Charge		V <sub>GS</sub> =				13		nC	
λ <sub>gd</sub>	Gate-Dra	-				(Note 4)		18		nC	
										/	
Drain-S	ource D	iode Characteri	stics ar	nd Ma	ximum Ratings						
S	Maximum	Continuous Drain-S	ource Dic	de For	ward Current				8.0	А	
SM	Maximum	Pulsed Drain-Sourc	e Diode F	orward	Current				32.0	Α	
/ <sub>SD</sub>	Drain-Sou	urce Diode Forward	/oltage		= 0 V, I <sub>S</sub> = 8 A				1.4	V	
rr	Reverse I	Recovery Time			= 0 V, I <sub>S</sub> = 9 A,			550		ns	
۲ <sup>ر</sup>	Reverse	Recovery Charge		dl <sub>F</sub> / c	lt = 100 Α/μs			6.5		μC	
otes: Repetitive ra	ating : pulse-wi	dth limited by maximum jun	ction temper	ature.						0	

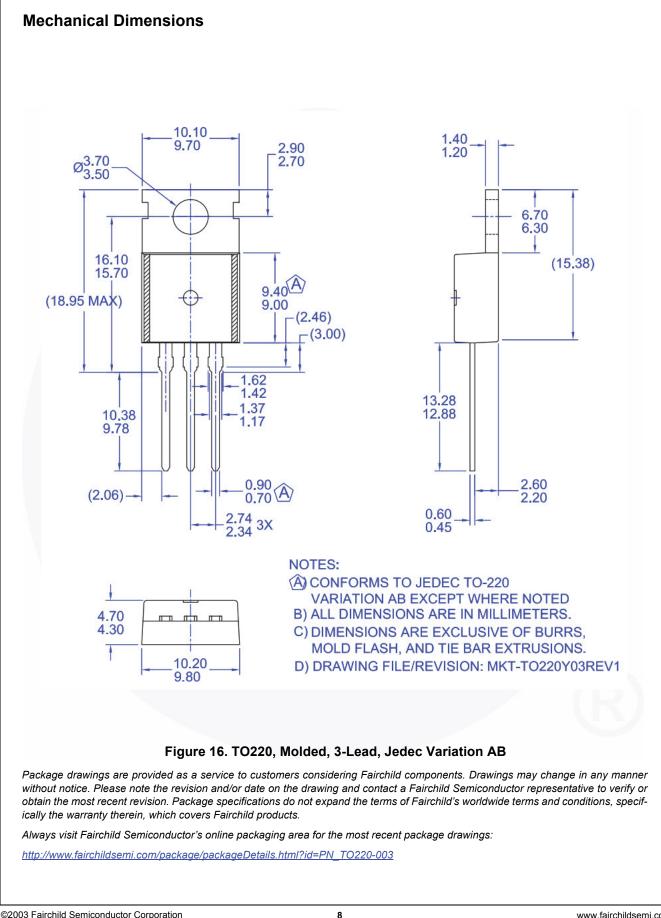


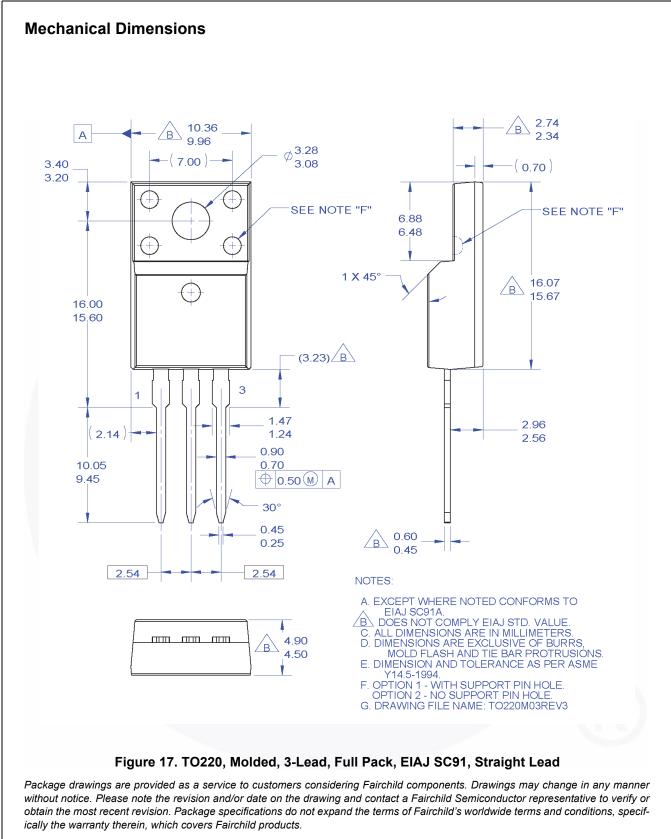












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FQP9N90C / FQPF9N90C ---

N-Channel QFET<sup>®</sup> MOSFET



Rev. 166

Preliminary

No Identification Needed

Obsolete

First Production

Full Production

Not In Production

notice to improve design.

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