

### November 2013

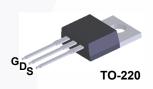
## **FQP11N40C / FQPF11N40C** N-Channel QFET<sup>®</sup> MOSFET 400 V, 10.5 A, 530 mΩ

## Features

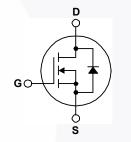
- + 10.5 A, 400 V,  $\rm R_{DS(on)}$  = 530 m $\Omega$  (Max.) @  $\rm V_{GS}$  = 10 V,  $\rm I_{D}$  = 5.25 A
- Low Gate Charge (Typ. 28 nC)
- Low Crss (Typ. 85 pF)
- 100% Avalanche Tested

## Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.







## MOSFET Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted.

Symbol	Parameter			FQP11N40C	FQPF11N40C	Unit
V <sub>DSS</sub>	Drain to Source Voltage			4	V	
	Durain Querrant	-Continuous (T <sub>C</sub> = 25 <sup>o</sup> C)	-Continuous (T <sub>C</sub> = 25 <sup>o</sup> C)		10.5 *	А
I <sub>D</sub>	Drain Current	-Continuous ( $T_C = 100^{\circ}C$ )		6.6	6.6 *	А
I <sub>DM</sub>	Drain Current	- Pulsed	(Note 1)	42	42 *	А
V <sub>GSS</sub>	Gate to Source Voltage	1		±	V	
E <sub>AS</sub>	Single Pulsed Avalanche Energy		(Note 2)	360		mJ
I <sub>AR</sub>	Avalanche Current		(Note 1)	11		А
E <sub>AR</sub>	Repetitive Avalanche Energy		(Note 1)	13.5		mJ
dv/dt	Peak Diode Recovery dv/dt (N		(Note 3)	4.5		V/ns
P <sub>D</sub>	David Dia dia atian	(T <sub>C</sub> = 25°C)		135	44	W
	Power Dissipation	- Derate above 25°C		1.07	0.35	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range			-55 to +150		°C
TL	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds			3	°C	
*Drain current li	mited by maximum junction t					

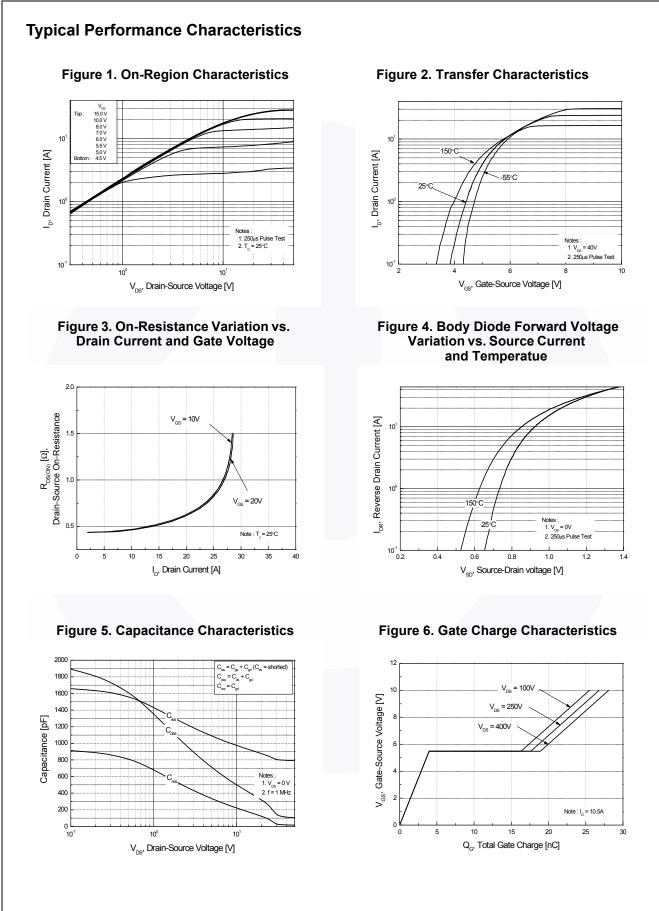
## **Thermal Characteristics**

Symbol	Parameter	FQP11N40C	FQPF11N40C	Unit	
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max	0.93	2.86	°C/W	
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient, Max	62.5	62.5	°C/W	

Device Marking Device   FQP11N40C FQP11N40C   FQPF11N40C FQPF11N40C		Device		Package	Re	el Size	Tape Widtl	ו Qu	antity	
		FQP11N40C		TO-220	-	Tube	N/A		50 units	
		TO-220F Tu		Tube	N/A	50	50 units			
lectri	cal Charact	eristics T <sub>C</sub> = 25°C ur	nless other	wise noted.						
Symbol	1	arameter		Test Conditions		Min	Тур	Max	Unit	
Off Cha	aracteristics									
BV <sub>DSS</sub>	1	eakdown Voltage	$V_{GS} = 0$	V, I <sub>D</sub> = 250 μA		400			V	
$\Delta BV_{DSS}$ / $\Delta T_J$		age Temperature Coeffi-		μA, Referenced to	to 25°C		0.54		V/°C	
	Zero Gate Voltage Drain Current		V <sub>DS</sub> = 40	0 V, V <sub>GS</sub> = 0 V				1	μA	
I <sub>DSS</sub>			$V_{\rm DS} = 320 \text{ V}, \text{ T}_{\rm C} = 125^{\circ}\text{C}$					10	μΑ	
I <sub>GSSF</sub>	Gate-Body Leak	age Current, Forward	$V_{GS} = 30 \text{ V}, V_{DS} = 0 \text{ V}$					100	nA	
GSSR		age Current, Reverse		0 V, V <sub>DS</sub> = 0 V				-100	nA	
On Cha	racteristics									
V <sub>GS(th)</sub>	Gate Threshold	Voltage	$V_{DS} = V_0$	<sub>GS</sub> , I <sub>D</sub> = 250 μA		2.0		4.0	V	
R <sub>DS(on)</sub>	Static Drain-Sou On-Resistance	•	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 5.25 \text{ A}$			0.43	0.53	Ω		
9 <sub>FS</sub>	Forward Transco	onductance	V <sub>DS</sub> = 40	V, I <sub>D</sub> = 5.25 A			7.1		S	
Dynam	ic Characteris	stics								
C <sub>iss</sub>	Input Capacitance			5 V, V <sub>GS</sub> = 0 V,			840	1090	pF	
C <sub>oss</sub>	Output Capacita	nce	f = 1.0 MHz			250	325	pF		
C <sub>rss</sub>	Reverse Transfe	er Capacitance					85	110	pF	
Switch	ing Character	istics								
t <sub>d(on)</sub>	Turn-On Delay T		$V_{DD}$ = 200 V, I <sub>D</sub> = 10.5 A, R <sub>G</sub> = 25 $\Omega$			14	40	ns		
t <sub>r</sub>	Turn-On Rise Ti	ne				89	190	ns		
t <sub>d(off)</sub>	Turn-Off Delay T	ĩme				81	170	ns		
t <sub>f</sub>	Turn-Off Fall Tim	ne			(Note 4)		81	170	ns	
Q <sub>g</sub>	Total Gate Charg	ge	$V_{DS} = 320 \text{ V}, \text{ I}_{D} = 10.5 \text{ A},$ $V_{GS} = 10 \text{ V}$ (Note 4)			28	35	nC		
Q <sub>gs</sub>	Gate-Source Ch						4		nC	
Q <sub>gd</sub>	Gate-Drain Char					15		nC		
		Charactoristics and	Maxim	um Patings				/		
l <sub>s</sub>		Characteristics and nuous Drain-Source Diode						10.5	A	
's I <sub>SM</sub>		d Drain-Source Diode For						42	A	
SM V <sub>SD</sub>		ode Forward Voltage		V, I <sub>S</sub> = 10.5 A				1.4	V	
∙sD t <sub>rr</sub>	Reverse Recove	0		v, is = 10.5 A V, is = 10.5 A,			290		ns	
		,		v, i <sub>S</sub> – 10.5 A, 100 A/μs			2.4		μC	
Q <sub>rr</sub>	Reverse Recove	a y Ghaige		100 10 μ3			2.7		μΟ	

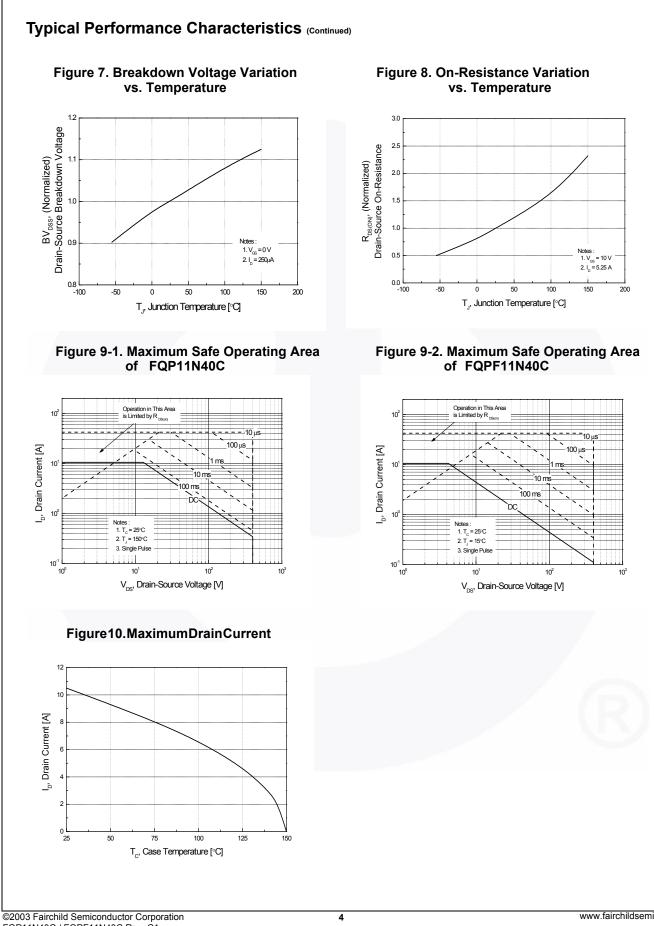
4. Essentially independent of operating temperature.

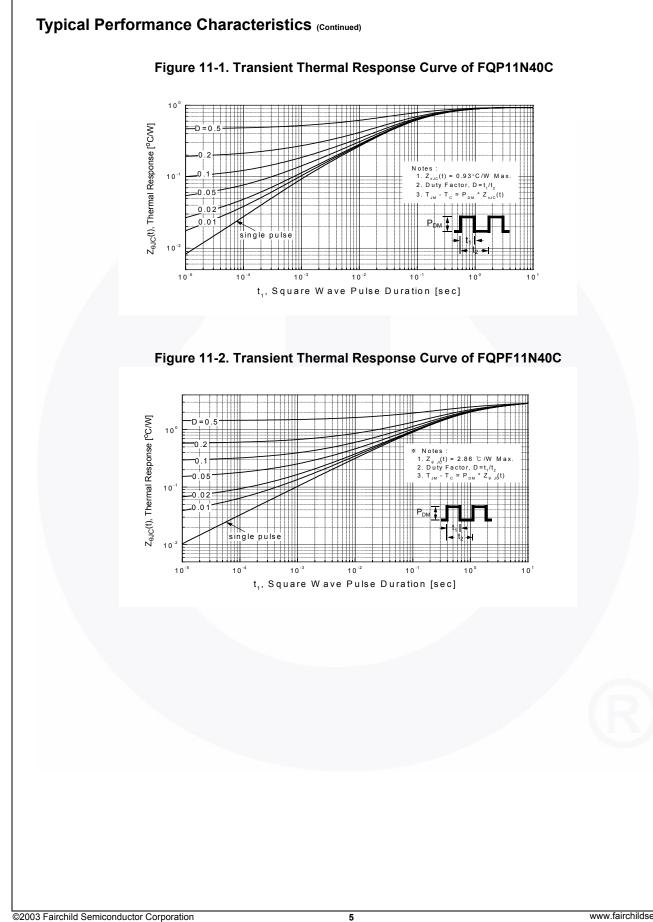
FQP11N40C / FQPF11N40C — N-Channel QFET<sup>®</sup> MOSFET



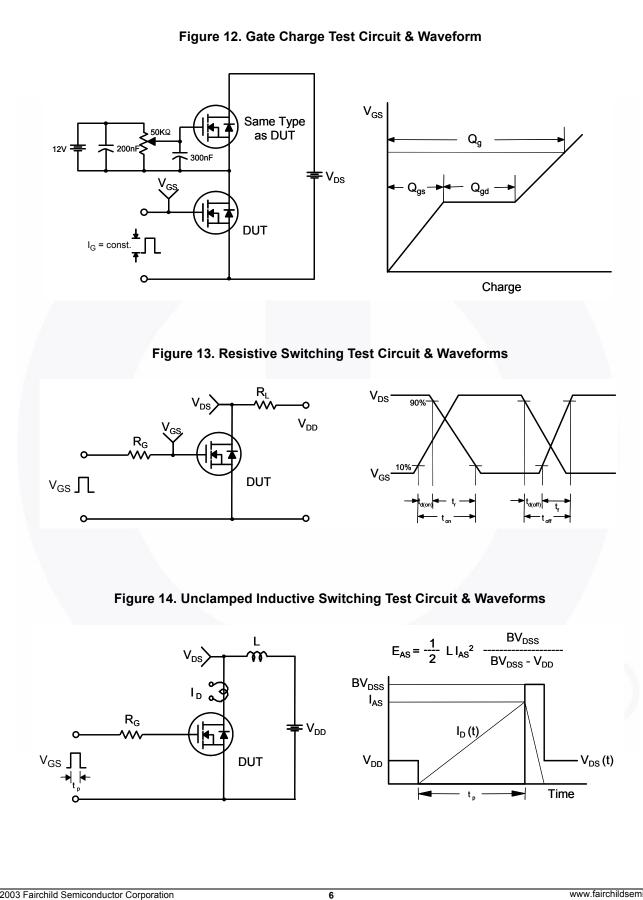
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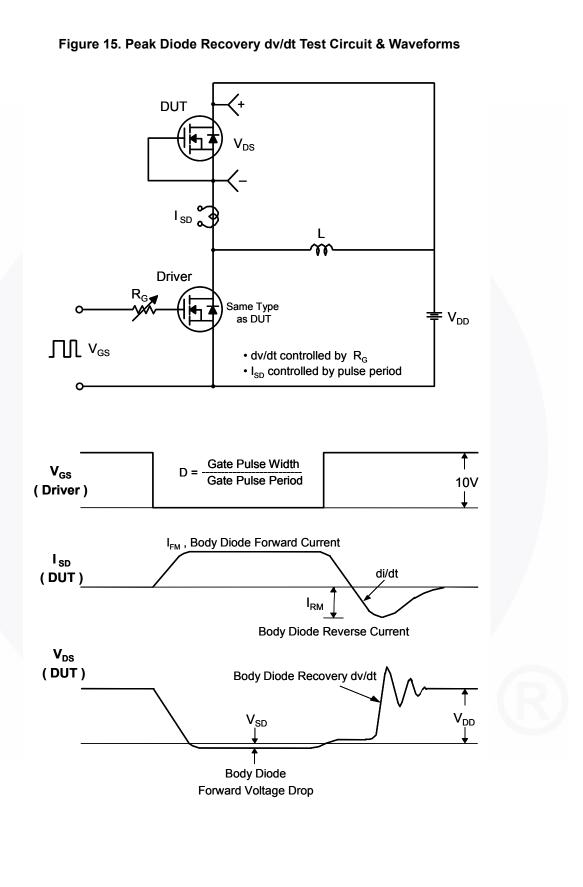


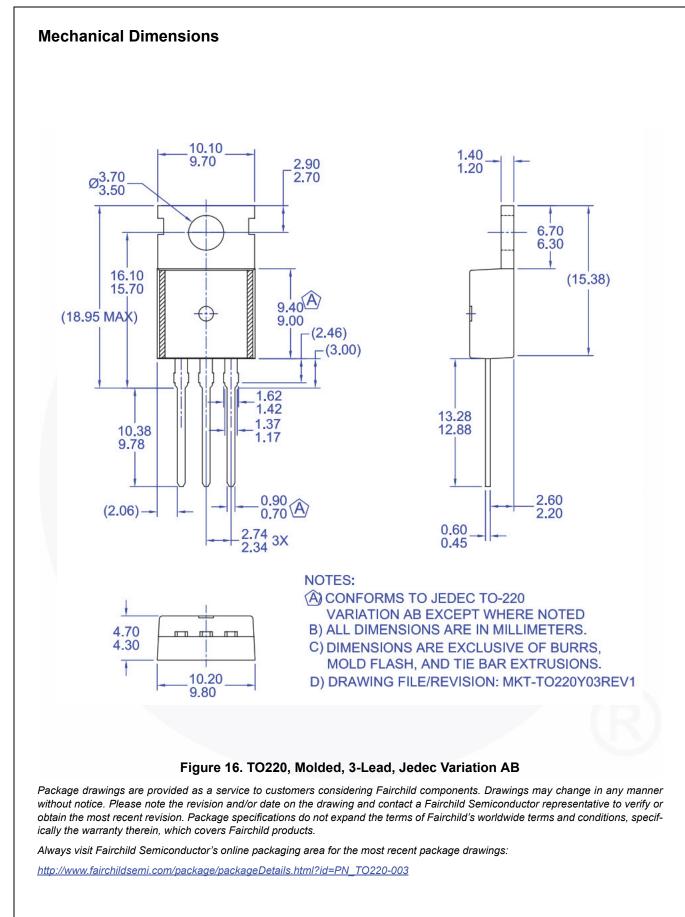


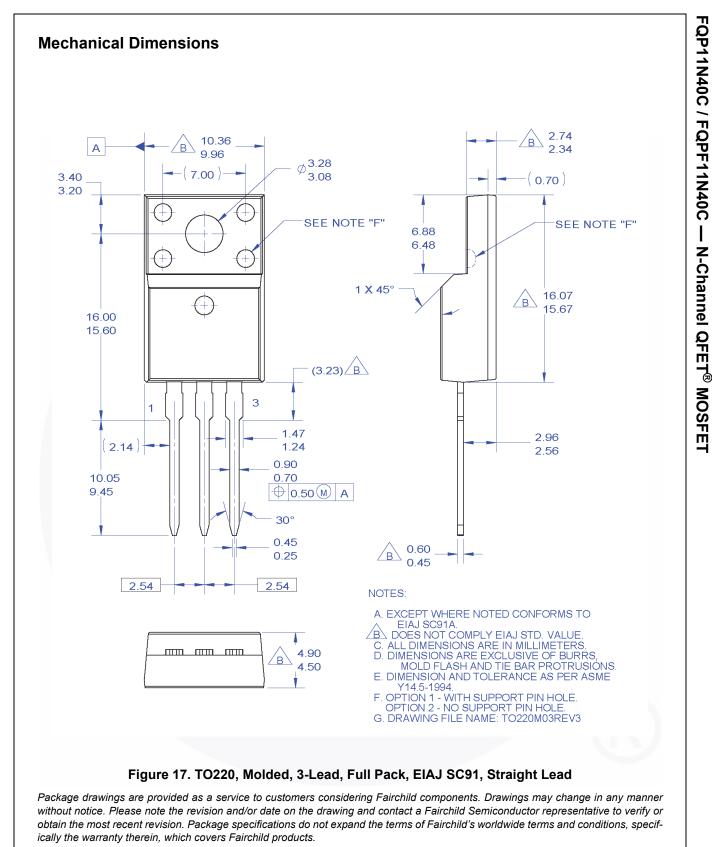


FQP11N40C / FQPF11N40C — N-Channel QFET<sup>®</sup> MOSFET









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