

November 2013

FQPF20N06

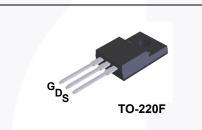
N-Channel QFET[®] MOSFET 60 V, 15 A, 60 m Ω

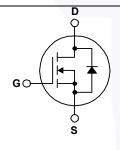
Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

Features

- 15 A, 60 V, $R_{DS(on)}$ = 60 m Ω (Max.) @ V_{GS} = 10 V, I_D = 7.5 A
- Low Gate Charge (Typ. 11.5 nC)
- Low Crss (Typ. 25 pF)
- 100% Avalanche Tested
- 175°C Maximum Junction Temperature Rating





Absolute Maximum Ratings T_c = 25°C unless otherwise noted.

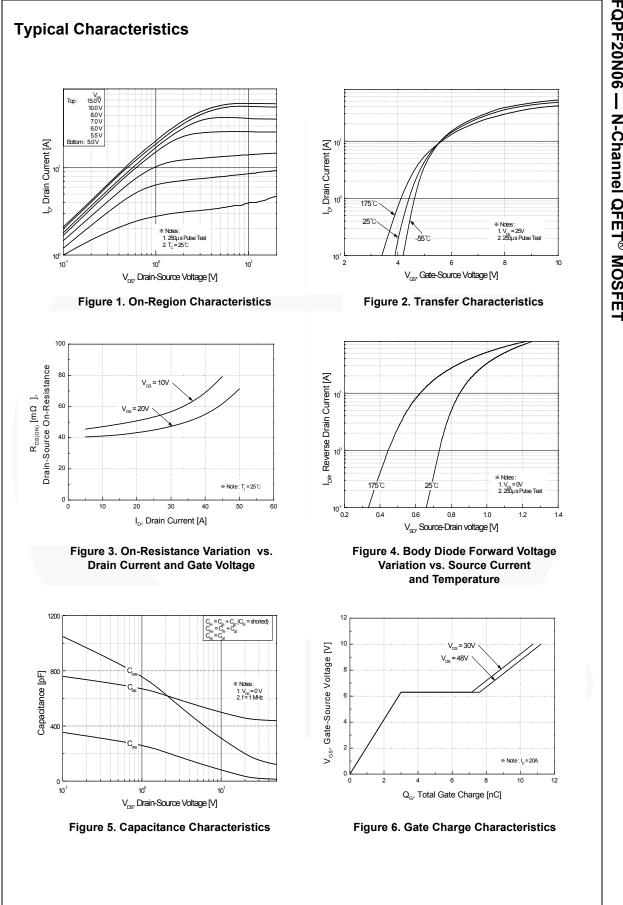
Symbol	Parameter	FQPF20N06	Unit	
V _{DSS}	Drain-Source Voltage		60	V
I _D	Drain Current - Continuous ($T_C = 25^\circ$	C)	15	A
	- Continuous (T _C = 100	°C)	10.7	A
DM	Drain Current - Pulsed	(Note 1)	60	A
V _{GSS}	Gate-Source Voltage		± 25	V
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	155	mJ
AR	Avalanche Current	(Note 1)	15	А
E _{AR}	Repetitive Avalanche Energy	(Note 1)	3.0	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	7.0	V/ns
P _D	Power Dissipation ($T_C = 25^{\circ}C$)		30	W
	- Derate above 25°C		0.2	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +175	°C
TL	Maximum Lead Temperature for Solderin	g,	300	°C
۰L	1/8" from Case for 5 seconds		000	Ŭ

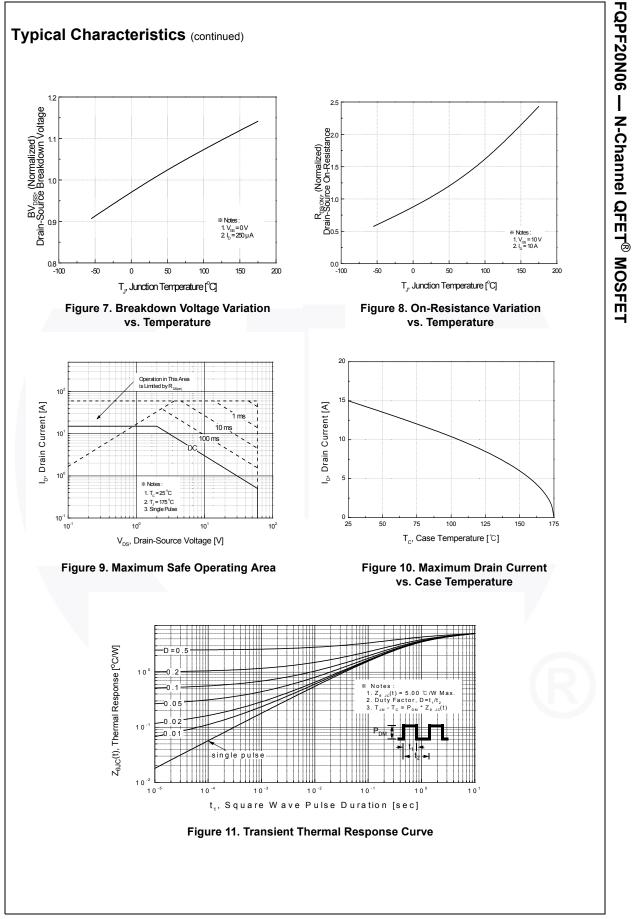
Thermal Characteristics

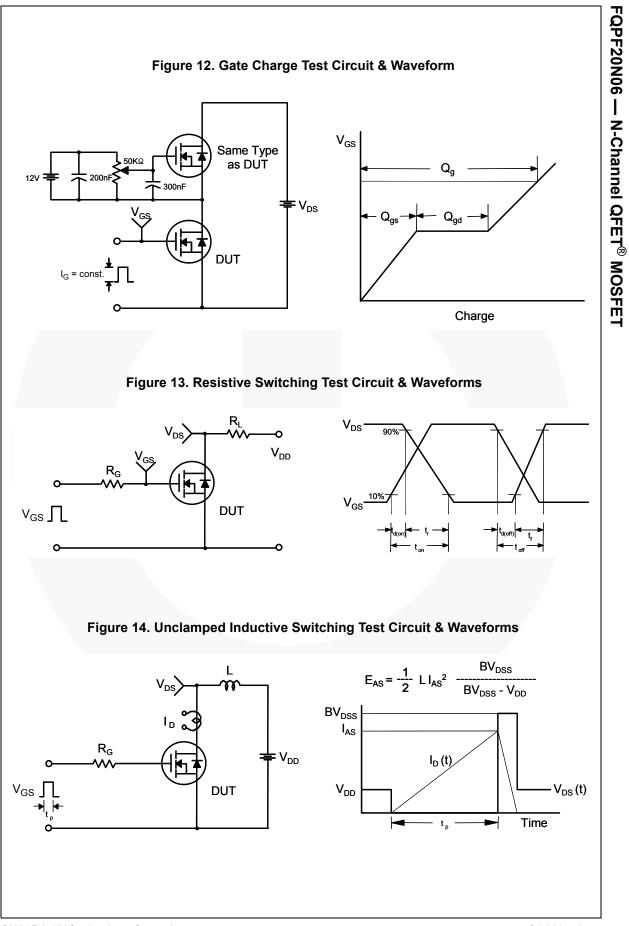
Symbol	Parameter	FQPF20N06	Unit	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	5.00	°C/W	
R_{\thetaJA}	Thermal Resistance, Junction-to-Ambient, Max.	62.5	°C/W	

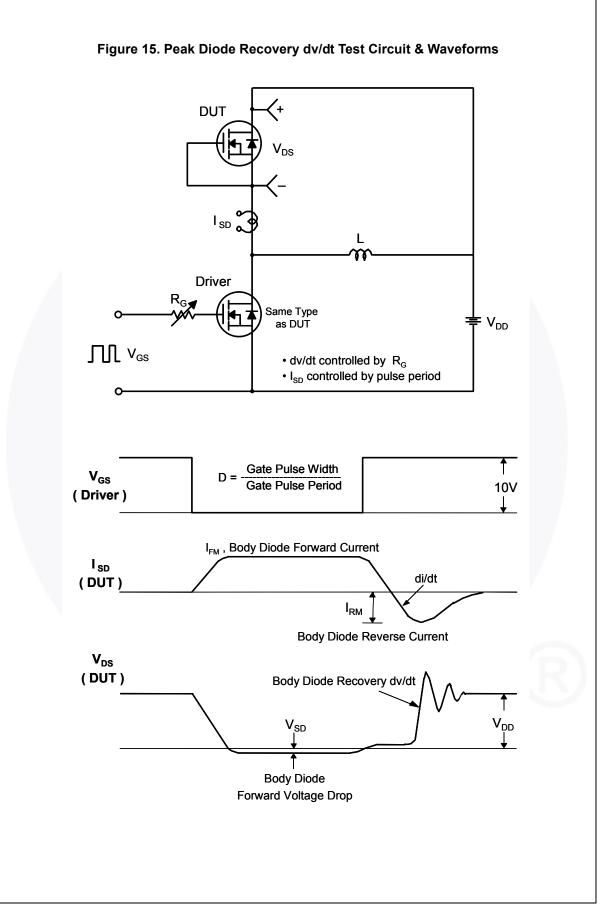
Part NumberTop MarkPackageFQPF20N06FQPF20N06TO-220F		Packing Method	Reel Size	Tape Width		h Q	Quantity		
		Tube N/A		N/A		5	50 units		
lectri	cal Cl	naracteristics	T _C = 25°C	unless otherwise noted.					
Symbol		Parameter		Test Condit	ions	Min	Тур	Max	Unit
Off Cha				(1 - 0)(1 - 0)(0)	. A				
BV _{DSS}	Drain-Source Breakdown Voltage		$V_{GS} = 0 V, I_D = 250 \mu A$		60			V	
ΔBV _{DSS} ΔT _J		Breakdown Voltage Temperature Coefficient		I_D = 250 µA, Referenced to 25°C			0.07		V/°C
DSS	Zero (ate Voltage Drain Cu	rrent	V_{DS} = 60 V, V_{GS} = 0				1	μA
	Zero C	ale voltage Drain Cu	nem	V _{DS} = 48 V, T _C = 150				10	μA
GSSF	Gate-E	Body Leakage Curren	t, Forward	V_{GS} = 25 V, V_{DS} = 0				100	nA
GSSR	Gate-E	Body Leakage Curren	t, Reverse	V_{GS} = -25 V, V_{DS} = 0) V			-100	nA
On Cha	aracter	istics							
V _{GS(th)}	Gate T	hreshold Voltage		$V_{DS} = V_{GS}, I_D = 250$	μΑ	2.0		4.0	V
R _{DS(on)}		Drain-Source sistance		$V_{GS} = 10 \text{ V}, I_{D} = 7.5 \text{ A}$			0.048	0.06	Ω
9 _{FS}		rd Transconductance		V _{DS} = 25 V, I _D = 7.5	A		10		S
Dynam C _{iss}	ic Characteristics		V		450	590	pF		
C _{oss}		Capacitance		$V_{DS} = 25 V, V_{GS} = 0$ f = 1.0 MHz	v,		170	220	pF
C _{rss}		se Transfer Capacitar	ice	I = 1.0 MHz			25	35	pF
							1		
Switch	_ <u> </u>	aracteristics							
t _{d(on)}		n Delay Time		V _{DD} = 30 V, I _D = 10 Å	۹,		5	20	ns
r		n Rise Time		R _G = 25 Ω			45	100	ns
d(off)		Off Delay Time			(Note 4)		20	50	ns
f		off Fall Time			(Note 4)		25	60	ns
Qg		Bate Charge		V _{DS} = 48 V, I _D = 20 A	۹,		11.5	15	nC
Q _{gs}		Source Charge		V _{GS} = 10 V			3		nC
Q _{gd}	Gate-L	Drain Charge			(Note 4)		4.5		nC
Drain-S	Source	Diode Characte	ristics an	d Maximum Rati	ings				
I _S	Maxim	um Continuous Drain	-Source Dio	de Forward Current				15	Α
	Maxim	um Pulsed Drain-Sou	Irce Diode F	orward Current				60	Α
	Drain_	Source Diode Forwar	d Voltage	V_{GS} = 0 V, I _S = 15 A				1.5	V
I _{SM}	Diam-			$V_{GS} = 0 V, I_{S} = 20 A,$			43		ns
I _{SM} V _{SD} t _{rr}		se Recovery Time		$dI_{\rm F}$ / dt = 100 A/µs					

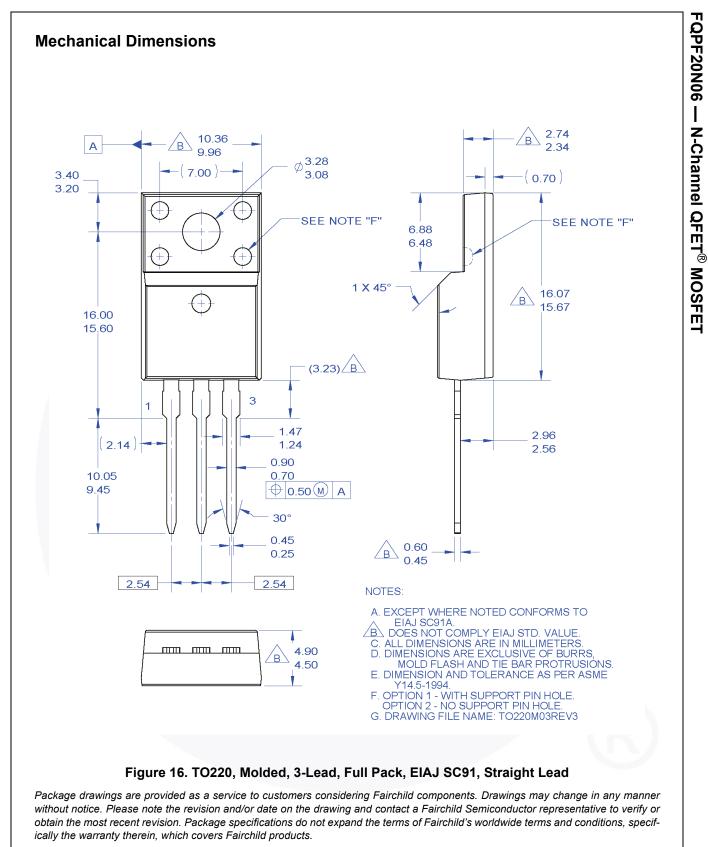
FQPF20N06 — N-Channel QFET[®] MOSFET











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