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March 2014

FSA4157, FSA4157A Low-Voltage, 1 Ω SPDT Analog Switch

Features

- FSA4157A Features Lower I_{CC} when the S Input is Lower Than V_{CC}
- Maximum 1.15 Ω On Resistance (R_{ON}) at 4.5 V V_{CC}
- 0.3 Ω Maximum R_{ON} Flatness at 4.5 V V_{CC}
- Space-Saving 6-lead, MicroPak[™] and SC70 6 Packages
- Broad V_{CC} Operating Range:
 FSA4157: 1.65 V to 5.5 V
 FSA4157A: 2.7 V to 5.5 V
- Fast Turn-On and Turn-Off Time
- Break-Before-Make Enable Circuitry
- Over-Voltage Tolerant TTL-Compatible Control Circuitry

Description

FSA4157 and FSA4157A are high performance Single Pole/Double Throw (SPDT) analog switches. Both devices feature ultra low R_{ON} of 1.15 Ω maximum at 4.5 V V_{CC} and operates over the wide V_{CC} range of 1.65 V to 5.5 V for FSA4157, and 2.7 V to 5.5 V for FSA4157A. The device is fabricated with sub-micron CMOS technology to achieve fast switching speeds and is designed for break-before-make operation. The select input is TTL level compatible.

The FSA4157A features very low quiescent current even when the control voltage is lower than the $V_{\rm CC}$ supply. This feature services the mobile handset applications very well allowing for the direct interface with baseband processor general purpose I/Os.

Ordering Information

Part Number	Top Mark	Package Description	Packing Method
FSA4157P6X	A57	6-Lead SC70, EIAJ SC88, 1.25 mm Wide	3000 Units Tape and Reel
FSA4157L6X	EG	6-Lead MicroPak,™ 1.0 mm Wide	5000 Units Tape and Reel
FSA4157AP6X	B57	6-Lead SC70, EIAJ SC88, 1.25 mm Wide	3000 Units Tape and Reel
FSA4157AL6X	EU	6-Lead MicroPak™, 1.0 mm Wide	5000 Units Tape and Reel

Pin Configurations

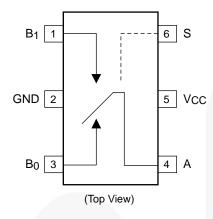


Figure 1. SC70 Pin Assignments

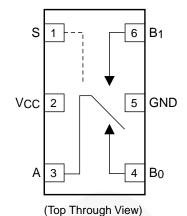


Figure 2. MicroPak™ Pin Assignments

Pin Definitions

Pin# SC70	Pin# MicroPak™	Name	Description
1	6	B1	Data Ports
2	5	GND	Ground
3	4	B0	Data Ports
4	3	А	Data Ports
5	2	V _{CC}	Supply Voltage
6	1	S	Control Input

Truth Table

Control Input (S)	Function
Low	B0 connected to A
High	B1 connected to A

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Paramete	er	Min.	Max.	Unit
V _{CC}	Supply Voltage	-0.5	6.0	V	
Vs	DC Switch Voltage ⁽¹⁾		-0.5	V _{CC} + 0.5	V
V _{IN}	DC Input Voltage ⁽¹⁾		-0.5	6.0	V
I _{IK}	DC Input Diode Current		-50		mA
I _{SW}	Switch Current		200	mA	
I _{SWPEAK}	Peak Switch Current (Pulse at 1 ms du		400	mA	
В	Dower Dissipation at 95°C	SC70		180	mW
P _D	Power Dissipation at 85°C	MicroPak™		100	IIIVV
T _{STG}	Storage Temperature Range		-65	+150	°C
TJ	Maximum Junction Temperature		+150	°C	
TL	Lead Temperature (Soldering, 10 second		+260	°C	
ESD	Electrostatic Discharge Capability	Human Body Model, JESD22-A114 (FSA4157A)		7500	V

Note:

1. Input and output negative ratings may be exceeded if input and output diode current ratings are observed.

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Min.	Max.	Unit		
V	Cumply Voltage	FSA4157	1.65	5.50	V	
V_{CC}	Supply Voltage	FSA4157A	2.7	5.5	V	
V _{CNTRL}	Control Input Voltage ⁽²⁾	0	V _{CC}	V		
V _{SW}	Switch Input Voltage	0	V _{CC}	V		
T _A	Operating Temperature			+85	°C	
0	Thermal Resistance in Still Air	SC70		350	°C/W	
$\theta_{\sf JA}$	Thermal Resistance in Still All	MicroPak™ (Estimated)		330	C/VV	

Note:

2. Control input must be held HIGH or LOW and it must not float.

DC Electrical Characteristics

Typical values are at 25°C unless otherwise specified.

				Ambient Temperature					
Symbol	Parameter	Conditions	V _{cc} (V)	-25°			-40 to +85°C		Unit
				Min.	Тур.	Max.	Min.	Max.	
		FSA4157 Only	1.8 to 2.7				1.0		
V_{IH}	V _{IH} Input Voltage High		2.7 to 3.6				2.0		V
		4.5 to 5.5				2.4			
		FSA4157 Only	1.8 to 2.7					0.4	
\/	Input Voltage Low	FSA4157A Only	2.7 to 3.6					0.4	V
V_{IL}	Input Voltage Low		2.7 to 3.6					0.6	V
			4.5 to 5.5					0.8	
	Control Input	\/ 0\/ to\/	2.7 to 3.6				-1.0	1.0	
I _{IN}	Leakage	$V_{IN}=0 V \text{ to } V_{CC}$	4.5 to 5.5				-1.0	1.0	μΑ
I _{NO(OFF)} , I _{NC(OFF)}	Off Leakage Current of Port B0 and B1	A=1 V, 4.5 V, B ₀ or B ₁ =4.5, 1 V	5.5		±2		-20	20	nA
I _{A(ON)}	On Leakage Current of Port A	A=1 V, 4.5v, B ₀ or B ₁ =4.5, 1 V,4.5 V or Floating	5.5		±4		-40	40	nA
D	Switch On	I _{OUT} =100 mA, B ₀ or B ₁ =1.5 V	2.7		2.6	4.0		4.3	0
R _{ON}	Resistance	I _{OUT} =100mA, B ₀ or B ₁ =3.5V	4.5		0.95	1.15		1.30	Ω
ΔR_{ON}	On Resistance Matching Between Channels ⁽⁴⁾	I _{OUT} =100 mA, B ₀ or B ₁ =1.5 V	4.5		0.06	0.12		0.15	Ω
	On Resistance	I _{OUT} =100 mA, B ₀ or B _I =0 V, 0.75 V,1.5 V	2.7		1.4				
R _{FLAT(ON)}	Flatness ⁽⁴⁾	I _{OUT} =100 mA, B ₀ or B _I =0 V, 1 V, 2 V	4.5		0.2	0.3		0.4	Ω
	Quiescent Supply	V _{IN} =0 V or V _{CC} ,	3.6		0.1	0.5		1.0	^
I _{CC}	Current	I _{OUT} =0 V	5.5		0.1	0.5		1.0	μΑ
ΔI_{CC}	Increase in I _{CC} per Input	One Input at 2.7 V, others at V _{CC} or GND (FSA4157A Only)	4.3		0.2			10.0	μA

Notes:

- Measured by the voltage drop between the A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltage on the two (A or B ports).
- $\Delta R_{ON} = R_{ON \; max} R_{ON \; min}$ measured at identical V_{CC} , temperature, and voltage. Flatness is defined as the difference between the maximum and minimum value of on resistance over the specified range of conditions.

AC Electrical Characteristics

Typical values are at 25°C unless otherwise specified.

				Ambient Temperature					l lm!4	-	
Symbol	Parameter	Conditions	V _{cc} (V)	-25°			-40 to	+85°C	Unit	Figure	
				Min.	Тур.	Max.	Min.	Max.			
	B_0 or B_1 =1.5 V, R_L =50 Ω , C_L =35 pF (FSA4157A Only)	2.7 to 3.6			60		65				
t _{ON}	Turn-On Time	B_0 or B_1 =1.5V, R_L =50 Ω , C_L =35pF	2.7 to 3.6			50		60	ns	Figure 8	
		B_0 or B_1 =1.5 V, R_L =50 Ω, C_L =35pF	4.5 to 5.5			35		40			
	Turn-Off	B_0 or B_1 =1.5 V, R_L =50 Ω, C_L =35 pF	2.7 to 3.6			20		30		Figure 8	
Time	B_0 or B_1 =1.5 V, R_L =50 Ω, C_L =35 pF	4.5 to 5.5			15		20	ns	i iguie o		
	Break- t _{BBM} Before-	FSA4157	2.7 to 3.6						ns	Figure 9	
t _{BBM}			4.5 to 5.5		20						
	Make Time	FSA4157A Only	4.5 to 5.5		25						
Q	Charge	C _L =1.0 nF,	2.7 to 3.6		10				рС	Figure 11	
Q	Injection	$V_{GE}=0 V, R_{GEN}=0 \Omega$	4.5 to 5.5		20				рС	rigule 11	
OIRR	Off Isolation	$f=1$ MHz, $R_L=50$ $Ω$	2.7 to 3.6		-70				dB	Figure 10	
Oliviv	On isolation	1=1 Wil 12, TC30 S2	4.5 to 5.5		-70				GD.	r igure re	
			2.7 to 3.6		-70						
Xtalk	Crosstalk	Crosstalk $f=1 \text{ MHz}, R_L=50 \Omega$ 4.5	Crosstalk f=1 MHz, R_L =50 Ω 4.5 to 5.5		-70				dB	Figure 10	
D)A/	-3db	B 50.0	2.7 to 3.6			300					
BW Bandwidth	$R_L=50 \Omega$	4.5 to 5.5			300			MHz	Figure 13		
THD	Total Harmon	R _L =600 Ω, V _{IN} =0.5,	2.7 to 3.6		0.002				%	Figure 1/	
	Distortion	f=20 Hz to 20 kHz	4.5 to 5.5		0.002				70	Figure 14	

Capacitance

Symbol	Symbol Parameter		V _{CC} (V)	Ambient Temperature -25°			Unit	Figure	
				Min.	Тур.	Max.		D	
C _{IN}	Control Pin Input Capacitance	f=1 MHz	0		3.5		pF	Figure 12	
C _{OFF}	B Port Off Capacitance f=1 MHz		4.5		12.0		pF	Figure 12	
C _{ON}	On Capacitance	f=1 MHz	4.5		40.0		pF	Figure 12	

Typical Performance Characteristics

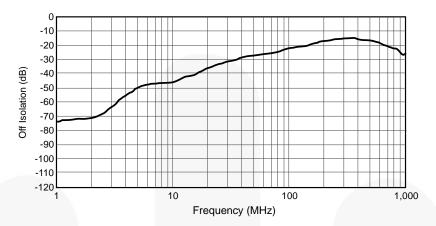


Figure 3. Off Isolation, $V_{CC} = 2.7 \text{ V}$ to 5.5 V

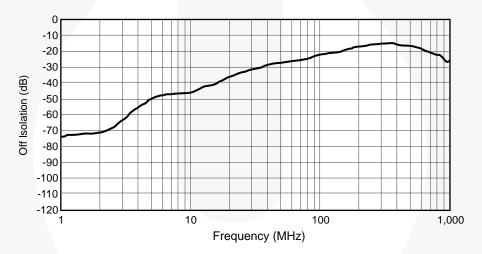


Figure 4. Crosstalk, $V_{CC} = 2.7 \text{ V to } 5.5 \text{ V}$

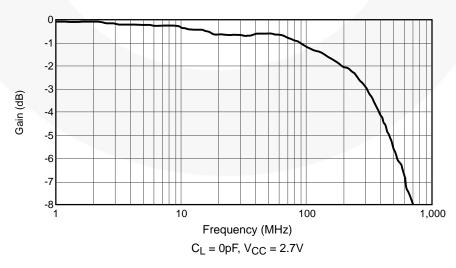


Figure 5. Bandwidth, $V_{CC} = 2.7 \text{ V}$ to 5.5 V

Typical Performance Characteristics (Continued)

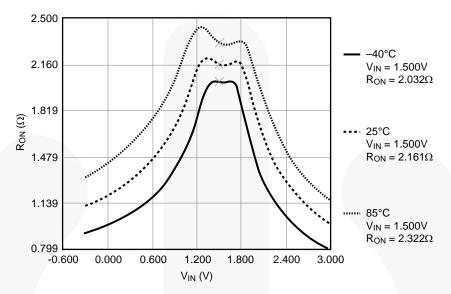


Figure 6. R_{ON} Switch On Resistance, I_{ON} = 100 mA, V_{CC} = 2.7

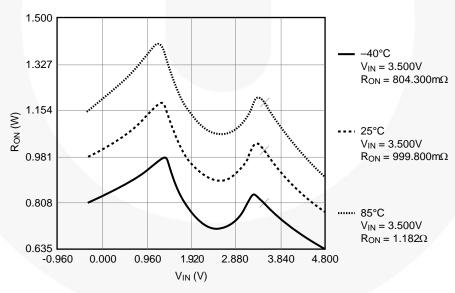
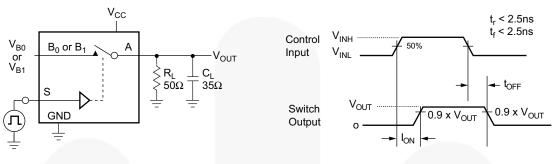


Figure 7. R_{ON} Switch On Resistance, $I_{ON} = 100$ mA, $V_{CC} = 4.5$ V

AC Loadings and Waveforms



C_L Includes Fixture and Stray Capacitance

Logic Input Waveforms Inverted for Switches that have the Opposite Logic Sense

Figure 8. Turn On / Off Timing

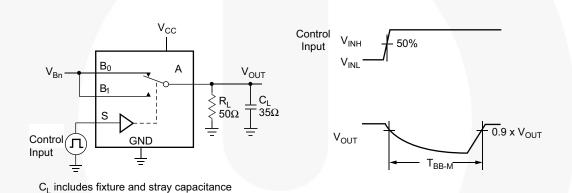


Figure 9. Break Before Make Timing

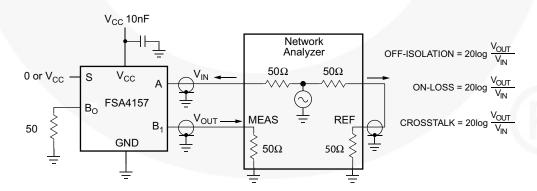


Figure 10. Off Isolation and Crosstalk

AC Loadings and Waveforms (Continued)

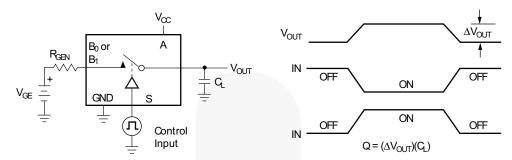


Figure 11. Charge Injection

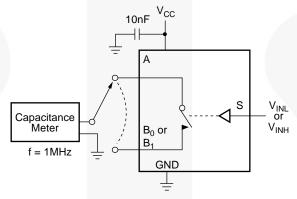


Figure 12. On / Off Capacitance Measurement Setup

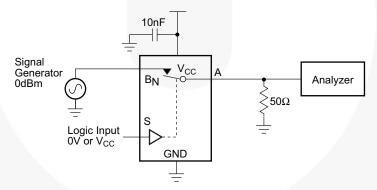


Figure 13. Bandwidth

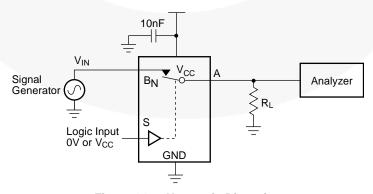
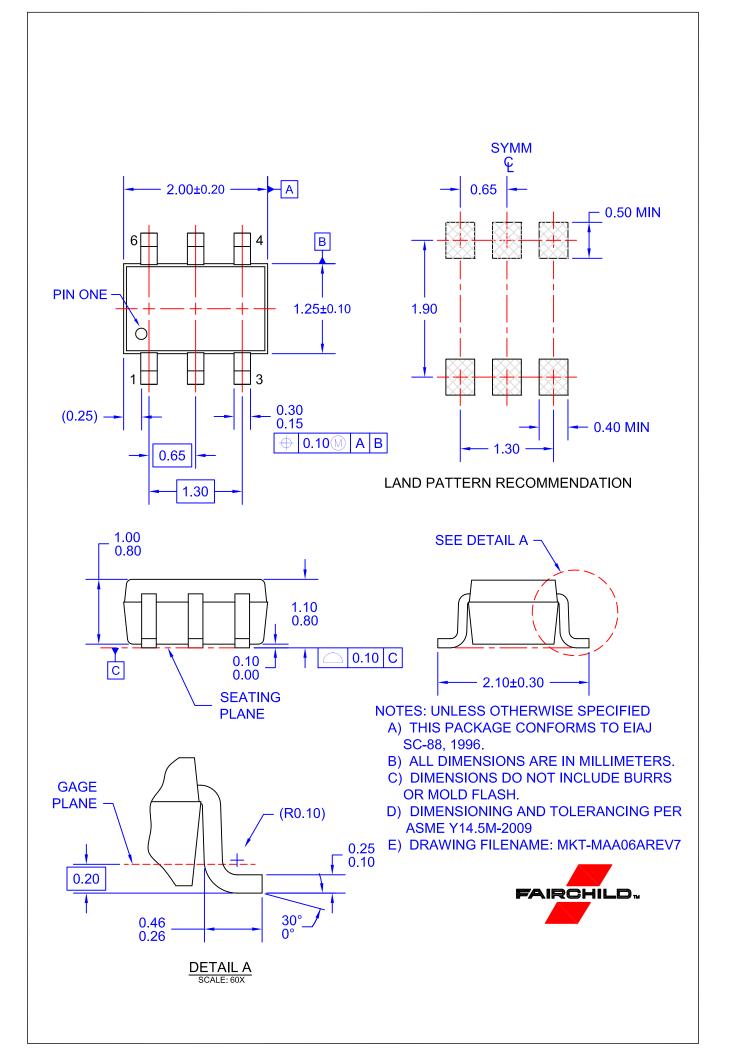
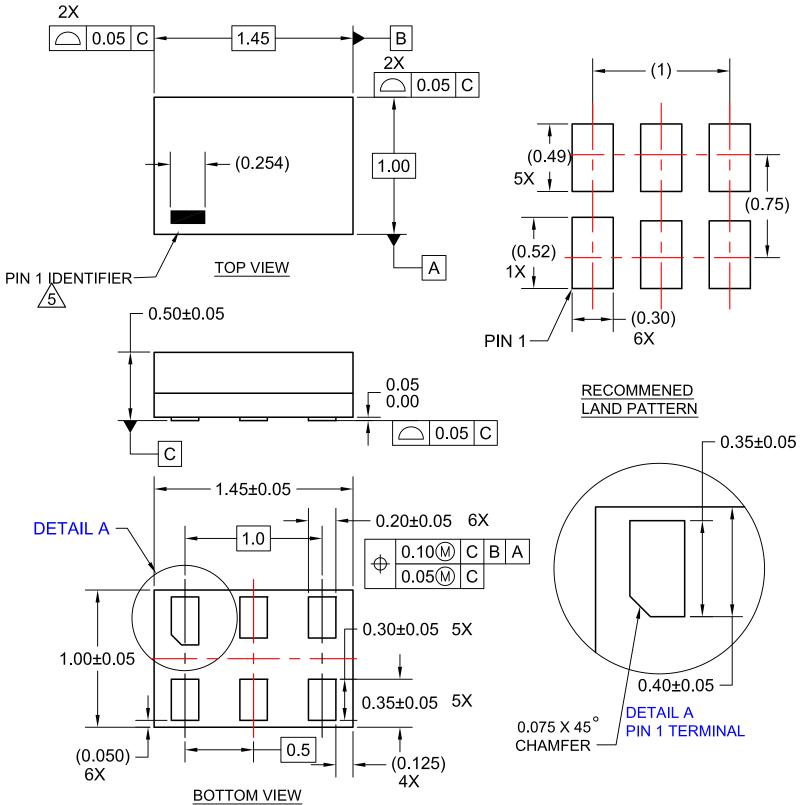


Figure 14. Harmonic Distortion





NOTES:

- 1. CONFORMS TO JEDEC STANDARD MO-252 VARIATION UAAD
- 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-2009
- 4. LANDPATTERN RECOMMENDATION PER FSC

PIN ONE IDENTIFIER IS 2X LENGTH OF ANY

OTHER LINE IN THE MARK CODE LAYOUT.

6. FILENAME AND REVISION: MAC06AREV6







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Definition of Terms

Deminition of Terms		
Datasheet Identification	Product Status	Definition
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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