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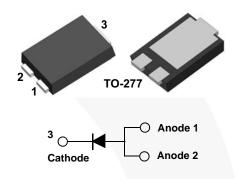
September 2015



## FSV10120V 10 A, 120 V Ultra-Low VF Schottky Rectifier

### Features

- Ultra-Low Forward Voltage Drop
- Low Thermal Resistance
- Very Low Profile: Typical Height of 1.1 mm
- Trench Schottky Technology
- RoHS Compliant
- Green Molding Compound as per IEC61249 Standard
- Lead Free in Compliance with EU RoHS 2011/65/EU Directive
- Qualified per AEC-Q101 Rev. C Standard



### **Ordering Information**

Part Number	Top Mark	Package	Packing Method	
FSV10120V	SV10120V FSV10120V		Tape and Reel	

### **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>RRM</sub>	Peak Repetitive Reverse Voltage	120	V
V <sub>RWM</sub>	Working Peak Reverse Voltage	120	V
V <sub>RMS</sub>	RMS Reverse Voltage	85	V
V <sub>R</sub>	DC Blocking Voltage	120	V
I <sub>F(AV)</sub>	Average Rectified Peak Forward Surge Current	10	Α
I <sub>FSM</sub>	Non-Repetitive Peak Forward Surge Current	180	А
TJ	Operating Junction Temperature Range	-55 to +150	°C
T <sub>STG</sub>	Storage Temperature Range	-55 to +150	°C

### Thermal Characteristics<sup>(1)</sup>

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Minimum Land Pattern	Maximum Land Pattern	Unit
R <sub>θJA</sub>	Junction-to-Ambient Thermal Resistance	100	40	°C/W
	Junction-to-Lead Thermal Characteristics, Thermocouple Soldered to Anode	15	12	°C/W
Ψjl	Junction-to-Lead Thermal Characteristics, Thermocouple Soldered to Cathode	6	5	0/00

#### Note:

The thermal resistances (R<sub>θJA</sub> & ψ<sub>JL</sub>) are characterized with device mounted on the following FR4 printed circuit boards, as shown in Figure 1 and Figure 2. PCB size: 76.2 x 114.3 mm. Minimum land pattern size: 4.9 x 4.8 mm (big pattern, x1), 1.4 x 1.52 mm (small pattern, x2). Maximum land pattern size: 30 x 30 mm (pattern, x2). Force line trace size = 55 mils, sense line trace size = 4 mils.



Figure 1. Minimum Land Pattern of 2 oz Copper

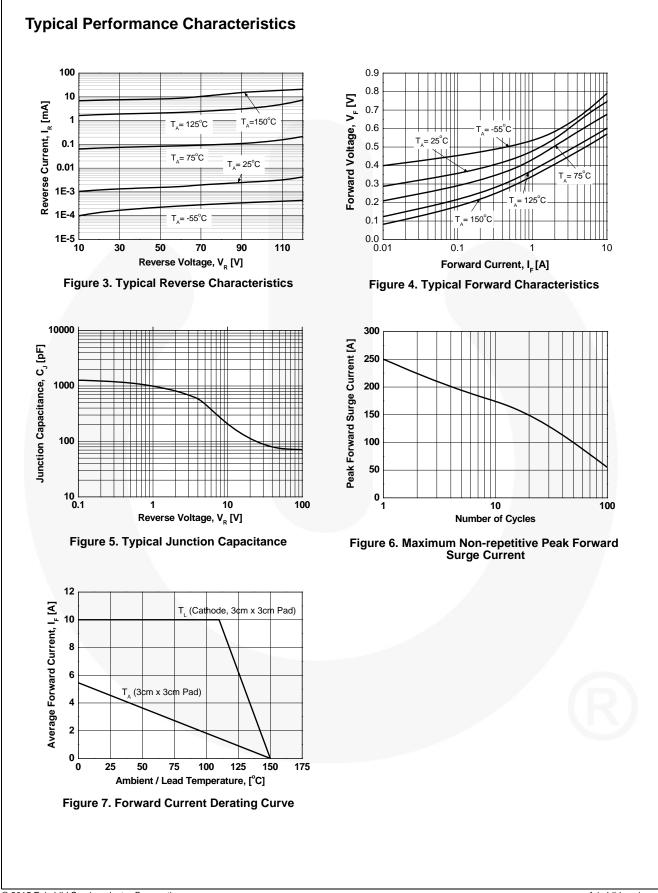
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Figure 2. Maximum Land Pattern of 2 oz Copper

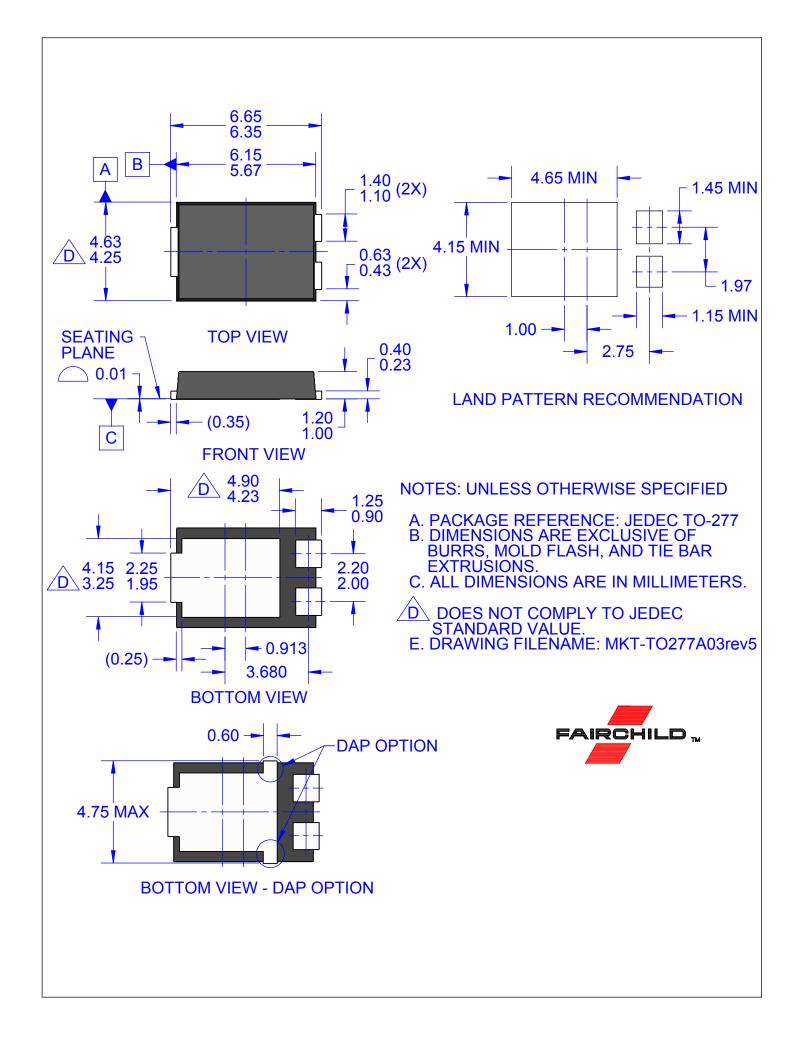
### **Electrical Characteristics**

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>R</sub>	Breakdown Voltage	I <sub>R</sub> = 0.5 mA	120			V
V <sub>F</sub>	Forward Voltage Drop	I <sub>F</sub> = 5 A		0.667		V
		I <sub>F</sub> = 5 A, T <sub>A</sub> = 125°C		0.530	/	
		I <sub>F</sub> = 10 A		0.748	0.800	
		I <sub>F</sub> = 10 A, T <sub>A</sub> = 125°C		0.602	0.620	
	Reverse Current	V <sub>R</sub> = 80 V		0.0016		mA
I <sub>R</sub>		V <sub>R</sub> = 80 V, T <sub>A</sub> = 125°C		2.703		
		V <sub>R</sub> = 120 V		0.004	0.025	
		V <sub>R</sub> = 120 V, T <sub>A</sub> = 125°C		6.791	10	
CJ	Junction Capacitance	V <sub>R</sub> = 4 V, f = 1 MHz		608		pF
T <sub>rr</sub>	Reverse Recovery Time	$I_{\rm F} = 0.5 \text{ A}, I_{\rm R} = 1 \text{ A}, I_{\rm rr} = 0.25 \text{ A}$		16.70		ns



FSV10120V — 10 A, 120 V Ultra-Low VF Schottky Rectifier





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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.		
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.		

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