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September 2015

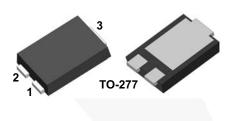
FSV8100V 8 A, 100 V Ultra-Low VF Schottky Rectifier

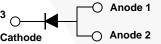
Features

- Ultra-Low Forward Voltage Drop
- Low Thermal Resistance
- Very Low Profile: Typical Height of 1.1 mm
- Trench Schottky Technology
- RoHS Compliant
- · Green Molding Compound as per IEC61249 Standard
- Lead Free in Compliance with EU RoHS 2011/65/EU Directive
- Qualified per AEC-Q101 Rev. C Standard

Applications

- AC-DC and DC-DC Converter
- Mobile Charger
- LED lighting
- Solar Panel
- Reverse Polarity Protection





Ordering Information

Part Number Top Mark		Package	Packing Method	
FSV8100V	FSV8100V	TO-277 3L	Tape and Reel	

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{RRM}	Peak Repetitive Reverse Voltage	100	V
V _{RWM}	Working Peak Reverse Voltage	100	V
V _{RMS}	RMS Reverse Voltage	70	V
V _R	DC Blocking Voltage	100	V
I _{F(AV)}	Average Rectified Peak Forward Surge Current	8	Α
I _{FSM}	Non-Repetitive Peak Forward Surge Current	150	Α
TJ	Operating Junction Temperature Range	-55 to +150	°C
T _{STG}	Storage Temperature Range	-55 to +150	°C

Thermal Characteristics(1)

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Minimum Land Pattern	Maximum Land Pattern	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance	100	40	°C/W
ΨJL	Junction-to-Lead Thermal Characteristics, Thermocouple Soldered to Anode	15	12	°C/W
	Junction-to-Lead Thermal Characteristics, Thermocouple Soldered to Cathode	6	5	C/VV

Note:

1. The thermal resistances ($R_{\theta JA} \& \psi_{JL}$) are characterized with device mounted on the following FR4 printed circuit boards, as shown in Figure 1 and Figure 2. PCB size: 76.2 x 114.3 mm. Minimum land pattern size: 4.9 x 4.8 mm (big pattern, x1), 1.4 x 1.52 mm (small pattern, x2). Maximum land pattern size: 30 x 30 mm (pattern, x2). Force line trace size = 55 mils, sense line trace size = 4 mils.



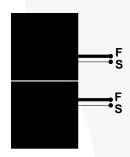


Figure 1. Minimum Land Pattern of 2 oz Copper

Figure 2. Maximum Land Pattern of 2 oz Copper

Electrical Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _R	Breakdown Voltage	I _R = 0.5 mA	100			V
V _F	Forward Voltage Drop	I _F = 5 A		0.542		V
		I _F = 5 A, T _A = 125°C		0.496		
		I _F = 8 A		0.620	0.670	
		I _F = 8 A, T _A = 125°C		0.574	0.600	
		I _F = 10 A		0.674		
		I _F = 10 A, T _A = 125°C		0.611	- /	
I _R	Reverse Current	V _R = 70 V		0.006		- mA
		V _R = 70 V, T _A = 125°C		5.57		
		V _R = 100 V		0.008	0.05	
		V _R = 100 V, T _A = 125°C		15.65	20	
CJ	Junction Capacitance	V _R = 4 V, f = 1 MHz		672		pF
T _{rr}	Reverse Recovery Time	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{rr} = 0.25 \text{ A}$		19.64		ns

Typical Performance Characteristics

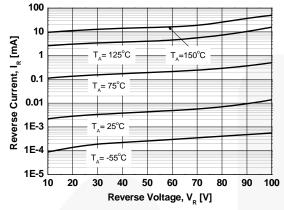
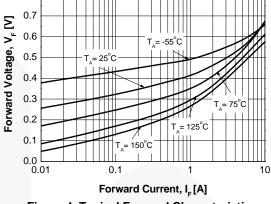


Figure 3. Typical Reverse Characteristics



8.0

Figure 4. Typical Forward Characteristics

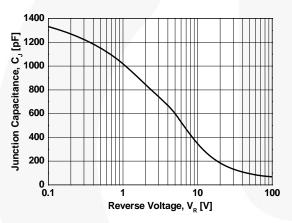


Figure 5. Typical Junction Capacitance

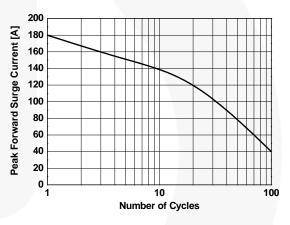


Figure 6. Maximum Non-repetitive Peak Forward Surge Current

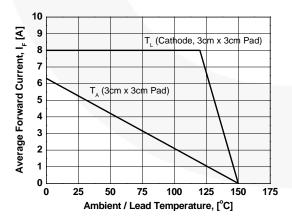
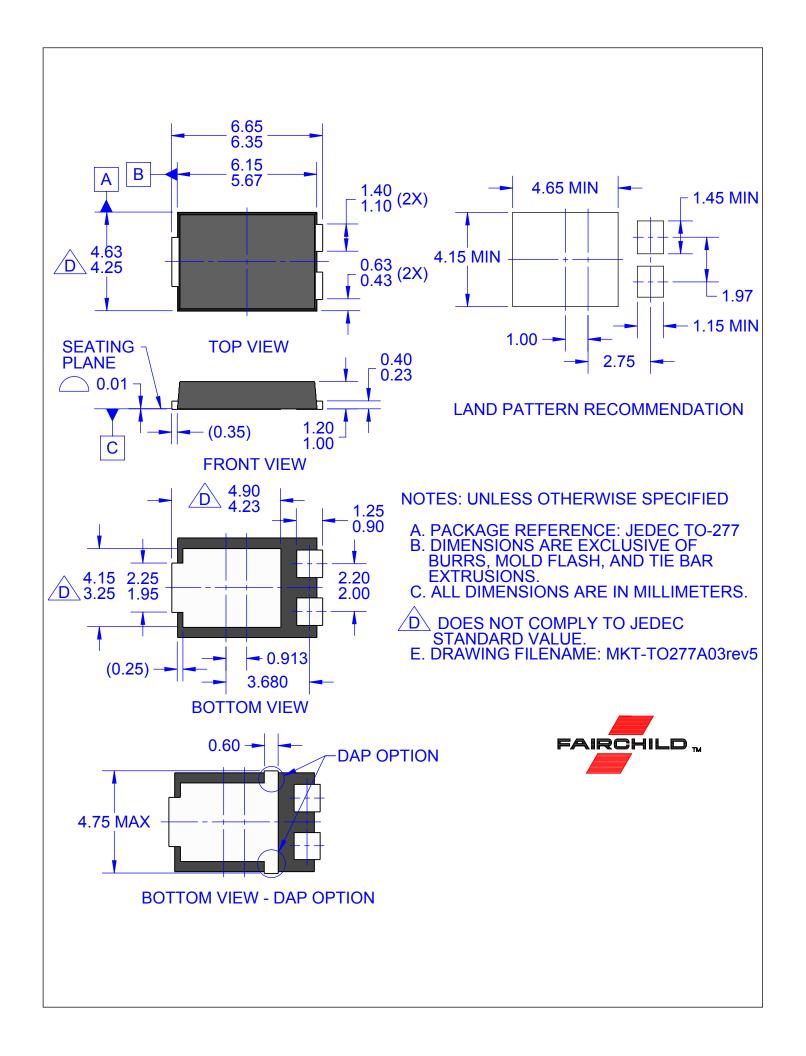


Figure 7. Forward Current Derating Curve







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Definition of Terms

Definition of Terms				
Datasheet Identification	Product Status	Definition		
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.		
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.		
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.		

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