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November 2014

KSC1008 NPN Epitaxial Silicon Transistor

Features

- Low-Frequency Amplifier Medium Speed Switching
- High Collector-Base Voltage: V_{CBO} = 80 V
- Collector Current : I_C = 700 mA
- Suffix "-C" means Center Collector (1. Emitter 2. Collector 3. Base)
- Non Suffix "-C" means Side Collector (1. Emitter 2. Base 3. Collector)
- Complement to KSA708



KSC1008: 1. Emitter 2. Base 3. Collector KSC1008C: 1. Emitter 2. Collector 3. Base

Ordering Information(1)

Part Number	Top Mark	Package	Packing Method
KSC1008OBU	C1008 O-	TO-92 3L	Bulk
KSC1008YBU	C1008 Y-	TO-92 3L	Bulk
KSC1008YTA	C1008 Y-	TO-92 3L	Ammo, 2000 pcs
KSC1008CYTA	C1008 YC	TO-92 3L	Ammo, 2000 pcs
KSC1008GTA	C1008 G-	TO-92 3L	Ammo, 2000 pcs

Note:

1. Affix "-C-" means center collector pin. Affix "-O-, -Y-, -G-" means h_{FE} classification. Suffix "-BU" means bulk packing, straight lead form. Suffix "-TA" means tape and ammo packing, 0.200 in-line spacing lead form.

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-Base Voltage	80	V
V _{CEO}	Collector-Emitter Voltage	60	V
V _{EBO}	Emitter-Base Voltage	8	V
I _C	Collector Current	700	mA
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 to 150	°C

Thermal Characteristics⁽²⁾

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
В	Power Dissipation	800	mW
P _D	Derate Above 25°C	6.4	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	156	°C/W

Note:

2. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

Electrical Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{CBO}	Collector-Base Breakdown Voltage	$I_C = 100 \mu A, I_E = 0$	80			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_C = 10 \text{ mA}, I_B = 0$	60			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = 10 \mu A, I_C = 0$	8			V
I _{CBO}	Collector Cut-Off Current	$V_{CB} = 60 \text{ V}, I_{E} = 0$			0.1	μΑ
I _{EBO}	Emitter Cut-Off Current	$V_{EB} = 5 \text{ V}, I_{C} = 0$			0.1	μΑ
h _{FE}	DC Current Gain	$V_{CE} = 2 \text{ V}, I_{C} = 50 \text{ mA}$	40		400	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_C = 500 \text{ mA}, I_B = 50 \text{ mA}$		0.2	0.4	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	$I_C = 500 \text{ mA}, I_B = 50 \text{ mA}$		0.86	1.10	V
f _T	Current Gain Bandwidth Product	$V_{CE} = 10 \text{ V}, I_{C} = 50 \text{ mA}$	30	50		MHz
C _{ob}	Output Capacitance	$V_{CB} = 10 \text{ V}, I_{E} = 0,$ f = 1 MHz		8		pF

h_{FE} Classification

Classification	R	0	Y	G
h _{FE}	40 ~ 80	70 ~ 140	120 ~ 240	200 ~ 400

Typical Performance Characteristics

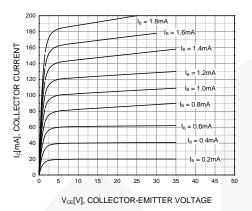


Figure 1. Static Characteristic

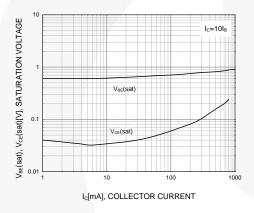


Figure 3. Base-Emitter Saturation Voltage and Collector-Emitter Saturation Voltage

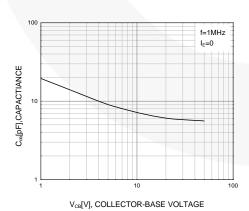


Figure 5. Collector Output Capacitance

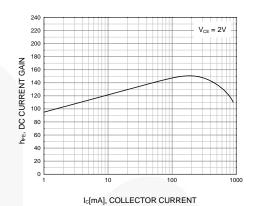


Figure 2. DC Current Gain

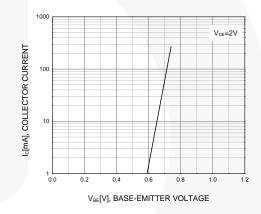
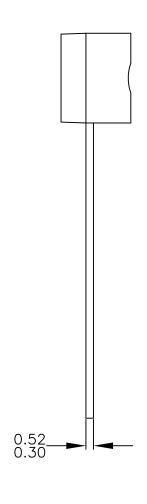


Figure 4. Base-Emitter On Voltage

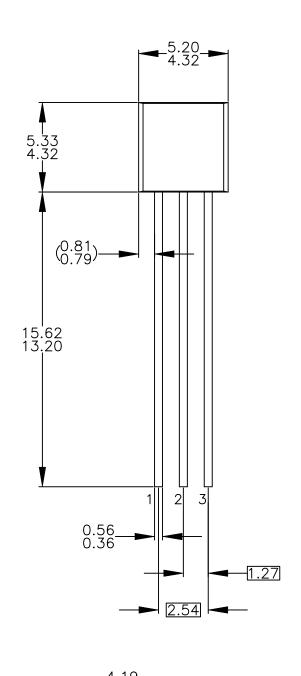






NOTES: UNLESS OTHERWISE SPECIFIED

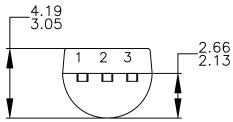
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 ALL DIMENSIONS ARE IN MILLIMETERS.
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Definition of Terms

Definition of Terms				
Datasheet Identification	Product Status	Definition		
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
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