# FAIRCHILD

SEMICONDUCTOR TM

# **KSD363**

### **B/W TV Horizontal Deflection Output**

- Collector-Base Voltage : V<sub>CBO</sub>=300V
  Collector Current : I<sub>C</sub>=6A
- Collector Dissipation :  $P_C=40W(T_C=25^{\circ}C)$



1.Base 2.Collector 3.Emitter

## **NPN Epitaxial Silicon Transistor**

### Absolute Maximum Ratings T<sub>C</sub>=25°C unless otherwise noted

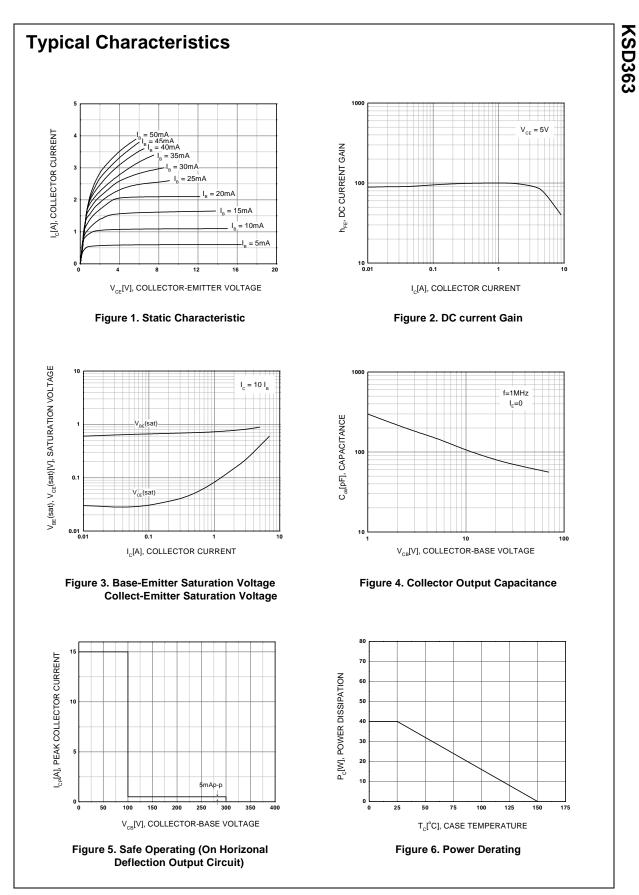
Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage	300	V
V <sub>CEO</sub>	Collector-Emitter Voltage	120	V
V <sub>EBO</sub>	Emitter-Base Voltage	8	V
I <sub>C</sub>	Collector Current	6	A
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C)	40	W
TJ	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	- 55 ~ 150	°C

### Electrical Characteristics $T_C=25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	$I_{C} = 1 \text{ mA}, I_{E} = 0$	300			V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	$I_{\rm C} = 20 {\rm mA}, I_{\rm B} = 0$	120			V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = 1mA, I <sub>C</sub> = 0	8			V
I <sub>CBO</sub>	Collector Cut-off Current	V <sub>CB</sub> = 250V, I <sub>E</sub> = 0			1	mA
h <sub>FE</sub>	DC Current Gain	$V_{CE} = 5V, I_{C} = 1A$	40		240	
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 1A, I <sub>B</sub> = 0.1A			1	V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	I <sub>C</sub> = 1A, I <sub>B</sub> = 0.1A			1.5	V
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE} = 5V, I_{C} = 0.5A$		10		MHz

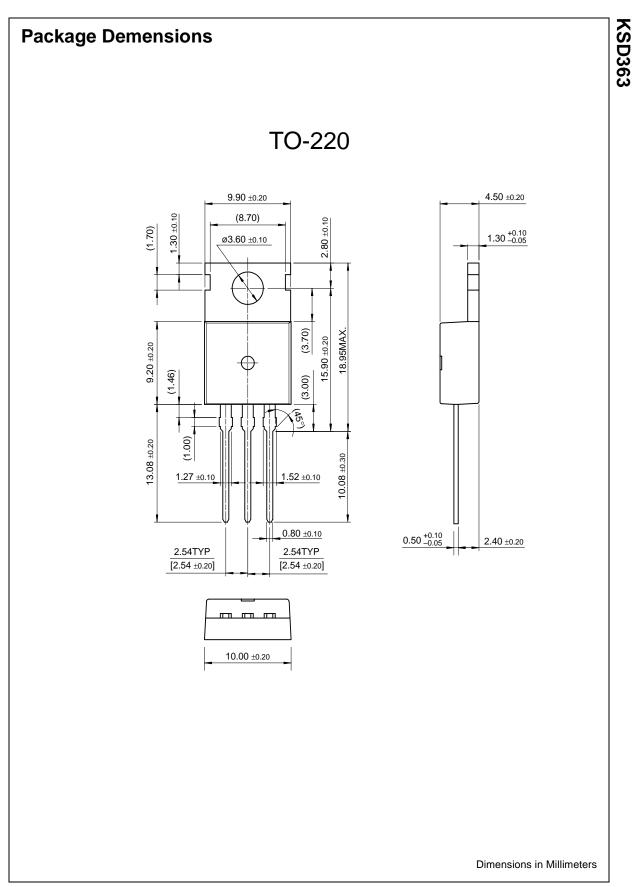
## h<sub>FE</sub> Classification

Classification	R	0	Y
h <sub>FE</sub>	40 ~ 80	70 ~ 140	120 ~ 240



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Rev. A, February 2000



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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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