

October 2015

# KSD5041 **NPN Epitaxial Silicon Transistor**

#### **Features**

- · AF Output Amplifier for Electronic Flash Unit
- Low Collector-Emitter Saturation Voltage
- · High Performance at Low Supply Voltage



### **Ordering Information**

Part Number	Top Mark	Package	Packing Method
KSD5041RTA	D5041	TO-92 3L	Ammo
KSD5041QTA	D5041	TO-92 3L	Ammo

### **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CBO</sub>	Collector-Base Voltage	40	V
V <sub>CEO</sub>	Collector-Emitter Voltage	20	V
$V_{EBO}$	Emitter-Base Voltage	7	V
I <sub>C</sub>	Collector Current	5	Α
TJ	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	-55 to 150	°C

### Thermal Characteristics(1)

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
В	Power Dissipation	0.75	W
$P_{D}$	Derate Above 25°C	6.0	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	166.6	°C/W

### Note:

1. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

### **Electrical Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	$I_C = 1 \text{ mA}, I_B = 0$	20			V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = 10 \mu\text{A},  I_C = 0$	7			V
I <sub>CBO</sub>	Collector Cut-Off Current	$V_{CB} = 10 \text{ V}, I_{E} = 0$			0.1	μΑ
I <sub>EBO</sub>	Emitter Cut-Off Current	$V_{EB} = 7 \text{ V, } I_{C} = 0$			0.1	μΑ
h <sub>FE1</sub>	DC Current Gain	$V_{CE} = 2 \text{ V}, I_{C} = 0.5 \text{ A}$	180		600	
h <sub>FE2</sub>	DC Current Gain	$V_{CE} = 2 \text{ V}, I_{C} = 2 \text{ A}$	150			
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	$I_C = 3 \text{ A}, I_B = 0.1 \text{ A}$			1	V
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE} = 6 \text{ V}, I_{C} = 50 \text{ mA}$		150		MHz
C <sub>ob</sub>	Output Capacitance	$V_{CB} = 20 \text{ V, } I_{E} = 0,$ f = 1 MHz			50	pF

## h<sub>FE</sub> Classification

Classification	Р	0	R
h <sub>FE1</sub>	180 ~ 270	230 ~ 380	340 ~ 600

## **Typical Performance Characteristics**

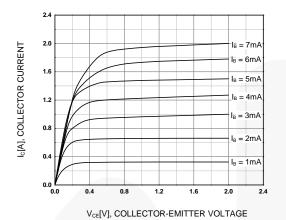


Figure 1. Static Characteristic

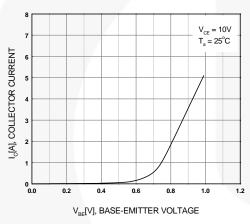


Figure 3. Base-Emitter Saturation Voltage

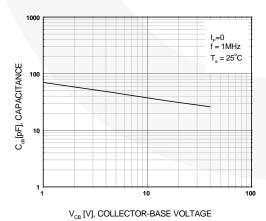


Figure 5. Collector Output Capacitance

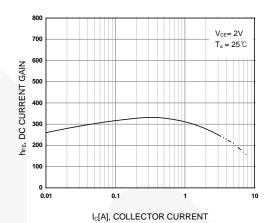


Figure 2. DC Current Gain

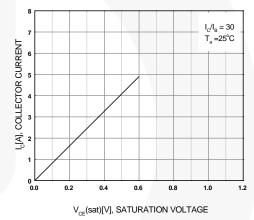


Figure 4. Collector-Emitter Saturation Voltage

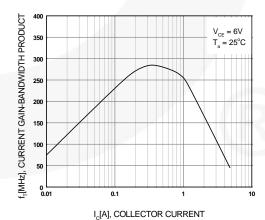


Figure 6. Current Gain Bandwidth Product

## **Typical Performance Characteristics** (Continued)

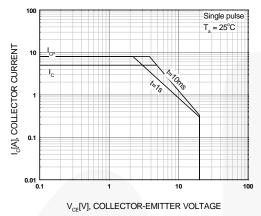


Figure 7. Safe Operating Area

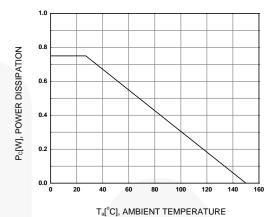
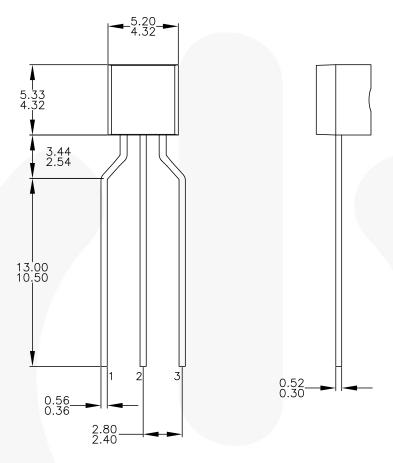
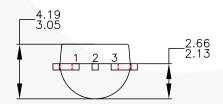


Figure 8. Power Derating

## **Physical Dimensions**





NOTES: UNLESS OTHERWISE SPECIFIED

- DRAWING CONFORMS TO JEDEC MS-013, VARIATION AC. ALL DIMENSIONS ARE IN MILLIMETERS. DRAWING CONFORMS TO ASME Y14.5M-2009. DRAWING FILENAME: MKT-ZAO3FREV3. FAIRCHILD SEMICONDUCTOR.

Figure 9. 3-Lead, TO-92, Molded, 0.2 In Line Spacing Lead Form, Ammo Type





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Definition of Terms				
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