

November 2007

# **KSD882 NPN Epitaxial Silicon Transistor**

### **Recommended Applications**

• Audio Frequency Power Amplifier

#### **Featuers**

- Low Speed Switcing
- Complement to KSB772.



# Absolute Maximum Ratings\* T<sub>a</sub> = 25°C unless otherwise noted

Symbol	Parameter	Ratings	Units	
BV <sub>CBO</sub>	Collector-Base Voltage	40	V	
BV <sub>CEO</sub>	Collector-Emitter Voltage	30	V	
BV <sub>EBO</sub>	Emitter-Base Voltage	5	V	
I <sub>C</sub>	Collector Current(DC)	3	А	
I <sub>C</sub>	Collector Current(Pulse)**	7	А	
I <sub>B</sub>	Base Current	0.6	А	
P <sub>D</sub>	Total Device Dissipation(T <sub>C</sub> =25°C) Total Device Dissipation(T <sub>a</sub> =25°C)	10 1	W W	
T <sub>J</sub> , T <sub>STG</sub>	Junction and Storage Temperature	- 55 ~ +150	°C	

<sup>\*</sup> These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

### Electrical Characteristics. $T_a=25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> =500uA, I <sub>E</sub> =0	40			٧
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> =5mA, I <sub>B</sub> =0	30			V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> =500uA, I <sub>C</sub> =0	5			V
I <sub>CBO</sub>	Collector Cut-off Current	$V_{CB} = 30V, I_{E} = 0$			1	μΑ
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB} = 3V, I_{C} = 0$			1	μΑ
h <sub>FE1</sub> h <sub>FE2</sub>	*DC Current Gain	$V_{CE} = 2V$ , $I_C = 20$ mA $V_{CE} = 2V$ , $I_C = 1$ A	30 60	150 160	400	
V <sub>CE</sub> (sat)	*Collector-Emitter Saturation Voltage	I <sub>C</sub> = 2A, I <sub>B</sub> = 0.2A		0.3	0.5	V
V <sub>BE</sub> (sat)	*Base-Emitter Saturation Voltage	$I_C = 2A, I_B = 0.2A$		1.0	2.0	V
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE} = 5V, I_{E} = 0.1A$		90		MHz
C <sub>ob</sub>	Output Capacitance	$V_{CB} = 10V, I_E = 0$ f = 1MHz		45		pF

 $<sup>^{\</sup>star}~$  Pulse Test: PW≤350µs, Duty Cycle≤2% Pulsed

<sup>\*\*</sup> PW≤10ms, Duty Cycle≤50%

# h<sub>FE</sub> Classification

Classification	R	0	Y	G
h <sub>FE2</sub>	60 ~ 120	100 ~ 200	160 ~ 320	200 ~ 400

### **Ordering Information**

Part Number	Marking	Package	Packing Method	Remarks
KSD882OSTU	D882O	TO-126	TUBE	hFE1 R grade
KSD882RSTU	D882R	TO-126	TUBE	hFE1 O grade
KSD882YSTU	D882Y	TO-126	TUBE	hFE1 Y grade
KSD882GSTU	D882G	TO-126	TUBE	hFE1 G grade

<sup>\* 1.</sup> Affix "-S-" means the standard TO126 Package. If the affix is "-STS-" instead of "-S-", that means the short-lead TO126 package.

2. Suffix "-TU" means the tube packing, The Suffix "TU" could be replaced to other suffix character as packing method.

# **Typical Characteristics**

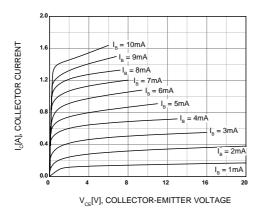


Figure 1. Static Characteristic

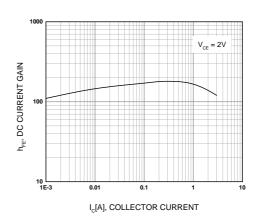


Figure 2. DC current Gain

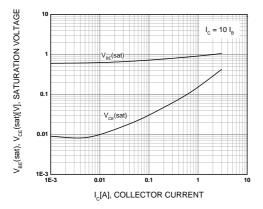


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

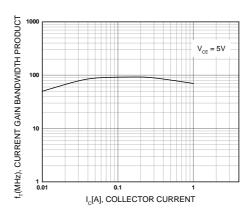


Figure 4. Current Gain Bandwidth Product

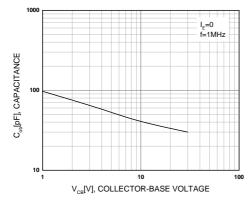


Figure 5. Collector Output Capacitance

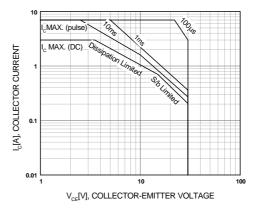


Figure 6. Safe Operating Area

# **Typical Characteristics**

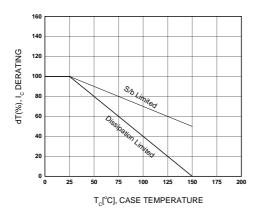


Figure 7. Derating Curve Of Safe Operating Areas

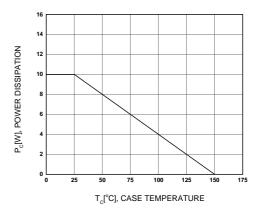


Figure 8. Power Derating





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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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