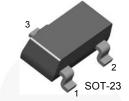


February 2015

# **KST2907A PNP Epitaxial Silicon Transistor**

## **Features**

· General-Purpose Transistor



1. Base 2. Emitter 3. Collector

## **Ordering Information**

Part Number	Marking	Package	Packing Method
KST2907AMTF	2F	SOT-23 3L	Tape and Reel

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CBO</sub>	Collector-Base Voltage	-60	V
V <sub>CEO</sub>	Collector-Emitter Voltage	-60	V
V <sub>EBO</sub>	Emitter-Base Voltage	-5	V
I <sub>C</sub>	Collector Current	-600	mA
T <sub>STG</sub>	Storage Temperature	150	°C

## Thermal Characteristics(1)

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Max.	Unit
D	Total Device Dissipation	350	mW
$P_{D}$	Derate Above 25°C	2.8	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	357	°C/W

### Note:

1. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

## **Electrical Characteristics**

Values are at  $T_A$  = 25°C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	$I_C = -10  \mu A, I_E = 0$	-60		V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage <sup>(2)</sup>	I <sub>C</sub> = -10 mA, I <sub>B</sub> = 0	-60		V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = -10  \mu A,  I_C = 0$	-5		V
I <sub>CBO</sub>	Collector Cut-Off Current	V <sub>CB</sub> = -50 V, I <sub>E</sub> = 0		-0.01	μΑ
		$V_{CE} = -10 \text{ V}, I_{C} = -0.1 \text{ mA}$	75		
h <sub>FE</sub>		$V_{CE} = -10 \text{ V}, I_{C} = -1.0 \text{ mA}$	100		
	DC Current Gain	$V_{CE} = -10 \text{ V}, I_{C} = -10 \text{ mA}$	100		
		$V_{CE} = -10 \text{ V}, I_{C} = -150 \text{ mA}^{(2)}$	100	300	
		$V_{CE}$ = -10 V, $I_{C}$ = -500 mA <sup>(2)</sup>	50		
V <sub>CE</sub> (sat)	Callactor Emitter Saturation Voltage <sup>(2)</sup>	I <sub>C</sub> = -150 mA, I <sub>B</sub> = -15 mA		-0.4	- V
	Collector-Emitter Saturation Voltage <sup>(2)</sup>	I <sub>C</sub> = -500 mA, I <sub>B</sub> = -50 mA		-1.6	
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage <sup>(2)</sup>	I <sub>C</sub> = -150 mA, I <sub>B</sub> = -15 mA		-1.3	V
		$I_C = -500 \text{ mA}, I_B = -50 \text{ mA}$	<u> </u>	-2.6	]
f <sub>T</sub>	Current Gain Bandwidth Product	I <sub>C</sub> = -50 mA, V <sub>CE</sub> = -20 V, f = 100 MHz	200		MHz
C <sub>ob</sub>	Output Capacitance	$V_{CB} = -10 \text{ V}, I_{E} = 0,$ f = 1.0 MHz		8	pF
t <sub>ON</sub>	Turn-On Time	$V_{CC} = -30 \text{ V}, I_{C} = -150 \text{ mA},$ $I_{B1} = -15 \text{ mA}$		50	ns
t <sub>OFF</sub>	Turn-Off Time	$V_{CC} = -6 \text{ V}, I_{C} = -150 \text{ mA},$ $I_{B1} = I_{B2} = -15\text{mA}$		110	ns

## Note:

2. Pulse test: pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2.0%.

## **Typical Performance Characteristics**

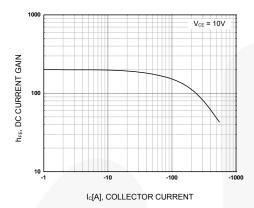


Figure 1. DC Current Gain

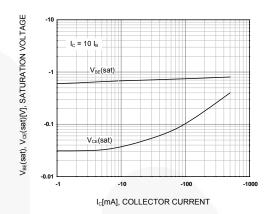


Figure 2. Collector-Emitter Saturation Voltage and Base-Emitter Saturation Voltage

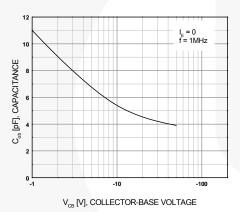


Figure 3. Output Capacitance

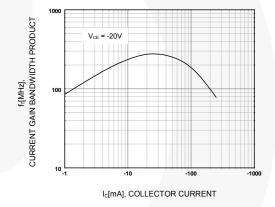


Figure 4. Current Gain Bandwidth Product

## **Physical Dimensions**

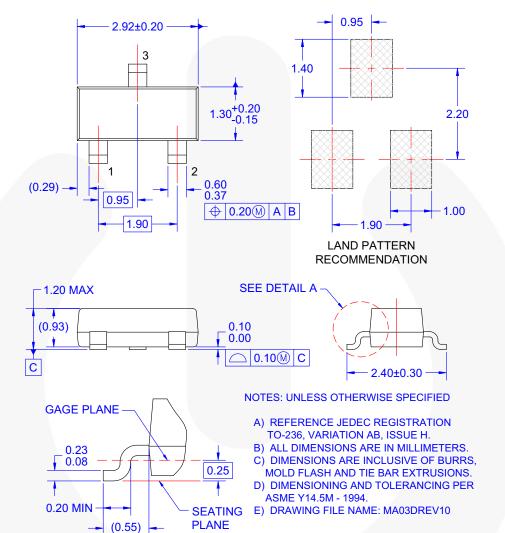


Figure 5. 3-LEAD, SOT23, JEDEC TO-236, LOW PROFILE

**DETAIL A** 





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