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LM78XX / LM78XXA — 3-Terminal 1 A Positive Voltage Regulator



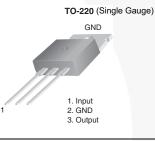
LM78XX / LM78XXA 3-Terminal 1 A Positive Voltage Regulator

Features

- Output Current up to 1 A
- Output Voltages: 5, 6, 8, 9, 10, 12, 15, 18, 24 V
- Thermal Overload Protection
- Short-Circuit Protection
- Output Transistor Safe Operating Area Protection

Description

The LM78XX series of three-terminal positive regulators is available in the TO-220 package and with several fixed output voltages, making them useful in a wide range of applications. Each type employs internal current limiting, thermal shut-down, and safe operating area protection. If adequate heat sinking is provided, they can deliver over 1 A output current. Although designed primarily as fixedvoltage regulators, these devices can be used with external components for adjustable voltages and currents.

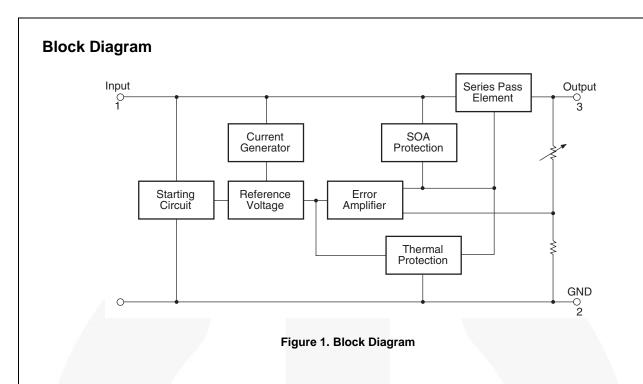


Product Number	Output Voltage Tolerance	Package	Operating Temperature	Packing Method
LM7805CT				
LM7806CT				1
LM7808CT				
LM7809CT				
LM7810CT	±4%		-40°C to +125°C	
LM7812CT				
LM7815CT		TO-220		Deil
LM7818CT		(Single Gauge)		Rail
LM7824CT				
LM7805ACT				
LM7809ACT				
LM7810ACT	±2%		0°C to +125°C	
LM7812ACT				
LM7815ACT				

Ordering Information⁽¹⁾

Note:

1. Above output voltage tolerance is available at 25°C.



Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Paramet	ter	Value	Unit
M		V _O = 5 V to 18 V	35	V
VI	Input Voltage	V _O = 24 V	40	V
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction-Case	5	°C/W	
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-Air (T	O-220)	65	°C/W
т	Operating Temperature Renge	LM78xx	-40 to +125	C
T _{OPR}	Operating Temperature Range	LM78xxA	0 to +125	
T _{STG}	Storage Temperature Range	÷	- 65 to +150	°C

Electrical Characteristics (LM7805)

Refer to the test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 10 V, C_I = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	0	Conditions	Min.	Тур.	Max.	Unit
		T _J = +25°C		4.80	5.00	5.20	
V _O	Output Voltage	$I_0 = 5 \text{ mA to}$ $V_1 = 7 \text{ V to } 20$	1 A, P _O ≤15 W,) V	4.75	5.00	5.25	V
Doglino	Line Regulation ⁽²⁾	T _{.1} = +25°C	$V_{I} = 7 V \text{ to } 25 V$		4.0	100.0	mV
Regline		$I_{\rm J} = +25^{\circ}{\rm C}$	V _I = 8 V to 12 V		1.6	50.0	
Declard	Load Regulation ⁽²⁾	T .25%C	$I_0 = 5 \text{ mA to } 1.5 \text{ A}$		9.0	100.0	~\/
Regload	Load Regulation 7	T _J = +25°C	$I_0 = 250 \text{ mA to } 750 \text{ mA}$		4.0	50.0	mV
Ι _Q	Quiescent Current	T _J = +25°C			5	8	mA
41	Quiescent Current	$I_{O} = 5 \text{ mA to}$	1 A		0.03	0.50	mA
ΔI_Q	Change	$V_{\rm I} = 7 \rm V \ to \ 25$	5 V		0.30	1.30	ma
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽³⁾	I _O = 5 mA			-0.8		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		42		μV
RR	Ripple Rejection ⁽³⁾	f = 120 Hz, V	_I = 8 V to 18 V	62	73		dB
V _{DROP}	Dropout Voltage	$T_{J} = +25^{\circ}C, I_{0}$	_O = 1 A		2		V
R _O	Output Resistance ⁽³⁾	f = 1 kHz			15		mΩ
I _{SC}	Short-Circuit Current	T _J = +25°C, ∖	/ _I = 35 V		230		mA
I _{PK}	Peak Current ⁽³⁾	T _J = +25°C			2.2		Α

Notes:

2. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (LM7806)

Refer to the test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 11 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	(Conditions	Min.	Тур.	Max.	Unit
		T _J = +25°C		5.75	6.00	6.25	
Vo	Output Voltage	$I_0 = 5 \text{ mA to}$ $V_1 = 8.0 \text{ V to}$	1 A, P _O ≤ 15 W, 21 V	5.70	6.00	6.30	V
Poglino	Line Regulation ⁽⁴⁾	T _{.1} = +25°C	$V_{I} = 8 V \text{ to } 25 V$		5.0	120.0	mV
Regline		$I_{\rm J} = +25$ C	V _I = 9 V to 13 V		1.5	60.0	
Declard	Load Regulation ⁽⁴⁾	T .25%C	I _O = 5 mA to 1.5 A		9.0	120.0 mV	
Regload		T _J = +25°C	I _O = 250 mA to 750 mA		3.0	60.0	IIIV
Ι _Q	Quiescent Current	T _J = +25°C			5	8	mA
A I	Quiescent Current	$I_0 = 5 \text{ mA to}$	1 A			0.5	mA
ΔI_Q	Change	$V_{I} = 8 V \text{ to } 25$	5 V			1.3	mA
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽⁵⁾	I _O = 5 mA			-0.8		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		45		μV
RR	Ripple Rejection ⁽⁵⁾	f = 120 Hz, V	I = 8 V to 18 V	62	73		dB
V _{DROP}	Dropout Voltage	T _J = +25°C, I	_O = 1 A		2		V
R _O	Output Resistance ⁽⁵⁾	f = 1 kHz			19		mΩ
I _{SC}	Short-Circuit Current	T _J = +25°C, ∖	/ _I = 35 V		250		mA
I _{PK}	Peak Current ⁽⁵⁾	T _J = +25°C			2.2		Α

Notes:

4. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (LM7808)

Refer to the test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 14 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	(Conditions	Min.	Тур.	Max.	Unit
		T _J = +25°C		7.7	8.0	8.3	
Vo	Output Voltage	$I_0 = 5 \text{ mA to}$ V _I = 10.5 V to	1 A, P _O ≤ 15 W, o 23 V	7.6	8.0	8.4	V
Paglina	Line Regulation ⁽⁶⁾	T - 125°C	V _I = 10.5 V to 25 V		5	160	mV
Regline	Line Regulation	T _J = +25°C	V _I = 11.5 V to 17 V		2	80	
Dealaad	Load Regulation ⁽⁶⁾	T .25%C	I _O = 5 mA to 1.5 A		10	160 mV	
Regload		T _J = +25°C	I _O = 250 mA to 750 mA		5	80	IIIV
Ι _Q	Quiescent Current	T _J = +25°C			5	8	mA
AL	Quiescent Current	$I_{O} = 5 \text{ mA to 1 A}$	1 A		0.05	0.50	mA
Δl _Q	Change	$V_{\rm I} = 10.5 \rm V tc$	o 25 V		0.5	1.0	ma
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽⁷⁾	I _O = 5 mA			-0.8		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		52		μV
RR	Ripple Rejection ⁽⁷⁾	f = 120 Hz, V	₁ = 11.5 V to 21.5 V	56	73		dB
V _{DROP}	Dropout Voltage	I _O = 1 A, T _J =	⊧ +25°C		2		V
R _O	Output Resistance ⁽⁷⁾	f = 1 kHz			17		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _J	= +25°C		230		mA
I _{PK}	Peak Current ⁽⁷⁾	T _J = +25°C			2.2		Α

Notes:

6. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (LM7809)

Refer to the test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 15 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	(Conditions	Min.	Тур.	Max.	Unit
		T _J = +25°C		8.65	9.00	9.35	
Vo	Output Voltage	$I_0 = 5 \text{ mA to}$ $V_1 = 11.5 \text{ V to}$	1 A, P _O ≤15 W, o 24 V	8.60	9.00	9.40	V
Poglino	Line Regulation ⁽⁸⁾	T - 125°C	V _I = 11.5 V to 25 V		6	180	180 mV
Regline		T _J = +25°C	V _I = 12 V to 17 V		2	90	
Declard	Load Regulation ⁽⁸⁾	T .25%C	I _O = 5 mA to 1.5 A		12	180	mV
Regload		T _J = +25°C	I _O = 250 mA to 750 mA		4	90	
Ι _Q	Quiescent Current	T _J = +25°C			5	8	mA
A I	Quiescent Current	$I_0 = 5 \text{ mA to}$	1 A			0.5	mA
Δl _Q	Change	$V_{\rm I} = 11.5 \rm V tc$	26 V			1.3	mA
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽⁹⁾	I _O = 5 mA			-1		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		58		μV
RR	Ripple Rejection ⁽⁹⁾	f = 120 Hz, V	_I = 13 V to 23 V	56	71		dB
V _{DROP}	Dropout Voltage	I _O = 1 A, T _J =	+25°C		2		V
R _O	Output Resistance ⁽⁹⁾	f = 1 kHz			17		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _J	= +25°C		250		mA
I _{PK}	Peak Current ⁽⁹⁾	T _J = +25°C			2.2		Α

Notes:

8. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (LM7810)

Refer to the test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 16 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	(Conditions	Min.	Тур.	Max.	Unit
		$T_J = +25^{\circ}C$		9.6	10.0	10.4	
Vo	Output Voltage	$I_0 = 5 \text{ mA to}$ $V_1 = 12.5 \text{ V to}$	1 A, P _O ≤ 15 W, o 25 V	9.5	10.0	10.5	V
Doglino	Line Regulation ⁽¹⁰⁾	T,₁ = +25°C	V _I = 12.5 V to 25 V		10	200	mV
Regline		$T_{\rm J} = +25$ C	$V_{I} = 13 \text{ V} \text{ to } 25 \text{ V}$		3	100	
Declard	Load Regulation ⁽¹⁰⁾	T	$I_0 = 5 \text{ mA to } 1.5 \text{ A}$		12	200 mV	
Regload	Load Regulation	T _J = +25°C	$I_0 = 250 \text{ mA to } 750 \text{ mA}$		4	400	IIIV
Ι _Q	Quiescent Current	$T_J = +25^{\circ}C$			5.1	8.0	mA
A I	Quiescent Current	$I_{O} = 5 \text{ mA to}$	1 A			0.5	mA
Δl _Q	Change	$V_{\rm I} = 12.5 \rm V tc$	29 V			1.0	
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽¹¹⁾	I _O = 5 mA			-1		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		58		μV
RR	Ripple Rejection ⁽¹¹⁾	f = 120 Hz, V	_I = 13 V to 23 V	56	71		dB
V _{DROP}	Dropout Voltage	I _O = 1 A, T _J =	+25°C		2		V
R _O	Output Resistance ⁽¹¹⁾	f = 1 kHz			17		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _J	= +25°C		250		mA
I _{PK}	Peak Current ⁽¹¹⁾	T _J = +25°C			2.2		Α

Notes:

10. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (LM7812)

Refer to the test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 19 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	(Conditions	Min.	Тур.	Max.	Unit
		T _J = +25°C		11.5	12.0	12.5	
Vo	Output Voltage	$I_0 = 5 \text{ mA to}$ V _I = 14.5 V to	1 A, P _O ≤ 15 W, o 27 V	11.4	12.0	12.6	V
Poglino	Line Regulation ⁽¹²⁾	T _J = +25°C	V _I = 14.5 V to 30 V		10	240	- mV
Regline		$T_{\rm J} = +25$ C	$V_{I} = 16 \text{ V}$ to 22 V		3	120	
Declard	Load Regulation ⁽¹²⁾	T	$I_0 = 5 \text{ mA to } 1.5 \text{ A}$		11	240	mV
Regload	Load Regulation	T _J = +25°C	I _O = 250 mA to 750 mA		5	120	
Ι _Q	Quiescent Current	T _J = +25°C			5.1	8.0	mA
A I	Quiescent Current I _O = 5 r	$I_0 = 5 \text{ mA to}$	1 A		0.1	0.5	mA
ΔI_Q	Change	V _I = 14.5 V to	o 30 V		0.5	1.0	mA
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽¹³⁾	l _O = 5 mA			-1		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		76		μV
RR	Ripple Rejection ⁽¹³⁾	f = 120 Hz, V	_I = 15 V to 25 V	55	71		dB
V _{DROP}	Dropout Voltage	I _O = 1 A, T _J =	+25°C		2		V
R _O	Output Resistance ⁽¹³⁾	f = 1 kHz			18		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _J	= +25°C		230		mA
I _{PK}	Peak Current ⁽¹³⁾	T _J = +25°C			2.2		A

Notes:

12. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (LM7815)

Refer to the test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 23 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	(Conditions	Min.	Тур.	Max.	Unit
		T _J = +25°C		14.40	15.00	15.60	
Vo	Output Voltage	$I_0 = 5 \text{ mA to}$ V _I = 17.5 V to	1 A, P _O ≤ 15 W, o 30 V	14.25	15.00	15.75	V
Poglino	Line Regulation ⁽¹⁴⁾	T = 125°C	V _I = 17.5 V to 30 V		11	300	mV
Regline		T _J = +25°C	$V_{\rm I} = 20 \text{ V} \text{ to } 26 \text{ V}$		3	150	
Declard	Load Regulation ⁽¹⁴⁾	T 125°C	$I_0 = 5 \text{ mA to } 1.5 \text{ A}$		12	300	mV
Regload	Load Regulation	T _J = +25°C	$I_0 = 250 \text{ mA to } 750 \text{ mA}$		4	150	
ا _Q	Quiescent Current	T _J = +25°C			5.2	8.0	mA
A I	Quiescent Current	$I_0 = 5 \text{ mA to}$	1 A			0.5	mA
ΔI_Q	Change	V _I = 17.5 V to	30 V			1.0	11174
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽¹⁵⁾	I _O = 5 mA			-1		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		90		μV
RR	Ripple Rejection ⁽¹⁵⁾	f = 120 Hz, V	= 18.5 V to 28.5 V	54	70		dB
V _{DROP}	Dropout Voltage	I _O = 1 A, T _J =	+25°C		2		V
R _O	Output Resistance ⁽¹⁵⁾	f = 1 kHz			19		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _J	= +25°C		250		mA
I _{PK}	Peak Current ⁽¹⁵⁾	T _J = +25°C			2.2		А

Notes:

14. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (LM7818)

Refer to the test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 27 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	(Conditions	Min.	Тур.	Max.	Unit
		T _J = +25°C		17.3	18.0	18.7	
Vo	Output Voltage	$I_0 = 5 \text{ mA to}$ $V_1 = 21 \text{ V to } 3$	1 A, P _O ≤ 15 W, 33 V	17.1	18.0	18.9	V
Poglino	Line Regulation ⁽¹⁶⁾		$V_{I} = 21 \text{ V} \text{ to } 33 \text{ V}$		15	360	mV
Regline		T _J = +25°C	$V_{I} = 24 \text{ V} \text{ to } 30 \text{ V}$		5	180	
Declard	Load Regulation ⁽¹⁶⁾	T 125°C	$I_0 = 5 \text{ mA to } 1.5 \text{ A}$		15	360	mV
Regload	Load Regulation	T _J = +25°C	I _O = 250 mA to 750 mA		5	180	
Ι _Q	Quiescent Current	$T_J = +25^{\circ}C$			5.2	8.0	mA
A I	Quiescent Current	$I_0 = 5 \text{ mA to}$	1 A			0.5	0.5 mA
Δl _Q	Change	$V_{I} = 21 \text{ V to } 3$	33 V			1.0	
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽¹⁷⁾	I _O = 5 mA			-1		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		110		μV
RR	Ripple Rejection ⁽¹⁷⁾	f = 120 Hz, V	_I = 22 V to 32 V	53	69		dB
V _{DROP}	Dropout Voltage	I _O = 1 A, T _J =	+25°C		2		V
R _O	Output Resistance ⁽¹⁷⁾	f = 1 kHz			22		mΩ
I _{SC}	Short-Circuit Current	$V_{I} = 35 V, T_{J}$	= +25°C		250		mA
I _{PK}	Peak Current ⁽¹⁷⁾	T _J = +25°C			2.2		Α

Notes:

16. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (LM7824)

Refer to the test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 33 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	(Conditions	Min.	Тур.	Max.	Unit
		$T_J = +25^{\circ}C$		23.00	24.00	25.00	
Vo	Output Voltage	$I_0 = 5 \text{ mA to}$ $V_1 = 27 \text{ V to } 3$	1 A, P _O ≤15 W, 88 V	22.80	24.00	25.25	V
Regline	Line Regulation ⁽¹⁸⁾	T _{.1} = +25°C	$V_{\rm I} = 27 {\rm V}$ to 38 V		17	480	mV
Regime		$T_{\rm J} = +25$ C	$V_{I} = 30 \text{ V} \text{ to } 36 \text{ V}$		6	240	mv
Declard	Load Regulation ⁽¹⁸⁾	T	$I_0 = 5 \text{ mA to } 1.5 \text{ A}$		15	480	mV
Regload	Load Regulation	T _J = +25°C	I _O = 250 mA to 750 mA		5	240	ΠIV
Ι _Q	Quiescent Current	$T_J = +25^{\circ}C$			5.2	8.0	mA
A I	Quiescent Current	$I_{O} = 5 \text{ mA to}$	1 A		0.1	0.5	
Δl _Q	Change	V ₁ = 27 V to 38 V			0.5	1.0	mA
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽¹⁹⁾	l _O = 5 mA			-1.5		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		120		μV
RR	Ripple Rejection ⁽¹⁹⁾	f = 120 Hz, V	_I = 28 V to 38 V	50	67		dB
V _{DROP}	Dropout Voltage	I _O = 1 A, T _J =	+25°C		2		V
R _O	Output Resistance ⁽¹⁹⁾	f = 1 kHz			28		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _J	= +25°C		230		mA
I _{PK}	Peak Current ⁽¹⁹⁾	T _J = +25°C			2.2		А

Notes:

18. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (LM7805A)

Refer to the test circuit, $0^{\circ}C < T_J < 125^{\circ}C$, $I_O = 1$ A, $V_I = 10$ V, $C_I = 0.33 \ \mu$ F, $C_O = 0.1 \ \mu$ F, unless otherwise specified.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
		$T_J = +25^{\circ}C$	4.9	5.0	5.1	
V _O	Output Voltage	$I_{O} = 5 \text{ mA to 1 A}, P_{O} \le 15 \text{ W},$ V _I = 7.5 V to 20 V	4.8	5.0	5.2	V
		$V_{I} = 7.5 \text{ V to } 25 \text{ V}, I_{O} = 500 \text{ mA}$		5.0	50.0	
Doglino	Line Degulation ⁽²⁰⁾	V _I = 8 V to 12 V		3.0	50.0	
Regline	Line Regulation ⁽²⁰⁾	$V_{\rm I} = 7.3 \text{ V to } 20 \text{ V}$		5.0	50.0	- mV
		$T_J = +25^{\circ}C$ $V_I = 8 V \text{ to } 12 V$		1.5	25.0	
		$T_{J} = +25^{\circ}C, I_{O} = 5 \text{ mA to } 1.5 \text{ A}$		9	100	
Regload	Load Regulation ⁽²⁰⁾	$I_{O} = 5 \text{ mA to } 1 \text{ A}$		9	100	mV
		I _O = 250 mA to 750 mA		4	50	
Ι _Q	Quiescent Current	$T_J = +25^{\circ}C$		5	6	mA
		$I_{O} = 5 \text{ mA to } 1 \text{ A}$			0.5	
ΔI_Q	Quiescent Current Change	$V_{I} = 8 V \text{ to } 25 V, I_{O} = 500 \text{ mA}$			0.8	mA
	onango	$V_{I} = 7.5 \text{ V to } 20 \text{ V}, \text{ T}_{J} = +25^{\circ}\text{C}$			0.8	
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽²¹⁾	I _O = 5 mA		-0.8		mV/°C
V _N	Output Noise Voltage	$f = 10 \text{ Hz to } 100 \text{ kHz}, T_A = +25^{\circ}\text{C}$		42		μV
RR	Ripple Rejection ⁽²¹⁾	f = 120 Hz, V _O = 500 mA, V _I =8 V to 18 V		68		dB
V _{DROP}	Dropout Voltage	I _O = 1 A, T _J = +25°C		2		V
R _O	Output Resistance ⁽²¹⁾	f = 1 kHz		17		mΩ
I _{SC}	Short-Circuit Current	$V_{I} = 35 V, T_{J} = +25^{\circ}C$		250		mA
I _{PK}	Peak Current ⁽²¹⁾	T _J = +25°C		2.2		A

Notes:

20. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (LM7809A)

Refer to the test circuit, 0°C < T_J < 125°C, I_O = 1 A, V_I = 15 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	Conditions		Min.	Тур.	Max.	Unit
		$T_J = +25^{\circ}C$		8.82	9.00	9.16	
V _O	V _O Output Voltage		$I_O = 5 \text{ mA to 1 A}, P_O \le 15 \text{ W},$ $V_I = 11.2 \text{ V to 24 V}$		9.00	9.35	V
	Line Regulation ⁽²²⁾	$V_{I} = 11.7 \text{ V to } 25 \text{ V}, I_{O} = 500 \text{ mA}$			6	90	
Poglino		$V_{\rm I} = 12.5 \ V \ tc$	9 19 V		4	45	m\/
Regline		T _J = +25°C	$V_{I} = 11.5$ V to 24 V $V_{I} = 12.5$ V to 19 V		6	90	- mV
			V _I = 12.5 V to 19 V		2	45	
		$T_J = +25^{\circ}C$, $I_O = 5$ mA to 1.5 A			12	100	mV
Regload	Load Regulation ⁽²²⁾	$I_{O} = 5 \text{ mA to 1 A}$			12	100	
		I _O = 250 mA to 750 mA			5	50	
Ι _Q	Quiescent Current	$T_J = +25^{\circ}C$			5	6	mA
	لالم Quiescent Current Change	$I_{O} = 5 \text{ mA to } 1 \text{ A}$				0.5	
∆l _Q		$V_{I} = 12 \text{ V to } 25 \text{ V}, I_{O} = 500 \text{ mA}$				0.8	mA
		$V_{I} = 11.7 \text{ V to } 25 \text{ V}, T_{J} = +25^{\circ}\text{C}$				0.8	
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽²³⁾	I _O = 5 mA			-1		mV/°C
V _N	Output Noise Voltage	$f = 10 \text{ Hz to } 100 \text{ kHz}, T_A = +25^{\circ}\text{C}$			58		μV
RR	Ripple Rejection ⁽²³⁾				dB		
V _{DROP}	Dropout Voltage	I _O = 1 A, T _J = +25°C			2		V
R _O	Output Resistance ⁽²³⁾	f = 1 kHz			17		mΩ
I _{SC}	Short-Circuit Current	$V_{I} = 35 V, T_{J} = +25^{\circ}C$			250		mA
I _{PK}	Peak Current ⁽²³⁾	T _J = +25°C			2.2		А

Notes:

22. Load and line regulation are specified at constant junction temperature. Changes in V_0 due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (LM7810A)

Refer to the test circuit, $0^{\circ}C < T_J < 125^{\circ}C$, $I_O = 1$ A, $V_I = 16$ V, $C_I = 0.33 \ \mu$ F, $C_O = 0.1 \ \mu$ F, unless otherwise specified.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
		$T_J = +25^{\circ}C$	9.8	10.0	10.2		
Vo	Output Voltage	$I_{O} = 5 \text{ mA to } 1 \text{ A}, P_{O} \le 15 \text{ W},$ $V_{I} = 12.8 \text{ V to } 25 \text{ V}$	9.6	10.0	10.4	V	
		$V_{I} = 12.8 \text{ V to } 26 \text{ V}, I_{O} = 500 \text{ mA}$		8	100		
Regline	Line Regulation ⁽²⁴⁾	V _I = 13 V to 20 V		4	50	mV	
Regilite		$T_J = +25^{\circ}C$ $V_I = 12.5 V to 25 V$		8	100		
		$V_1 = 13 \text{ V} \text{ to } 20 \text{ V}$		3	50		
		$T_{J} = +25^{\circ}C, I_{O} = 5 \text{ mA to } 1.5 \text{ A}$		12	100		
Regload	Load Regulation ⁽²⁴⁾	I _O = 5 mA to 1 A		12	100	mV	
		I _O = 250 mA to 750 mA		5	50		
Ι _Q	Quiescent Current	$T_J = +25^{\circ}C$		5	6	mA	
		$I_{O} = 5 \text{ mA to } 1 \text{ A}$			0.5		
ΔI _Q Quiescent Current Change	$V_{\rm I} = 12.8 \text{ V to } 25 \text{ V}, \text{ I}_{\rm O} = 500 \text{ mA}$			0.8	mA		
	onango	$V_{I} = 13 \text{ V to } 26 \text{ V}, \text{ T}_{J} = +25^{\circ}\text{C}$			0.5		
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽²⁵⁾	I _O = 5 mA		-1		mV/°C	
V _N	Output Noise Voltage	$f = 10 \text{ Hz to } 100 \text{ kHz}, T_A = +25^{\circ}\text{C}$		58		μV	
RR	Ripple Rejection ⁽²⁵⁾	f = 120 Hz, V_0 = 500 mA, V ₁ =14 V to 24 V		62		dB	
V _{DROP}	Dropout Voltage	$I_{O} = 1 \text{ A}, T_{J} = +25^{\circ}\text{C}$ 2			V		
R _O	Output Resistance ⁽²⁵⁾	f = 1 kHz		17		mΩ	
I _{SC}	Short-Circuit Current	$V_{I} = 35 \text{ V}, \text{ T}_{J} = +25^{\circ}\text{C}$		250		mA	
I _{PK}	Peak Current ⁽²⁵⁾	$T_J = +25^{\circ}C$		2.2		A	

Notes:

24. Load and line regulation are specified at constant junction temperature. Changes in V_0 due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (LM7812A)

Refer to the test circuit, $0^{\circ}C < T_J < 125^{\circ}C$, $I_O = 1$ A, $V_I = 19$ V, $C_I = 0.33 \ \mu$ F, $C_O = 0.1 \ \mu$ F, unless otherwise specified.

Symbol	Parameter	Conditions		Min.	Тур.	Max.	Unit
		$T_J = +25^{\circ}C$		11.75	12.00	12.25	
V _O	V _O Output Voltage		$I_O = 5 \text{ mA to 1 A}, P_O \le 15 \text{ W},$ $V_I = 14.8 \text{ V to 27 V}$		12.00	12.50	V
		V _I = 14.8 V to 30 V, I _O = 500 mA			10	120	
Doglino	Line Regulation ⁽²⁶⁾	V _I = 16 V to 2	2 V		4	120	m\/
Regline		T	$V_1 = 14.5$ V to 27 V $V_1 = 16$ V to 22 V		10	120	mV
		T _J = +25°C	V _I = 16 V to 22 V		3	60	
		$T_{J} = +25^{\circ}C, I_{O} = 5 \text{ mA to } 1.5 \text{ A}$			12	100	mV
Regload I	Load Regulation ⁽²⁶⁾	$I_0 = 5 \text{ mA to 1 A}$			12	100	
		I _O = 250 mA to 750 mA			5	50	
Ι _Q	Quiescent Current	T _J = +25°C			5	6	mA
	Quiescent Current Change	$I_{O} = 5 \text{ mA to } 1 \text{ A}$				0.5	
ΔI_Q		$V_{I} = 14 \text{ V to } 27 \text{ V}, I_{O} = 500 \text{ mA}$				0.8	mA
	onange	$V_{I} = 15 \text{ V to } 30 \text{ V}, \text{ T}_{J} = +25^{\circ}\text{C}$				0.8	
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽²⁷⁾	$I_0 = 5 \text{ mA}$			-1		mV/°C
V _N	Output Noise Voltage	$f = 10 \text{ Hz to } 100 \text{ kHz}, T_A = +25^{\circ}\text{C}$			76		μV
RR	Ripple Rejection ⁽²⁷⁾	f = 120 Hz, V_0 = 500 mA, V ₁ =14 V to 24 V 60			dB		
V _{DROP}	Dropout Voltage	I _O = 1 A, T _J = +25°C			2		V
R _O	Output Resistance ⁽²⁷⁾	f = 1 kHz			18		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _J = +25°C			250		mA
I _{PK}	Peak Current ⁽²⁷⁾	T _{.1} = +25°C			2.2		A

Notes:

26. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

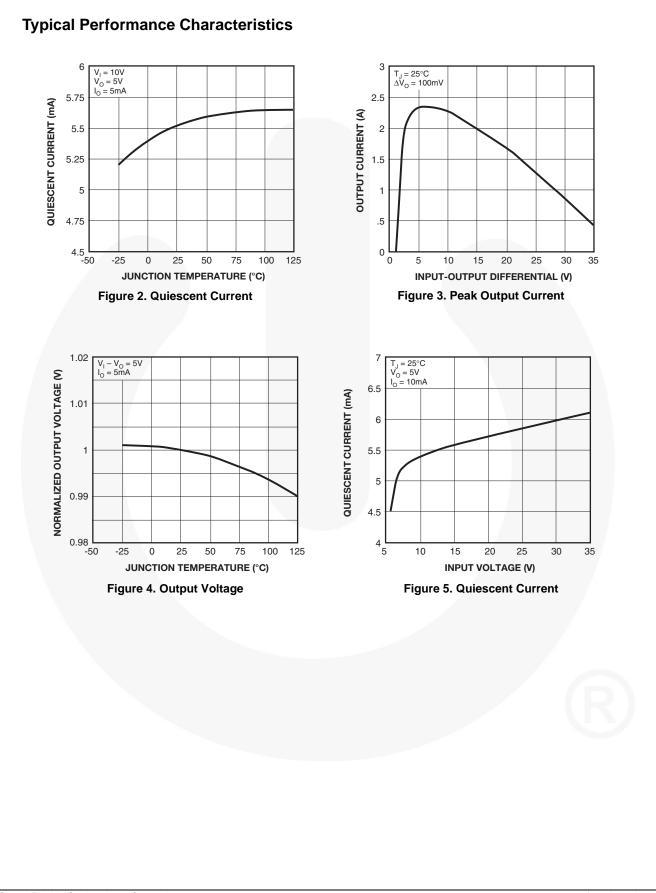
Electrical Characteristics (LM7815A)

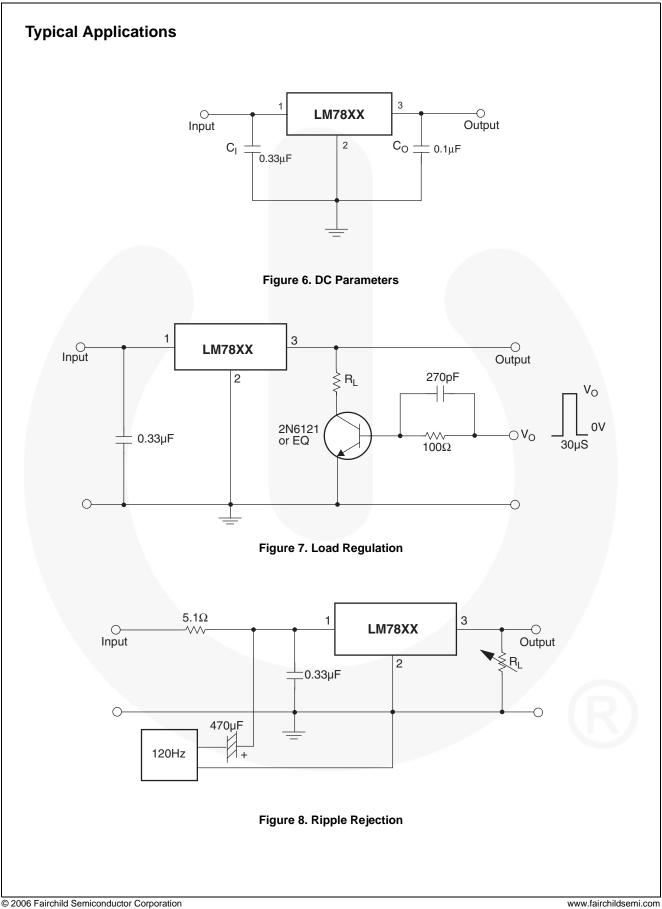
Refer to the test circuit, $0^{\circ}C < T_J < 125^{\circ}C$, $I_O = 1$ A, $V_I = 23$ V, $C_I = 0.33 \ \mu$ F, $C_O = 0.1 \ \mu$ F, unless otherwise specified.

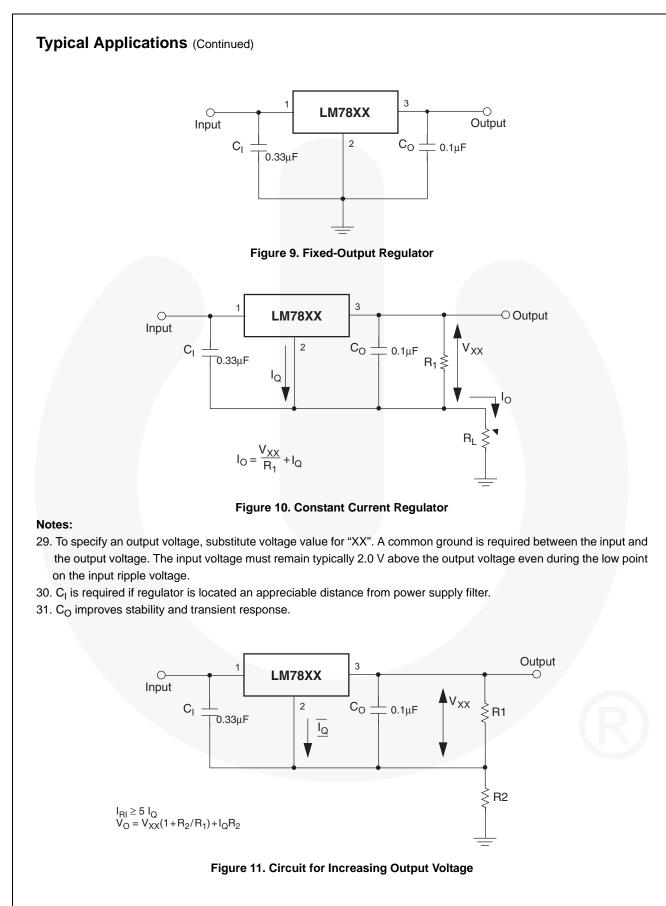
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
	$T_J = +25^{\circ}C$	14.75	15.00	15.30		
V _O	Output Voltage	$I_{O} = 5 \text{ mA to 1 A}, P_{O} \le 15 \text{ W},$ $V_{I} = 17.7 \text{ V to 30 V}$	14.40	15.00	15.60	V
		$V_{I} = 17.4 \text{ V to } 30 \text{ V}, I_{O} = 500 \text{ mA}$		10	150	
Doglino	Line Regulation ⁽²⁸⁾	V _I = 20 V to 26 V		5	150	m\/
Regline		$V_{\rm I} = 17.5 \text{ V to } 30 \text{ V}$		11	150	mV
		$T_J = +25^{\circ}C$ $V_I = 20 V$ to 26 V		3	75	
		$T_J = +25^{\circ}C, I_O = 5 \text{ mA to } 1.5 \text{ A}$		12	100	
Regload Load Regula	Load Regulation ⁽²⁸⁾	$I_{O} = 5 \text{ mA to } 1 \text{ A}$		12	100	mV
		I _O = 250 mA to 750 mA		5	50	
Ι _Q	Quiescent Current	$T_J = +25^{\circ}C$		5.2	6.0	mA
		$I_{O} = 5 \text{ mA to } 1 \text{ A}$			0.5	
	Quiescent Current Change	$V_{I} = 17.5 \text{ V to } 30 \text{ V}, I_{O} = 500 \text{ mA}$			0.8	mA
	onango	$V_{I} = 17.5 \text{ V to } 30 \text{ V}, \text{ T}_{J} = +25^{\circ}\text{C}$			0.8	
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽²⁹⁾	I _O = 5 mA		-1		mV/°C
V _N	Output Noise Voltage	$f = 10 \text{ Hz to } 100 \text{ kHz}, T_A = +25^{\circ}\text{C}$		90		μV
RR	Ripple Rejection ⁽²⁹⁾	f = 120 Hz, V_0 = 500 mA, V _I =18.5 V to 28.5 V 58			dB	
V _{DROP}	Dropout Voltage	I _O = 1 A, T _J = +25°C		2		V
R _O	Output Resistance ⁽²⁹⁾	f = 1 kHz		19		mΩ
I _{SC}	Short-Circuit Current	$V_{I} = 35 \text{ V}, \text{ T}_{J} = +25^{\circ}\text{C}$		250		mA
I _{PK}	Peak Current ⁽²⁹⁾	T _J = +25°C		2.2		Α

Notes:

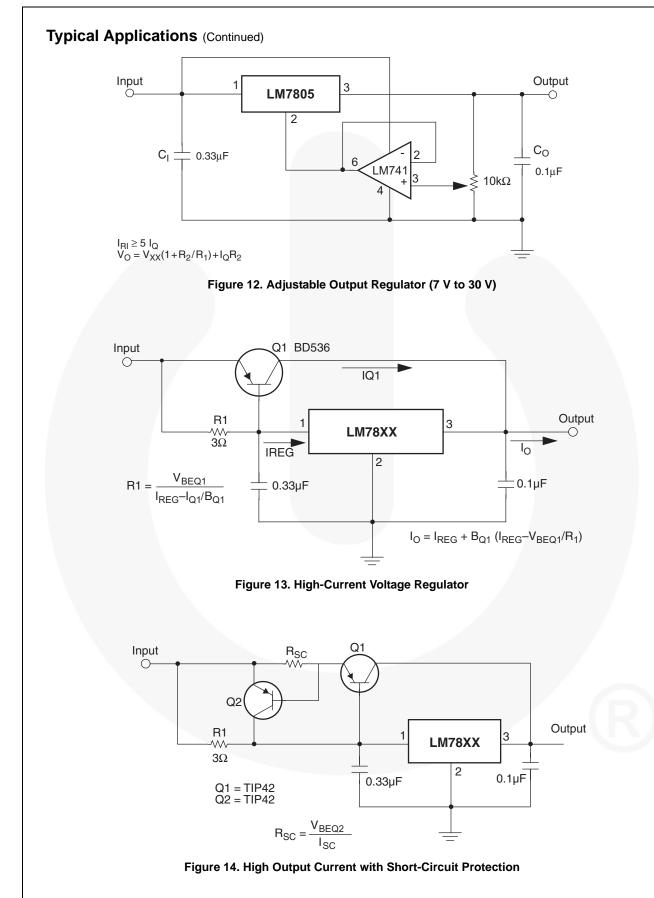
28. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

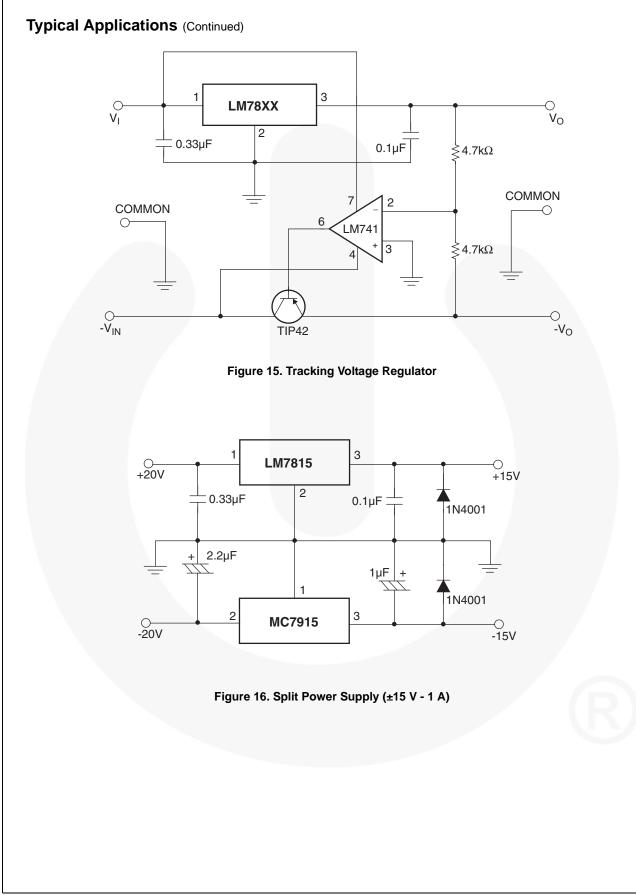


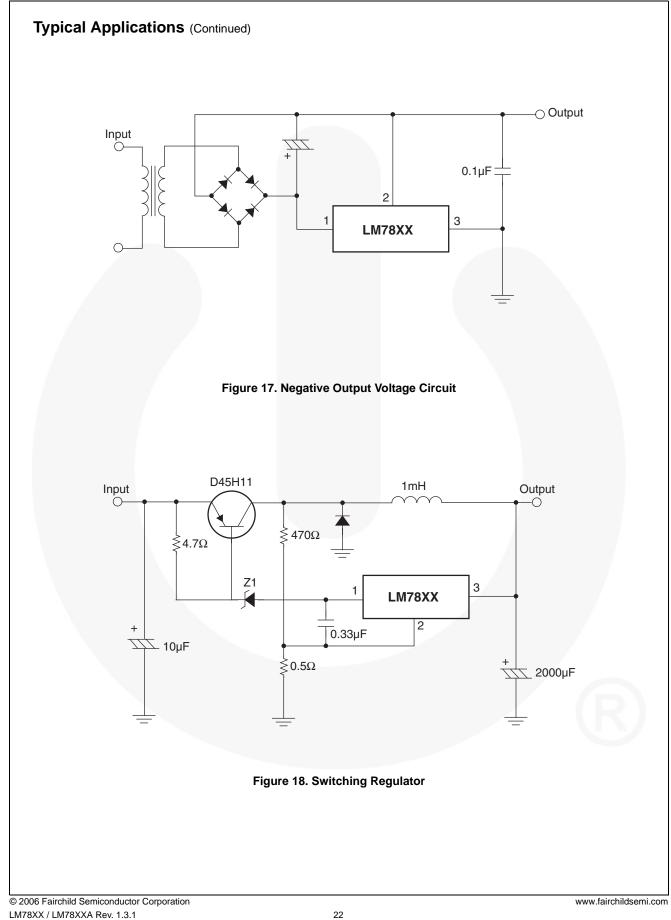


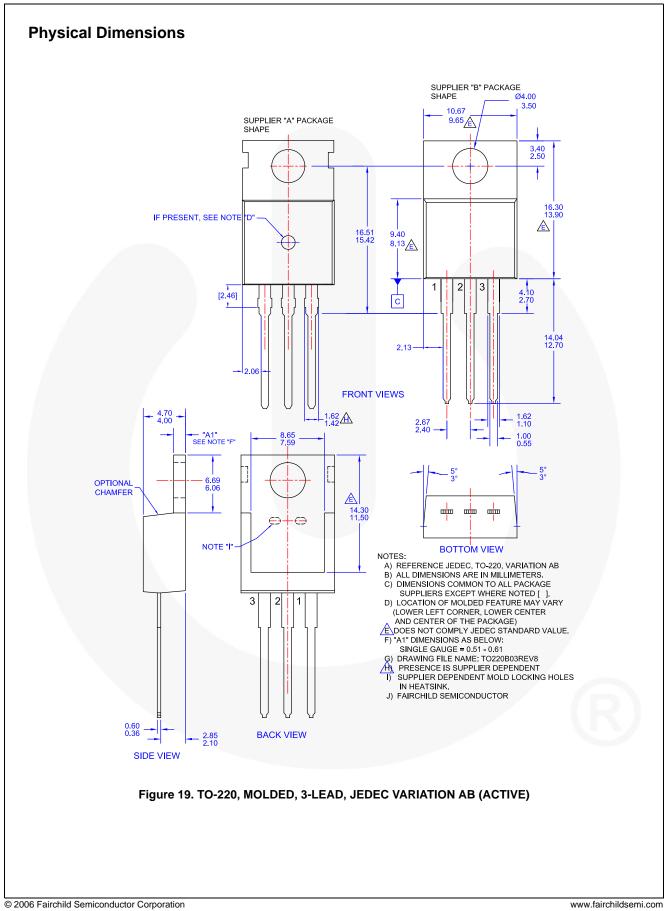












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