

July 2016

# MMBF4091/MMBF4092/MMBF4093 N-Channel Switch

#### **Features**

- This device is designed for low level analog switching applications, sample and hold circuits and chopper stabilized amplifiers.
- Sourced from Process 51.



# Ordering Information

Part Number	Top Mark	Package	Packing Method
MMBF4091	61J	SOT 23	Tape and Reel
MMBF4092	61K	SOT 23	Tape and Reel
MMBF4093	61L	SOT 23	Tape and Reel

### Absolute Maximum Ratings(1), (2)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>DG</sub>	Drain-Gate Voltage	40	V
$V_{GS}$	Gate-Source Voltage	-40	V
I <sub>GF</sub>	Forward Gate Current	50	mA
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to 150	°C

#### Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

## **Thermal Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Max.	Unit
В	Total Device Dissipation	350	mW
$P_{D}$	Derate Above 25°C	2.8	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient <sup>(3)</sup>	357	°C/W

### Notes:

3. Device mounted on FR-4 PCB, 1.6" x 1.6" x 0.06".

## **Electrical Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions		Min.	Max.	Unit
Off Chara	cteristics					*
V <sub>(BR)GSS</sub>	Gate-Source Breakdown Voltage	$I_G = 1 \mu A, V_{DS} = 0$		-40		V
V <sub>GS</sub> (off)	Gate-Source Cut-Off Voltage	$V_{DS} = 20 \text{ V}, I_D = 1 \text{ nA}$	MMBF4091 MMBF4092 MMBF4093	-5.0 -2.0 -1.0	-10.0 -7.0 -5.0	V
I <sub>DGO</sub>	Drain-Gate Leakage Current	$V_{DS} = 20 \text{ V}, I_{S} = 0$ $V_{DS} = 20 \text{ V}, I_{S} = 0, T_{A} = 15$	50°C		-200 -400	pA nA
I <sub>D</sub> (off)	Drain Cutoff Leakage Current	$\begin{split} &V_{DS} = 20 \text{ V, } V_{GS} = -12 \text{ V} \\ &V_{DS} = 20 \text{ V, } V_{GS} = -8 \text{ V} \\ &V_{DS} = 20 \text{ V, } V_{GS} = -6 \text{ V} \\ &V_{DS} = 20 \text{ V, } V_{GS} = -12 \text{ V,} \\ &T_{A} = 150 ^{\circ}\text{C} \\ &V_{DS} = 20 \text{ V, } V_{GS} = -8 \text{ V,} \\ &T_{A} = 150 ^{\circ}\text{C} \\ &V_{DS} = 20 \text{ V, } V_{GS} = -6 \text{ V,} \\ &T_{A} = 150 ^{\circ}\text{C} \end{split}$	MMBF4091 MMBF4093 MMBF4091 MMBF4092 MMBF4093		200 200 200 400 400	pA pA pA nA nA
On Chara	cteristics	!				1
I <sub>DSS</sub>	Zero-Gate Voltage Drain Current <sup>(4)</sup>	$V_{DS} = 20 \text{ V}, I_{GS} = 0$	MMBF4091 MMBF4092 MMBF4093	30 15 8		mA
V <sub>DS</sub> (on)	Drain-Source On Voltage	$\begin{split} I_D &= 6.6 \text{ mA}, \text{ V}_{GS} = 0 \\ I_D &= 4.0 \text{ mA}, \text{ V}_{GS} = 0 \\ I_D &= 2.5 \text{ mA}, \text{ V}_{GS} = 0 \end{split}$	MMBF4091 MMBF4092 MMBF4093		0.2 0.2 0.2	V
r <sub>DS</sub> (on)	Drain-Source On Resistance	$I_D = 1 \text{ mA}, V_{GS} = 0$	MMBF4091 MMBF4092 MMBF4093		30 50 80	Ω
Small Sig	nal Characteristics				•	·
r <sub>DS</sub> (on)	Drain-Source On Resistance	$V_{DS} = V_{GS} = 0$ , f = 1 kHz	MMBF4091 MMBF4092 MMBF4093		30 50 80	Ω
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V, f =	1.0 MHz		16	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	V <sub>DS</sub> = -20 V, f = 1.0 MHz			5	pF

Symbol	Parameter	Condit	ions	Min.	Max.	Unit
Switching Characteristics						
t <sub>On</sub>	Turn-On Time	$I_{D(on)} = 12 \text{ mA}$ $I_{D(on)} = 6.0 \text{ mA}$ $I_{D(on)} = 3.0 \text{ mA}$	MMBF4091 MMBF4092 MMBF4093		25 35 60	ns ns ns
t <sub>Off</sub>	Turn-Off Time	$V_{GS(off)} = 12 \text{ V} $ $V_{GS(off)} = 6.0 \text{ V} $ $V_{GS(off)} = 3.0 \text{ V} $	MMBF4091 MMBF4092 MMBF4093		40 60 80	ns ns ns

#### Note:

4. Pulse test: pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  1%.







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Definition of Terms				
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