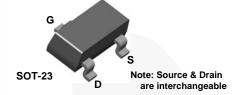


January 2015

# MMBF4391 / MMBF4392 / MMBF4393 N-Channel Switch

## **Description**

This device is designed for low level analog switching, sample and hold circuits and chopper stabalized amplifiers. Sourced from process 51. See J111 for characteristics.



# **Ordering Information**

Part Number	Top Mark	Package	Packing Method
MMBF4391	6J	SOT-23 3L	Tape and Reel
MMBF4392	6K	SOT-23 3L	Tape and Reel
MMBF4393	6G	SOT-23 3L	Tape and Reel

# **Absolute Maximum Ratings**(1), (2)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}\text{C}$  unless otherwise noted.

Symbol	Parameter	Value	Unit
$V_{DG}$	Drain-Gate Voltage	30	V
$V_{GS}$	Gate-Source Voltage	-30	V
I <sub>GF</sub>	Forward Gate Current	50	mA
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to 150	°C

### Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

### Thermal Characteristics(3)

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Max.	Unit
Total Device Dissipation		350	mW
$P_{D}$	Derate Above 25°C	2.8	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	357	°C/W

### Note:

3. Device mounted on FR-4 PCB 36mm × 18mm × 1.5mm; mounting pad for the collector lead minimum 6cm<sup>2</sup>.

### **Electrical Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions		Min.	Max.	Unit
Off Characte	eristics					
V <sub>(BR)GSS</sub>	Gate-Source Breakdown Voltage	$I_G = 1.0 \mu\text{A},  V_{DS} = 0$		-30		V
I <sub>GSS</sub>	Gate Reverse Current	$V_{GS} = -15 \text{ V}, V_{DS} = 0$			-1.0	nA
		V <sub>GS</sub> = -15 V, V <sub>DS</sub> = 0, T <sub>A</sub> = 150°C			-0.2	μΑ
	Gate-Source Cut-Off Voltage		MMBF4391	-4.0	-10.0	
$V_{GS(off)}$		$V_{DS} = 20 \text{ V}, I_{D} = 1.0 \text{ nA}$	MMBF4392	-2.0	-5.0	V
			MMBF4393	-0.5	-3.0	
V <sub>GS(f)</sub>	Gate-Source Forward Voltage	$I_G = 1.0 \text{ mA}, V_{DS} = 0$			1.0	V
		$V_{DS} = 20 \text{ V}, V_{GS} = -12 \text{ V}$	MMBF4391		0.1	nA μA
	Drain Cut-Off Leakage Current	$V_{DS} = 20 \text{ V}, V_{GS} = -7.0 \text{ V}$	MMBF4392		0.1	
		$V_{DS} = 20 \text{ V}, V_{GS} = -5.0 \text{ V}$	MMBF4393		0.1	
I <sub>D(off)</sub>		$V_{DS} = 20 \text{ V}, V_{GS} = -12 \text{ V},$ $T_A = 150^{\circ}\text{C}$	MMBF4391		0.2	
		$V_{DS} = 20 \text{ V}, V_{GS} = -7.0 \text{ V},$ $T_A = 150^{\circ}\text{C}$	MMBF4392		0.2	
		$V_{DS} = 20 \text{ V}, V_{GS} = -5.0 \text{ V},$ $T_A = 150^{\circ}\text{C}$	MMBF4393		0.2	
On Characte	eristics					
	Zero-Gate Voltage Drain Current <sup>(4)</sup>	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0	MMBF4391	50	150	mA
I <sub>DSS</sub>			MMBF4392	25	75	
			MMBF4393	5.0	30	
V <sub>DS(on)</sub>	Drain-Source On Voltage	$I_D = 12 \text{ mA}, V_{GS} = 0$	MMBF4391		0.4	V
		$I_D = 6.0 \text{ mA}, V_{GS} = 0$	MMBF4392		0.4	
		$I_D = 3.0 \text{ mA}, V_{GS} = 0$	MMBF4393		0.4	
	Drain-Source On Resistance		MMBF4391		30	
r <sub>DS(on)</sub>		D , G3 ,	MMBF4392		60	Ω
			MMBF4393		100	]

### Note:

4. Pulse test: pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2.0\%$ 

# **Electrical Characteristics** (Continued)

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Conditions		Min.	Max.	Unit
Small Signal Characteristics						
			MMBF4391		30	
r <sub>ds(on)</sub>	Drain-Source On Resistance	$V_{DS} = V_{GS} = 0$ , $f = 1kHz$	MMBF4392		60	Ω
, ,			MMBF4393		100	
C <sub>iss</sub>	Input Capacitance	$V_{DS} = 20 \text{ V}, V_{GS} = 0, f = 1.$	0 MHz		14	pF
		V <sub>GS</sub> = -12 V, f = 1.0 MHz	MMBF4391		3.5	pF
	Reverse Transfer Capacitance	$V_{GS} = -7.0 \text{ V}, f = 1.0 \text{ MHz}$	MMBF4392		3.5	
		$V_{GS} = -5.0 \text{ V}, f = 1.0 \text{ MHz}$	MMBF4393		3.5	
Switching C	Characteristics					
t <sub>r</sub> Rise		I <sub>D(on)</sub> = 12 mA	MMBF4391		5.0	
	Rise Time	$I_{D(on)} = 6.0 \text{ mA}$	MMBF4392		5.0	ns
		$I_{D(on)} = 3.0 \text{ mA}$	MMBF4393		5.0	
t <sub>f</sub> Fall Time		V <sub>GS(off)</sub> = 12 V	MMBF4391		15	
	Fall Time	$V_{GS(off)} = 6.0 \text{ V}$	MMBF4392		20	ns
		$V_{GS(off)} = 3.0 \text{ V}$	MMBF4393		30	
t <sub>on</sub>		I <sub>D(on)</sub> = 12 mA	MMBF4391		15	
	Turn-On Time	$I_{D(on)} = 6.0 \text{ mA}$	MMBF4392		15	ns
		$I_{D(on)} = 3.0 \text{ mA}$	MMBF4393		15	
		V <sub>GS(off)</sub> = 12 V	MMBF4391		20	
t <sub>off</sub>	Turn-Off Time	$V_{GS(off)} = 6.0 \text{ V}$	MMBF4392		35	ns
		$V_{GS(off)} = 3.0 \text{ V}$	MMBF4393		50	

## **Physical Dimensions** 0.95 2.92±0.20 3 1.40 1.30+0.20 2.20 2 0.60 0.37 (0.29) -0.95 ⊕ 0.20M A B 1.00 1.90 1.90 LAND PATTERN RECOMMENDATION SEE DETAIL A -1.20 MAX 0.10 (0.93) ○ 0.10 M C С 2.40±0.30 NOTES: UNLESS OTHERWISE SPECIFIED **GAGE PLANE** A) REFERENCE JEDEC REGISTRATION TO-236, VARIATION AB, ISSUE H. 0.23 B) ALL DIMENSIONS ARE IN MILLIMETERS. C) DIMENSIONS ARE INCLUSIVE OF BURRS, 0.25 MOLD FLASH AND TIE BAR EXTRUSIONS. D) DIMENSIONING AND TOLERANCING PER ASME Y14.5M - 1994. 0.20 MIN SEATING E) DRAWING FILE NAME: MA03DREV10 **PLANE** (0.55)**DETAIL A**

Figure 1. 3-LEAD, SOT23, JEDEC TO-236, LOW PROFILE





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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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