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August 2015

# MOC205M, MOC206M, MOC207M, MOC211M, MOC212M, MOC213M, MOC216M, MOC217M 8-pin SOIC Single-Channel Phototransistor Output Optocoupler

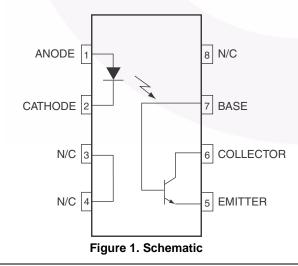
### Features

- Closely Matched Current Transfer Ratios
- Minimum BV<sub>CEO</sub> of 70 V Guaranteed
- MOC205M, MOC206M, MOC207M
- Minimum BV<sub>CEO</sub> of 30 V Guaranteed
  - MOC211M, MOC212M, MOC213M, MOC216M, MOC217M
- Low LED Input Current Required for Easier Logic Interfacing
  - MOC216M, MOC217M
- Convenient Plastic SOIC-8 Surface Mountable Package Style, with 0.050" Lead Spacing
- Safety and Regulatory Approvals:
  - UL1577, 2,500 VAC<sub>RMS</sub> for 1 Minute
  - DIN-EN/IEC60747-5-5, 565 V Peak Working Insulation Voltage

## Applications

- Feedback Control Circuits
- Interfacing and Coupling Systems of Different Potentials and Impedances
- General Purpose Switching Circuits
- Monitor and Detection Circuits

#### Schematic



## Description

These devices consist of a gallium arsenide infrared emitting diode optically coupled to a monolithic silicon phototransistor detector, in a surface mountable, small outline, plastic package. They are ideally suited for high-density applications, and eliminate the need for through-the-board mounting.

### Package Outline

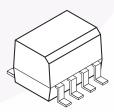


Figure 2. Package Outline

# Safety and Insulation Ratings

As per DIN EN/IEC 60747-5-5, this optocoupler is suitable for "safe electrical insulation" only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.

Parameter		Characteristics
Installation Classifications per DIN VDE	< 150 V <sub>RMS</sub>	I–IV
0110/1.89 Table 1, For Rated Mains Voltage	< 300 V <sub>RMS</sub>	I–III
Climatic Classification		55/100/21
Pollution Degree (DIN VDE 0110/1.89)		2
Comparative Tracking Index		175

Symbol	Parameter	Value	Unit
Input-to-Output Test Voltage, Method A, $V_{IORM} \times 1.6 = V_{PR}$ , Type and Sample Test with $t_m = 10$ s, Partial Discharge < 5 pC		904	V <sub>peak</sub>
V PR	$V_{PR}$ Input-to-Output Test Voltage, Method B, $V_{IORM} \times 1.875 = V_{PR}$ , 100% Production Test with $t_m = 1$ s, Partial Discharge < 5 pC		V <sub>peak</sub>
V <sub>IORM</sub>	Maximum Working Insulation Voltage	565	V <sub>peak</sub>
V <sub>IOTM</sub>	Highest Allowable Over-Voltage	4000	V <sub>peak</sub>
	External Creepage	≥ 4	mm
	External Clearance	≥ 4	mm
DTI	Distance Through Insulation (Insulation Thickness)	≥ 0.4	mm
Τ <sub>S</sub>	Case Temperature <sup>(1)</sup>	150	°C
I <sub>S,INPUT</sub>	Input Current <sup>(1)</sup>	200	mA
P <sub>S,OUTPUT</sub>	Output Power <sup>(1)</sup>	300	mW
R <sub>IO</sub>	Insulation Resistance at T <sub>S</sub> , $V_{IO} = 500 V^{(1)}$	> 10 <sup>9</sup>	Ω

#### Note:

1. Safety limit values - maximum values allowed in the event of a failure.

MOC20xM, MOC21xM — 8-pin SOIC Single-Channel Phototransistor Output Optocoupler

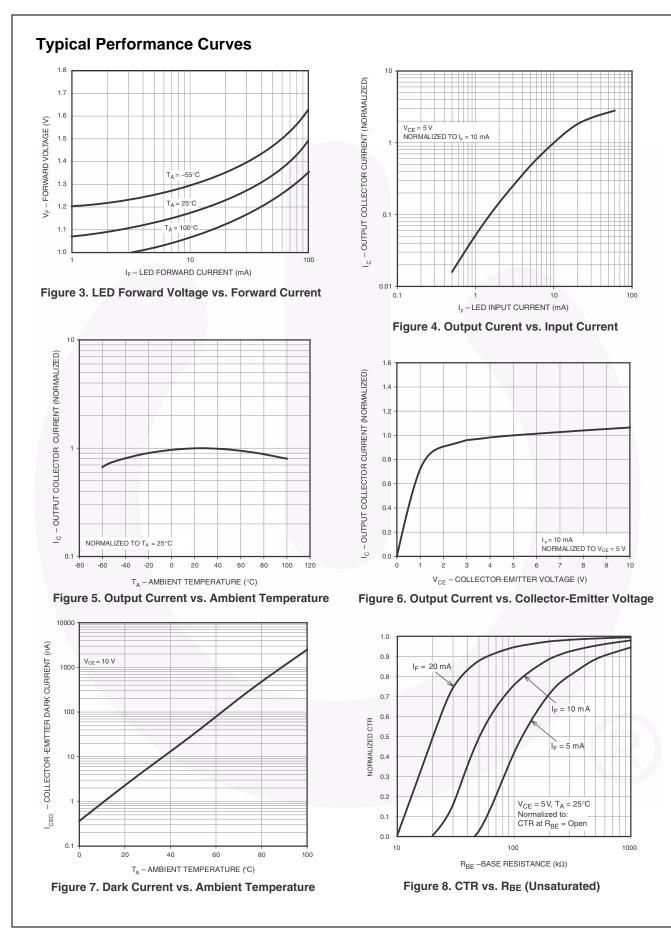
# **Absolute Maximum Ratings**

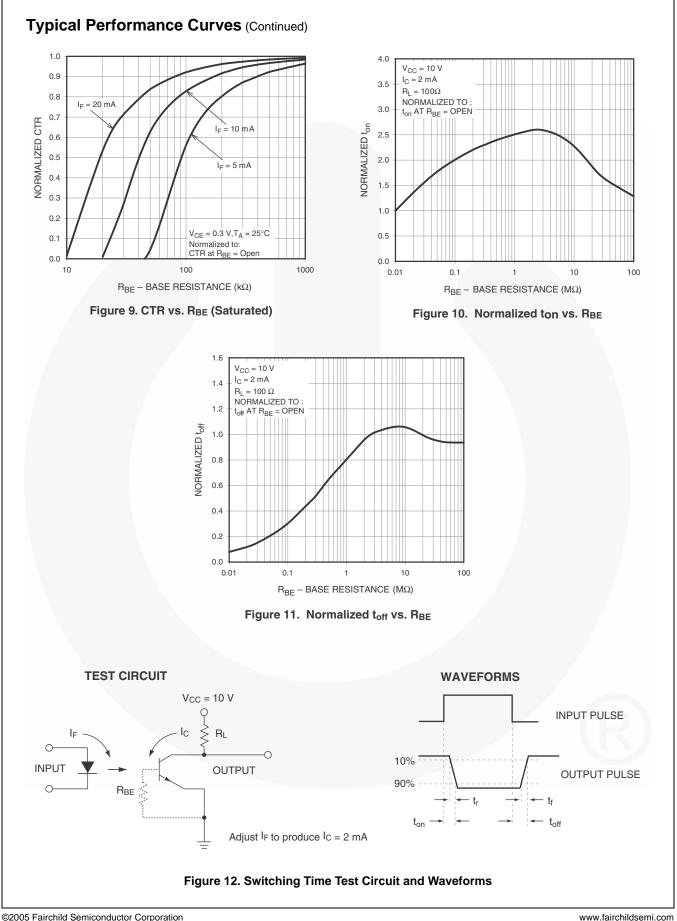
Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.  $T_A = 25^{\circ}C$  unless otherwise specified.

Symbol	Rating	Value	Unit
TOTAL DEVIC	CE		
T <sub>STG</sub>	Storage Temperature	-40 to +125	°C
T <sub>A</sub>	Ambient Operating Temperature	-40 to +100	°C
Τ <sub>J</sub>	Junction Temperature	-40 to +125	°C
T <sub>SOL</sub>	Lead Solder Temperature	260 for 10 seconds	°C
Р	Total Device Power Dissipation @ $T_A = 25^{\circ}C$	240	mW
PD	Derate above 25°C	2.94	mW/°C
EMITTER			
١ <sub>F</sub>	Continuous Forward Current	60	mA
I <sub>F</sub> (pk)	Forward Current – Peak (PW = 100 µs, 120 pps)	1.0	А
V <sub>R</sub>	Reverse Voltage	6.0	V
D	LED Power Dissipation @ T <sub>A</sub> = 25°C	90	mW
PD	Derate above 25°C	0.8	mW/°C
DETECTOR			
۱ <sub>C</sub>	Continuous Collector Current	150	mA
V <sub>CEO</sub>	Collector-Emitter Voltage	30	V
V <sub>ECO</sub>	Emitter-Collector Voltage	7	V
	Detector Power Dissipation @ T <sub>A</sub> = 25°C	150	mW
PD	Derate above 25°C	1.76	mW/°C

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
EMITTER	I					
	Input Forward Voltage					
V <sub>F</sub>	MOC216M, MOC217M	$I_F = 1 \text{ mA}$		1.07	1.3	V
۷F	MOC205M, MOC206M, MOC207M MOC211M, MOC212M, MOC213M	I <sub>F</sub> = 10 mA		1.15	1.5	V
I <sub>R</sub>	Reverse Leakage Current	V <sub>R</sub> = 6 V		0.001	100	μA
CIN	Input Capacitance			18		pF
DETECTO	DR					
I <sub>CEO1</sub>	Collector Emitter Dark Current	V <sub>CE</sub> = 10 V, T <sub>A</sub> = 25°C		1.0	50	nA
I <sub>CEO2</sub>	Collector-Emitter Dark Current	V <sub>CE</sub> = 10 V, T <sub>A</sub> = 100°C		1.0		μA
	Collector-Emitter Breakdown Voltage					
BV <sub>CEO</sub>	MOC205M, MOC206M, MOC207M	I <sub>C</sub> = 100 μA	70	100		V
DVCEO	MOC211M, MOC212M, MOC213M, MOC216M, MOC217M	I <sub>C</sub> = 100 μA	30	100		V
ΒV <sub>CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = 10 μA	70	120		V
BV <sub>ECO</sub>	Emitter-Collector Breakdown Voltage	I <sub>E</sub> = 100 μA	7	10		V
C <sub>CE</sub>	Collector-Emitter Capacitance	f = 1.0 MHz, V <sub>CE</sub> = 0		7		pF
COUPLED	0					
	Collector-Output Current					
	MOC205M	I <sub>F</sub> = 10 mA, V <sub>CE</sub> = 10 V	40		80	%
	MOC206M	I <sub>F</sub> = 10 mA, V <sub>CE</sub> = 10 V	63		125	%
	MOC207M	I <sub>F</sub> = 10 mA, V <sub>CE</sub> = 10 V	100		200	%
CTR	MOC211M	I <sub>F</sub> = 10 mA, V <sub>CE</sub> = 10 V	20			%
	MOC212M	I <sub>F</sub> = 10 mA, V <sub>CE</sub> = 10 V	50			%
	MOC213M	I <sub>F</sub> = 10 mA, V <sub>CE</sub> = 10 V	100	8		%
	MOC216M	I <sub>F</sub> = 1 mA, V <sub>CE</sub> = 5 V	50			%
	MOC217M	I <sub>F</sub> = 1 mA, V <sub>CE</sub> = 5 V	100			%
	Collector-Emitter Saturation Voltage					
V <sub>CE(SAT)</sub>	MOC205M, MOC206M, MOC207M MOC211M, MOC212M, MOC213M	I <sub>C</sub> = 2 mA, I <sub>F</sub> = 10 mA			0.4	V
MOC216M, MOC217M		I <sub>C</sub> = 100 μA, I <sub>F</sub> = 1 mA			0.4	V
t <sub>on</sub>	Turn-On Time	$I_{C} = 2 \text{ mA}, V_{CC} = 10 \text{ V},$ $R_{L} = 100 \Omega \text{ (Figure 12)}$		7.5		μs
t <sub>off</sub>	Turn-Off Time	$I_{C} = 2 \text{ mA}, V_{CC} = 10 \text{ V}, \\ R_{L} = 100 \Omega \text{ (Figure 12)}$		5.7		μs
t <sub>r</sub>	Rise Time	$\label{eq:lc} \begin{array}{l} I_{C} = 2 \text{ mA}, \ V_{CC} = 10 \text{ V}, \\ R_{L} = 100 \ \Omega \ (\text{Figure 12}) \end{array}$		3.2		μs
t <sub>f</sub>	Fall Time	$I_{C} = 2 \text{ mA}, V_{CC} = 10 \text{ V},$ $R_{L} = 100 \Omega \text{ (Figure 12)}$		4.7		μs

Symbol	Characteristic	Test Conditions	Min.	Тур.	Max.	Unit
V <sub>ISO</sub>	Input-Output Isolation Voltage	t = 1 Minute	2500			VAC <sub>RMS</sub>
C <sub>ISO</sub>	Isolation Capacitance	V <sub>I-O</sub> = 0 V, f = 1 MHz		0.2		pF
R <sub>ISO</sub>	Isolation Resistance	$V_{I-O} = \pm 500 \text{ VDC}, T_A = 25^{\circ}C$	10 <sup>11</sup>			Ω
						R





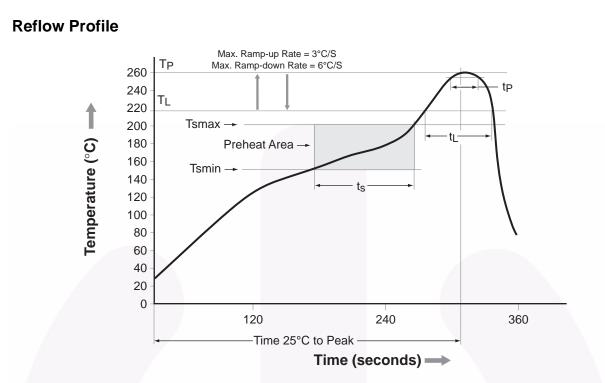


Figure	13.	Reflow	Profile

Profile Freature	Pb-Free Assembly Profile	
Temperature Minimum (Tsmin)	150°C	
Temperature Maximum (Tsmax)	200°C	
Time (t <sub>S</sub> ) from (Tsmin to Tsmax)	60–120 seconds	
Ramp-up Rate (t <sub>L</sub> to t <sub>P</sub> )	3°C/second maximum	
Liquidous Temperature (T <sub>L</sub> )	217°C	
Time (t <sub>L</sub> ) Maintained Above (T <sub>L</sub> ) 60–150 seconds		
Peak Body Package Temperature	260°C +0°C / –5°C	
Time (t <sub>P</sub> ) within 5°C of 260°C	30 seconds	
Ramp-down Rate (T <sub>P</sub> to T <sub>L</sub> )	6°C/second maximum	
Time 25°C to Peak Temperature8 minutes maximum		

### Ordering Information<sup>(2)</sup>

Part Number	Package	Packing Method
MOC205M	Small Outline 8-Pin	Tube (100 Units)
MOC205R2M	Small Outline 8-Pin	Tape and Reel (2500 Units)
MOC205VM	Small Outline 8-Pin, DIN EN/IEC60747-5-5 Option	Tube (100 Units)
MOC205R2VM	Small Outline 8-Pin, DIN EN/IEC60747-5-5 Option	Tape and Reel (2500 Units)

Note:

2. The product orderable part number system listed in this table also applies to the MOC20XM and MOC21XM products.

## **Marking Information**

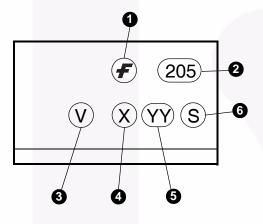
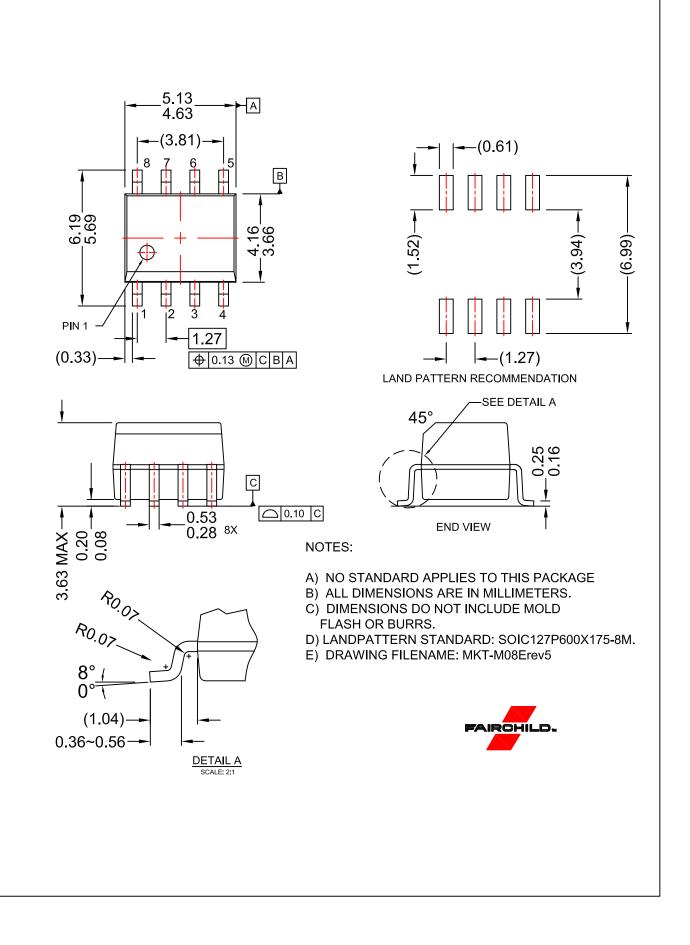
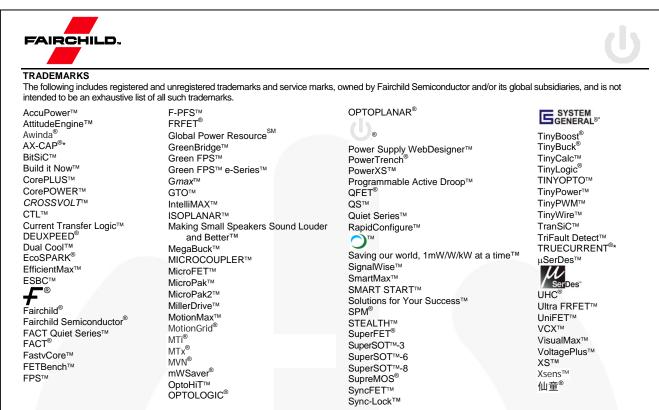


Figure 14. Top Mark

#### Table 1. Top Mark Definitions

1	Fairchild Logo	
2	Device Number	
3	DIN EN/IEC60747-5-5 Option (only appears on component ordered with this option)	
4	Dne-Digit Year Code, e.g., "4"	
5	Digit Work Week, Ranging from "01" to "53"	
6	Assembly Package Code	





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