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November 2015



MOC3081M, MOC3082M, MOC3083M 6-Pin DIP Zero-Cross Triac Driver Optocoupler (800 Volt Peak)

Features

- Simplifies Logic Control of 240 VAC Power
- Zero Voltage Crossing to Minimize Conducted and Radiated Line Noise
- 800 V Peak Blocking Voltage
- Superior Static dv/dt
 - 1500 V/µs Typical, 600 V/µs Guaranteed
- Safety and Regulatory Approvals
 - UL1577, 4,170 VAC_{RMS} for 1 Minute
 - DIN EN/IEC60747-5-5

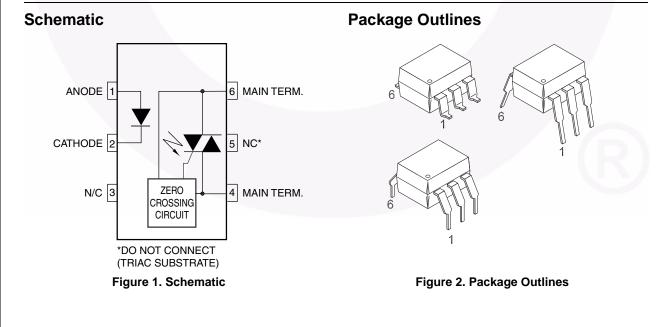
Applications

- Solenoid/Valve Controls
- Lighting Controls
- Static Power Switches
- AC Motor Starters
- Temperature Controls
- E.M. Contactors
- AC Motor Drives
- Solid State Relays

Description

The MOC3081M, MOC3082M and MOC3083M devices consist of a GaAs infrared emitting diode optically coupled to a monolithic silicon detector performing the function of a zero voltage crossing bilateral triac driver.

They are designed for use with a discrete power triac in the interface of logic systems to equipment powered from 240 VAC lines, such as solid-state relays, industrial controls, motors, solenoids and consumer appliances, etc.



Safety and Insulation Ratings

As per DIN EN/IEC 60747-5-5, this optocoupler is suitable for "safe electrical insulation" only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.

Parameter	Characteristics	
Installation Classifications per DIN VDE	< 150 V _{RMS}	I–IV
0110/1.89 Table 1, For Rated Mains Voltage	< 300 V _{RMS}	I–IV
Climatic Classification		40/85/21
Pollution Degree (DIN VDE 0110/1.89)		2
Comparative Tracking Index		175

Symbol	Parameter	Value	Unit
V	Input-to-Output Test Voltage, Method A, $V_{IORM} \times 1.6 = V_{PR}$, Type and Sample Test with t _m = 10 s, Partial Discharge < 5 pC	1360	V _{peak}
V _{PR}	Input-to-Output Test Voltage, Method B, $V_{IORM} \times 1.875 = V_{PR}$, 100% Production Test with t _m = 1 s, Partial Discharge < 5 pC	1594	V _{peak}
V _{IORM}	Maximum Working Insulation Voltage	850	V _{peak}
V _{IOTM}	Highest Allowable Over-Voltage	6000	V _{peak}
	External Creepage	≥ 7	mm
	External Clearance	≥ 7	mm
	External Clearance (for Option TV, 0.4" Lead Spacing)	≥ 10	mm
DTI	Distance Through Insulation (Insulation Thickness)	≥ 0.5	mm
R _{IO}	Insulation Resistance at T _S , V _{IO} = 500 V	> 10 ⁹	Ω

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. $T_A = 25^{\circ}C$ unless otherwise specified.

	Parameter	Value	Unit
TOTAL DEV	/ICE		I
T _{STG}	Storage Temperature	-40 to +150	°C
T _{OPR}	Operating Temperature	-40 to +85	°C
TJ	Junction Temperature Range	-40 to +100	°C
T _{SOL}	Lead Solder Temperature	260 for 10 seconds	°C
	Total Device Power Dissipation at 25°C Ambient	250	mW
PD	Derate Above 25°C	2.94	mW/°C
EMITTER			
١ _F	Continuous Forward Current	60	mA
V _R	Reverse Voltage	6	V
D	Total Power Dissipation at 25°C Ambient	120	mW
PD	Derate Above 25°C	1.41	mW/°C
DETECTOR			
V _{DRM}	Off-State Output Terminal Voltage	800	V
I _{TSM}	Peak Non-Repetitive Surge Current (Single Cycle 60 Hz Sine Wave)	1	А
Р	Total Power Dissipation at 25°C Ambient	150	mW
PD	Derate Above 25°C	1.76	mW/°C

Electrical Characteristics

 $T_A = 25^{\circ}C$ unless otherwise specified.

Individual Component Characteristics

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
EMITTER						
V _F	Input Forward Voltage	I _F = 30 mA		1.3	1.5	V
I _R	Reverse Leakage Current	V _R = 6 V		0.005	100	μA
DETECTO	DR					
I _{DRM1}	Peak Blocking Current, Either Direction	V _{DRM} = 800 V, I _F = 0 ⁽¹⁾		10	500	nA
dv/dt	Critical Rate of Rise of Off-State Voltage	I _F = 0 (Figure 11) ⁽²⁾	600	1500		V/µs

Transfer Characteristics

Symbol	Parameter	Test Conditions	Device	Min.	Тур.	Max.	Unit
			MOC3081M			15	
I _{FT}	LED Trigger Current (Rated I _{FT})	Main Terminal Voltage = 3 V ⁽³⁾	MOC3082M			10	mA
	(Rated I _{FT})	vollage ev	MOC3083M			5	
V _{TM}	Peak On-State Voltage, Either Direction	I_{TM} = 100 mA peak, I_F = rated I_{FT}	All		1.8	3.0	V
Ι _Η	Holding Current, Either Direction		All		500		μA

Zero Crossing Characteristics

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _{INH}	Inhibit Voltage (MT1-MT2 voltage above which device will not trigger)	I _F = Rated I _{FT}		12	20	V
I _{DRM2}	Leakage in Inhibited State	I _F = Rated I _{FT} , V _{DRM} = 600 V, off-state			2	mA

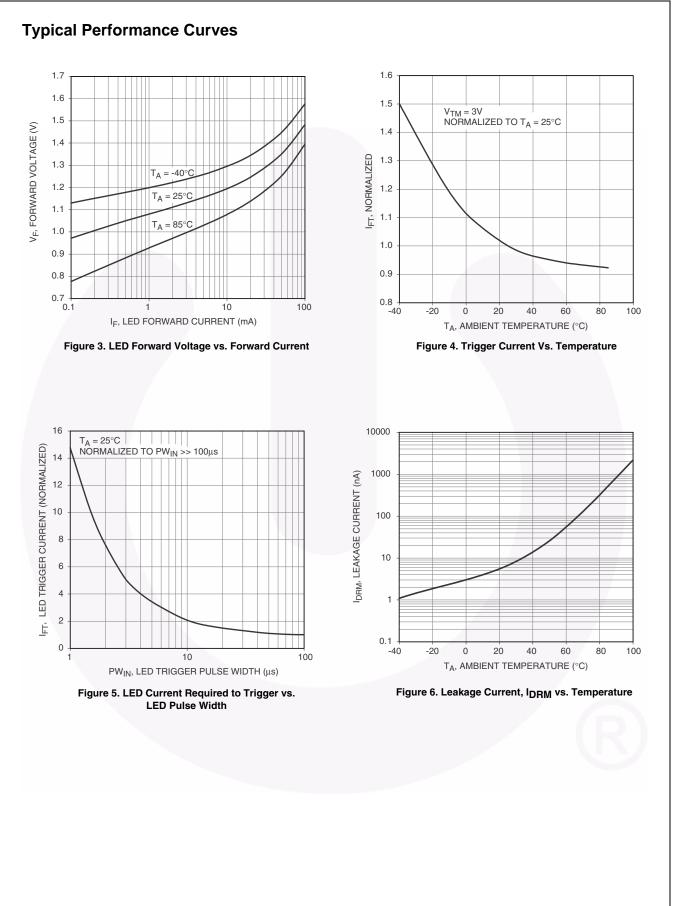
Isolation Characteristics

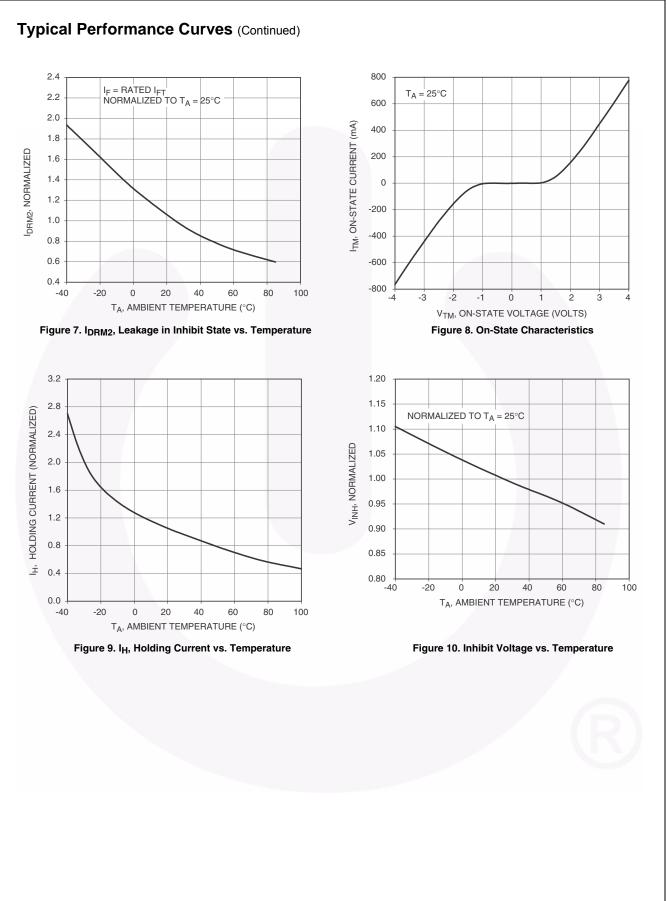
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _{ISO}	Isolation Voltage ⁽⁴⁾	f = 60 Hz, t = 1 Minute	4170			VAC _{RMS}
R _{ISO}	Isolation Resistance	V _{I-O} = 500 V _{DC}		10 ¹¹		Ω
C _{ISO}	Isolation Capacitance	V = 0 V, f = 1 MHz		0.2		pF

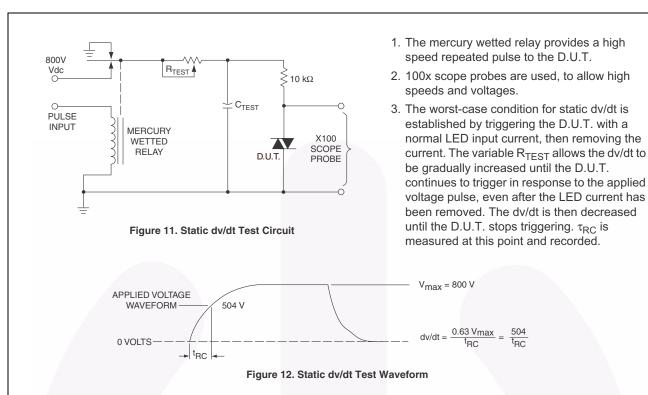
Notes:

1. Test voltage must be applied within dv/dt rating.

- 2. This is static dv/dt. See Figure 11 for test circuit. Commutating dv/dt is a function of the load-driving thyristor(s) only.
- All devices are guaranteed to trigger at an I_F value less than or equal to max I_{FT}. Therefore, recommended operating I_F lies between max I_{FT} (15 mA for MOC3081M, 10 mA for MOC3082M, 5 mA for MOC3083M) and absolute maximum I_F (60 mA).
- 4. Isolation voltage, V_{ISO}, is an internal device dielectric breakdown rating. For this test, pins 1 and 2 are common, and pins 4, 5 and 6 are common.

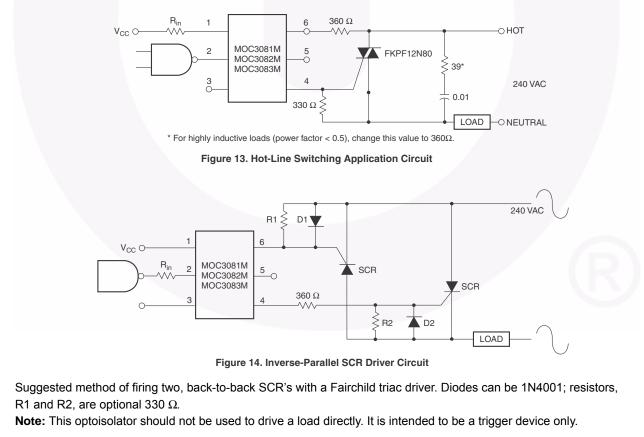


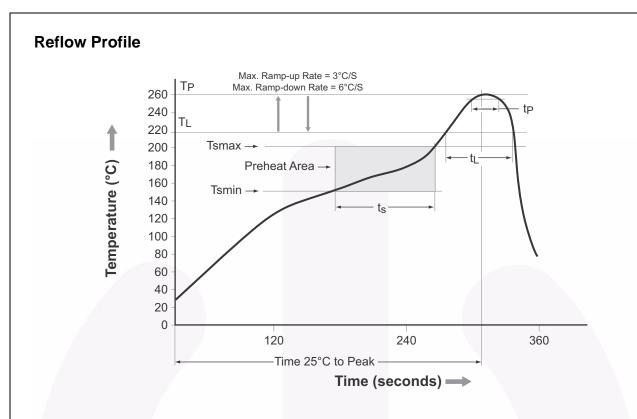




Typical circuit for use when hot line switching is required. In this circuit the "hot" side of the line is switched and the load connected to the cold or neutral side. The load may be connected to either the neutral or hot line.

 R_{in} is calculated so that I_F is equal to the rated I_{FT} of the part, 15 mA for the MOC3081M, 10 mA for the MOC3082M, and 5 mA for the MOC3083M. The 39 Ω resistor and 0.01 μ F capacitor are for snubbing of the triac and may or may not be necessary depending upon the particular triac and load use.





Profile Freature	Pb-Free Assembly Profile		
Temperature Minimum (Tsmin)	150°C		
Temperature Maximum (Tsmax)	200°C		
Time (t _S) from (Tsmin to Tsmax)	60 seconds to 120 seconds		
Ramp-up Rate (T _L to T _P)	3°C/second maximum		
Liquidous Temperature (T _L)	217°C		
Time (t _L) Maintained Above (T _L)	60 seconds to 150 seconds		
Peak Body Package Temperature	260°C +0°C / –5°C		
Time (t _P) within 5°C of 260°C	30 seconds		
Ramp-down Rate (T _P to T _L)	6°C/second maximum		
Time 25°C to Peak Temperature 8 minutes maximum			

Figure 15. Reflow Profile

Ordering Information⁽⁵⁾

Part Number	Package	Packing Method
MOC3081M	DIP 6-Pin	Tube (50 Units)
MOC3081SM	SMT 6-Pin (Lead Bend)	Tube (50 Units)
MOC3081SR2M	SMT 6-Pin (Lead Bend)	Tape and Reel (1000 Units)
MOC3081VM	DIP 6-Pin, DIN EN/IEC60747-5-5 Option	Tube (50 Units)
MOC3081SVM	SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option	Tube (50 Units)
MOC3081SR2VM	SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option	Tape and Reel (1000 Units)
MOC3081TVM	DIP 6-Pin, 0.4" Lead Spacing, DIN EN/IEC60747-5-5 Option	Tube (50 Units)

Note:

5. The product orderable part number system listed in this table also applies to the MOC3082M, and MOC3083M, product families.

Marking Information

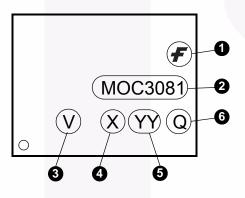


Figure 16. Top Mark

Тор М	Top Mark Definitions					
1	Fairchild Logo					
2	Device Number					
3	DIN EN/IEC60747-5-5 Option (only appears on component ordered with this option)					
4	One-Digit Year Code, e.g., '5'					
5	Two-Digit Work Week, Ranging from '01' to '53'					
6	Assembly Package Code					











NOTES:

- A) NO STANDARD APPLIES TO THIS PACKAGE.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSION
- D) DRAWING FILENAME AND REVSION: MKT-N06Drev4





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Definition of Terms		
Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

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