

November 2011

MPSA64 / MMBTA64 / PZTA64 PNP Darlington Transistor

Features

- This device is designed for applications requiring extremely high current gain at currents to 800 mA.
- Sourced from Process 61.



Absolute Maximum Ratings* T_a = 25°C unless otherwise noted

| Symbol | Parameter | Value | Units |
|----------------------------------|--|-------------|-------|
| V _{CES} | Collector-Emitter Voltage | -30 | V |
| V _{CBO} | Collector-Base Voltage | -30 | V |
| V _{EBO} | Emitter-Base Voltage | -10 | V |
| I _C | Collector Current - Continuous | -1.2 | Α |
| T _{J,} T _{stg} | Operating and Storage Junction Temperature Range | -55 to +150 | °C |

^{*} These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

- 1) These ratings are based on a maximum junction temperature of 150 degrees C.
- 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle

Thermal Characteristics $T_a = 25$ °C unless otherwise noted

| Symbol | Parameter | Max. | | | Units |
|-----------------|--|------------|------------|--------------|-------------|
| | | MPSA64 | *MMBTA64 | **PZTA64 | Office |
| P _D | Total Device Dissipation Derate above 25°C | 625 5.0 | 350 2.8 | 1,000 8.0 | mW mW/°C |
| $R_{\theta JC}$ | Thermal Resistance, Junction to Case | 83.3 | | | °C/W |
| $R_{	heta JA}$ | Thermal Resistance, Junction to Ambient | 200 | 357 | 125 | °C/W |

^{*} Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

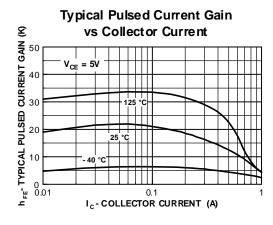
^{**} Device mounted on FR-4 PCB 36 mm X 18 mm X 1.5 mm; mounting pad for the collector lead min. 6 cm².

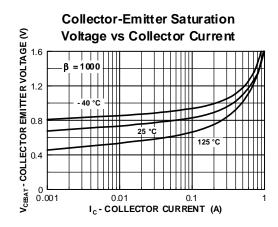
Electrical Characteristics $T_a = 25$ °C unless otherwise noted

| Symbol | Parameter | Test Condition | Min. | Max. | Units | | |
|------------------------------|--------------------------------------|---|------------------|------|-------|--|--|
| OFF CHARAC | OFF CHARACTERISTICS | | | | | | |
| V _{(BR)CES} | Collector-Emitter Breakdown Voltage | $I_C = -100 \mu A, I_B = 0$ | -30 | | V | | |
| I _{CBO} | Collector-Cutoff Current | $V_{CB} = -30V, I_{E} = 0$ | | -100 | nA | | |
| I _{EBO} | Emitter-Cutoff Current | $V_{EB} = -10V, I_{C} = 0$ | | -100 | nA | | |
| ON CHARACTERISTICS* | | | | | | | |
| h _{FE} | DC Current Gain | I _C = -10mA, V _{CE} = -5.0V I _C = -100mA, V _{CE} = -5.0V | 10,000 20,000 | | | | |
| V _{CE(sat)} | Collector-Emitter Saturation Voltage | $I_C = -100 \text{mA}, I_B = -0.1 \text{mA}$ | | -1.5 | V | | |
| V _{BE(on)} | Base-Emitter On Voltage | $I_C = -100 \text{mA}, V_{CE} = -5.0 \text{V}$ | | -2.0 | V | | |
| SMALL SIGNAL CHARACTERISTICS | | | | | | | |
| f _T | Current Gain - Bandwidth Product | $I_C = -10$ mA, $V_{CE} = -5.0$ V, $f = 100$ MHz | 125 | | MHz | | |

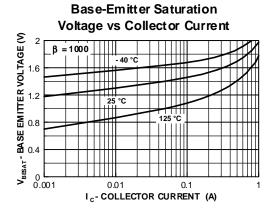
^{*} Pulse Test: Pulse Width $\leq 300 \mu s, \, Duty \, Cycle \leq 2.0\%$

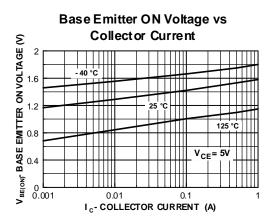
Typical Performance Characteristics



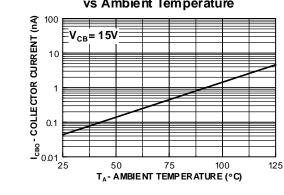


Typical Performance Characteristics (continued)

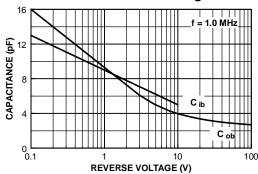




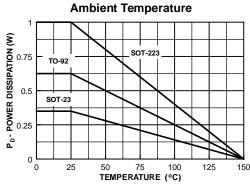




Input and Output Capacitance vs Reverse Bias Voltage



Power Dissipation vs







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|--------------------------|-----------------------|---|--|
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