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Data Sheet November 2013

## 15 A, 1200 V, Hyperfast Diode

The RHRP15120 is a hyperfast diode with soft recovery characteristics. It has the half recovery time of ultrafast diodes and is silicon nitride passivated ionimplanted epitaxial planar construction. These devices are intended to be used as freewheeling/ clamping diodes and diodes in a variety of switching power supplies and other power switching applications. Their low stored charge and hyperfast soft recovery minimize ringing and electrical noise in many power switching circuits reducing power loss in the switching transistors.

## Ordering Information

PART NUMBER	PACKAGE	BRAND
RHRP15120	TO-220AC-2L	RHR15120

NOTE: When ordering, use the entire part number.

## Symbol



### **Features**

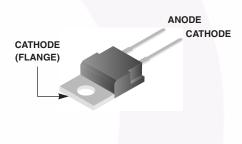
- Hyperfast Recovery  $t_{rr} = 75 \text{ ns}$  (@  $I_F = 15 \text{ A}$ )
- Max Forward Voltage, V<sub>F</sub> = 3.2 V (@ T<sub>C</sub> = 25°C)
- 1200 V Reverse Voltage and High Reliability
- · Avalanche Energy Rated
- RoHS Compliant

## **Applications**

- Switching Power Supplies
- Power Switching Circuits
- · General Purpose

## **Packaging**

**JEDEC TO-220AC** 



## **Absolute Maximum Ratings** $T_C = 25^{\circ}C$ , Unless Otherwise Specified

	KHKP15120	UNII	
Peak Repetitive Reverse Voltage	1200	V	
Working Peak Reverse Voltage	1200	V	
DC Blocking VoltageV <sub>R</sub>	1200	V	
Average Rectified Forward Current $I_{F(AV)}$ ( $T_C = 140^{\circ}C$ )	15	А	
Repetitive Peak Surge Current	30	Α	
Nonrepetitive Peak Surge Current	200	Α	
Maximum Power Dissipation	100	W	
Avalanche Energy (See Figures 10 and 11)	20	mJ	
Operating and Storage Temperature	-65 to 175	°C	

**Electrical Specifications**  $T_C = 25^{\circ}C$ , Unless Otherwise Specified

SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
V <sub>F</sub>	I <sub>F</sub> = 15 A	-	-	3.2	V
	$I_F = 15 \text{ A}, T_C = 150^{\circ}\text{C}$	-	-	2.6	V
I <sub>R</sub>	V <sub>R</sub> = 1200 V	-	-	100	μΑ
	V <sub>R</sub> = 1200 V, T <sub>C</sub> = 150°C	-	-	500	μΑ
T <sub>rr</sub>	I <sub>F</sub> = 1 A, dI <sub>F</sub> /dt = 100 A/μs	-	-	65	ns
	I <sub>F</sub> = 15 A, dI <sub>F</sub> /dt = 100 A/μs	-	-	75	ns
t <sub>a</sub>	I <sub>F</sub> = 15 A, dI <sub>F</sub> /dt = 100 A/μs	-	36	-	ns
t <sub>b</sub>	I <sub>F</sub> = 15 A, dI <sub>F</sub> /dt = 100 A/μs	-	28	-	ns
Q <sub>rr</sub>	$I_F = 15 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}$	-	150	-	nC
СЈ	V <sub>R</sub> = 10 V, I <sub>F</sub> = 0 A	-	55	-	pF
$R_{ heta JC}$		-	-	1.5	°C/W

### **DEFINITIONS**

 $V_F$  = Instantaneous forward voltage (pw = 300  $\mu$ s, D = 2%).

 $I_R$  = Instantaneous reverse current.

 $t_{rr}$  = Reverse recovery time (See Figure 9), summation of  $t_a + t_b$ .

 $t_a$  = Time to reach peak reverse current (See Figure 9).

 $t_b$  = Time from peak  $I_{RM}$  to projected zero crossing of  $I_{RM}$  based on a straight line from peak  $I_{RM}$  through 25% of  $I_{RM}$  (See Figure 9).

1000

Q<sub>rr</sub> = Reverse recovery charge.

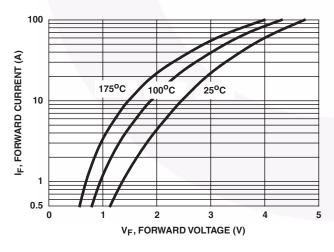
 $C_J$  = Junction capacitance.

 $R_{\theta JC}$  = Thermal resistance junction to case.

pw = pulse width.

D = duty cycle.

## **Typical Performance Curves**



100°C
100°C
100°C
100°C
100°C
100°C
V<sub>R</sub>, REVERSE VOLTAGE (V)

FIGURE 1. FORWARD CURRENT vs FORWARD VOLTAGE

FIGURE 2. REVERSE CURRENT vs REVERSE VOLTAGE

## Typical Performance Curves (Continued)

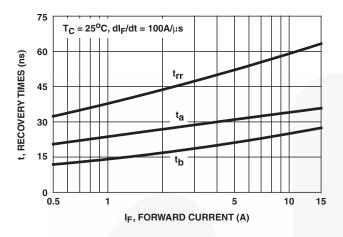


FIGURE 3.  $t_{rr}$ ,  $t_a$  AND  $t_b$  CURVES vs FORWARD CURRENT

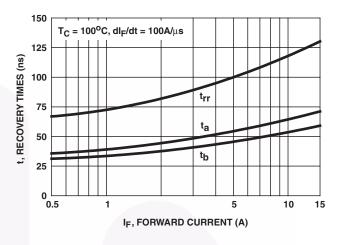


FIGURE 4.  $t_{rr}$ ,  $t_a$  AND  $t_b$  CURVES vs FORWARD CURRENT

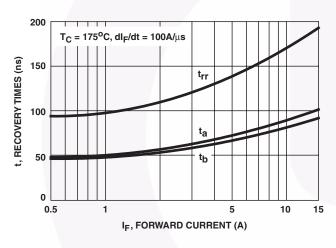


FIGURE 5.  $t_{rr}$ ,  $t_a$  and  $t_b$  curves vs forward current

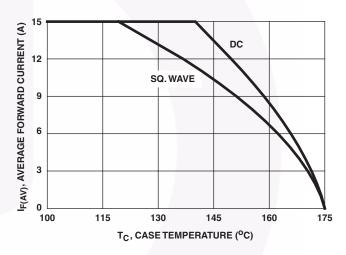


FIGURE 6. CURRENT DERATING CURVE

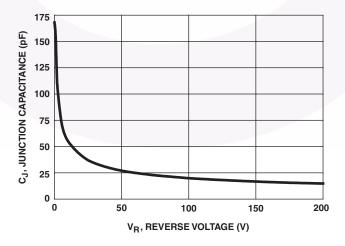


FIGURE 7. JUNCTION CAPACITANCE vs REVERSE VOLTAGE

## Test Circuits and Waveforms

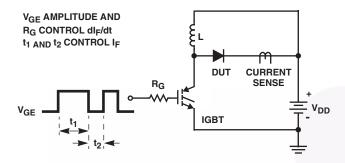


FIGURE 8.  $t_{rr}$  TEST CIRCUIT

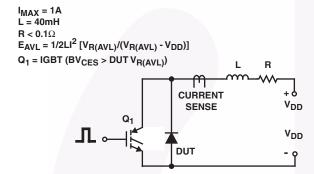


FIGURE 10. AVALANCHE ENERGY TEST CIRCUIT

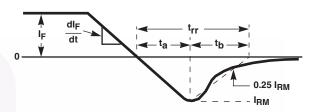


FIGURE 9. t<sub>rr</sub> WAVEFORMS AND DEFINITIONS

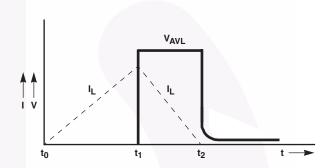
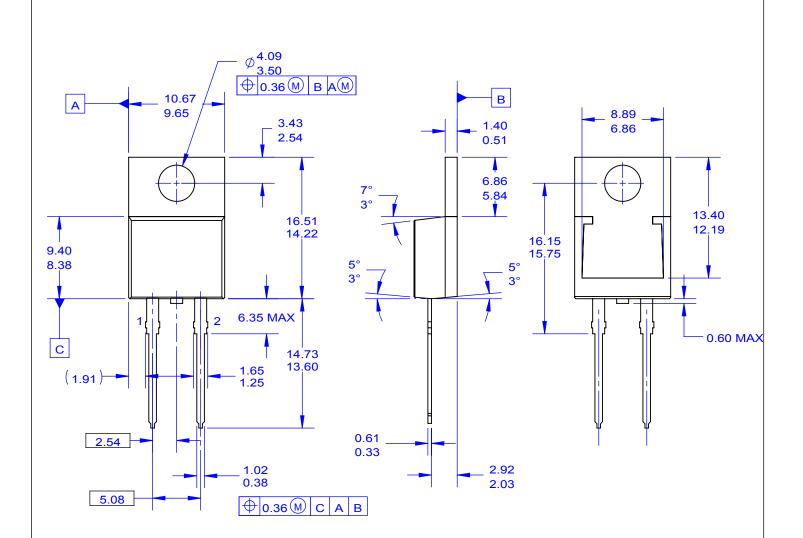
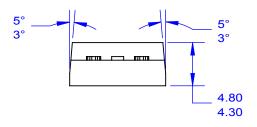


FIGURE 11. AVALANCHE CURRENT AND VOLTAGE WAVEFORMS







## **NOTES**:

- A. PACKAGE REFERENCE: JEDEC TO220,ISSUE K, VARIATION AC,DATED APRIL 2002.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSION AND TOLERANCE AS PER ASME Y14.5-2009.
- D. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.
- E. DRAWING FILE NAME: TO220A02REV5





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Deminition of Terms		
Datasheet Identification	Product Status	Definition
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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