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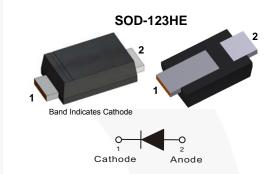


March 2016

SS12FP - S115FP 1 A, 20 V - 150 V Surface Mount Schottky Barrier Rectifiers

Features

- Larger Cathode Pad for Improved Power Dissipation
- Ultra Thin Profile Package Height < 1.0 mm
- · High Surge Current Capability
- · Low Power Loss, High Efficiency
- UL Flammability 94V-0 Classification
- MSL 1 per J-STD-020
- · RoHS Compliant / Green Molding Compound
- Industrial Device Qualified per AEC-Q101 Standards
 - * See authorized use policy



Ordering Information

Part Number	Top Mark	Package	Packing Method	
SS12FP	2FP	SOD-123HE	Tape and Reel	
SS13FP	3FP	SOD-123HE	Tape and Reel	
SS14FP	4FP	SOD-123HE	Tape and Reel	
SS16FP	6FP	SOD-123HE Tape and F		
S110FP	0FP	SOD-123HE	Tape and Reel	
S115FP	AFP	SOD-123HE Tape and Reel		

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter		SS13 FP	SS14 FP	SS16 FP	S110 FP	S115 FP	Unit	
V_{RRM}	Repetitive Peak Reverse Voltage		30	40	60	100	150	V	
V _{RMS}	RMS Reverse Voltage		21	28	42	70	105	V	
V _R	DC Blocking Voltage	20	30	40	60	100	150	V	
I _{F(AV)}	Average Forward Rectified Current	1						Α	
I _{FSM}	Peak Forward Surge Current: 8.3 ms Single Half Sine-Wave Superimposed on Rated Load	30						А	
T_J	Operating Junction Temperature Range	-55 to	+125		-55 to	+150		°C	
T _{STG}	Storage Temperature Range -55 to +150			°C					

Thermal Characteristics(1)

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
ΨJL	Thermal Characteristics, Junction-to-Lead ⁽²⁾	10	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	140	°C/W

Notes:

- 1. Per JESD51-3 recommended thermal test board. Device mounted on FR-4 PCB, board size = 76.2 mm x 114.3 mm.
- 2. Thermocouple soldered at cathode lead.

Electrical Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol		Condi- tions	Value						
	Parameter		SS12 FP	SS13 FP	SS14 FP	SS16 FP	S110 FP	S115 FP	Unit
	Maximum Instantaneous Forward Voltage ⁽³⁾	I _F = 0.5 A			0.51	0.58	0.70	0.75	V
		I _F = 1.0 A	0.45	0.50	0.55	0.70	0.80	0.90	V
l	Maximum Reverse Current at Rated V _R	T _J = 25°C	0.40				0.05		mA
		T _J = 125°C					0.	0.50	
CJ	Typical Junction Capacitance	V _R = 4 V, f = 1 MHz	54			2	.8	pF	
T _{rr}	Typical Reverse Recovery Time	$I_F = 0.5 A,$ $I_R = 1 A,$ $I_{RR} = 0.25 A$	6 14		4	ns			

Note:

3. Pulse test with PW = $300 \mu s$, 1% duty cycle

Typical Performance Characteristics

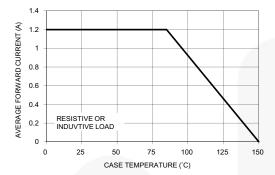


Figure 1. Forward Current Derating Curve

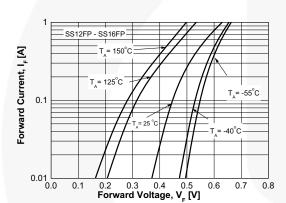


Figure 3. Typical Forward Characteristics

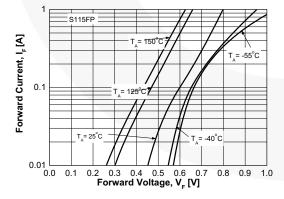


Figure 5. Typical Forward Characteristics

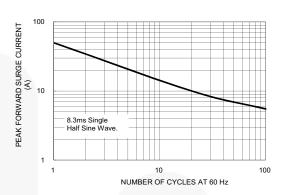


Figure 2. Maximum Non-Repetitive Forward Surge Curren

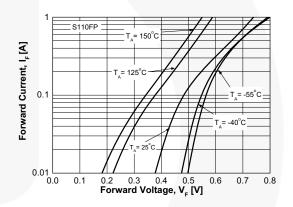


Figure 4. Typical Forward Characteristics

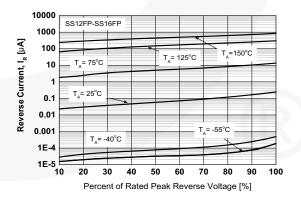


Figure 6. Typical Reverse Characteristics

Typical Performance Characteristics (Continued)

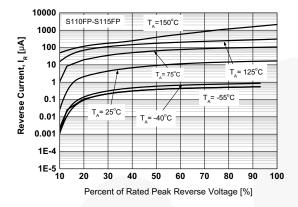


Figure 7. Typical Reverse Characteristics

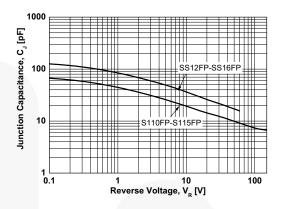
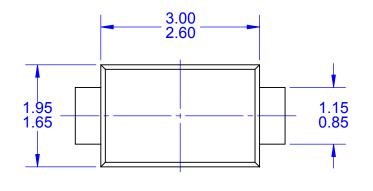
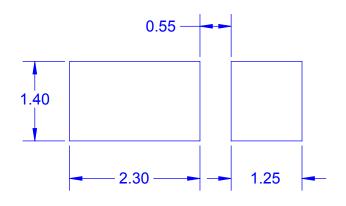
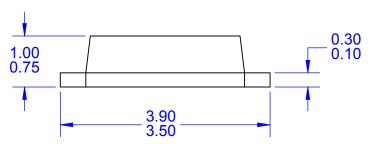


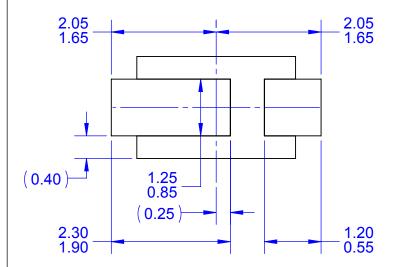
Figure 8. Typical Junction Capacitance











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Definition of Terms

Deminition of Terms							
Datasheet Identification		Definition					
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.					
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