

BP2857D Non-isolated Buck Offline LED Driver

Description

The BP2857D is a high precision buck constant current LED driver. The device operates in critical conduction mode and is suitable for 85Vac~265Vac universal input offline LED lighting.

The BP2857D integrates a 500V power MOSFET. With patent pending floating ground structure and current sensing technique, the operating current of the IC is very low. So it doesn't need the auxiliary winding for supplying the chip. It can achieve excellent constant current performance with very few external components, so the system cost and size are minimized.

The BP2857D utilizes patent pending current control method. It can achieve precise output current and excellent line regulation. The driver operates in critical conduction mode, the output current does not change with the inductance and LED output voltage.

The BP2857D offers rich protection functions to improve the system reliability, including LED open circuit protection, LED short circuit protection, VCC under voltage protection, CS resistor short circuit protection and thermal regulation function.

Typical Application

Features

- Critical Conduction Mode Operation
- Internal 500V Power MOSFET
- No Auxiliary Winding
- Ultra Low Operating Current
- Universal input
- ±5% LED Output Current Accuracy
- LED Open Protection
- LED Short Protection
- Current Sensing Resistor Short Protection
- VCC Under Voltage Protection
- Thermal Regulation Function
- Available in DIP-7 Package

Applications

- LED Candle Light
- LED Bulb
- Other LED Lighting

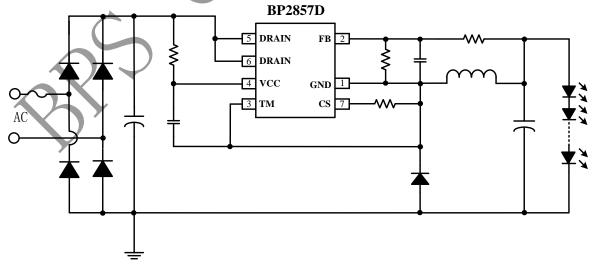


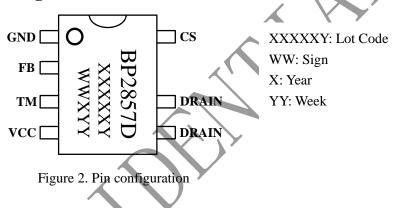
Figure 1. Typical application circuit for BP2857D



Ordering Information

Part Number	Package	Operating Temperature	Package Method	Marking
BP2857D	DIP7	-40 °C to 105 °C	Tube	BP2857D
			50 Piece/Tube	XXXXXY WWXYY

Pin Configuration and Marking Information



Pin Definition

Pin No.	Name	Description
1	GND	Ground
2	FB	Feedback Voltage Input Pin.
3	ТМ	For Test Mode. Must be connected to GND(Pin1)
4	VCC	Power Supply Pin
5,6	DRAIN	Internal HV Power MOSFET Drain.
7	CS	Current Sense Pin. Connect a sense resistor between this pin and GND pin.



Absolute Maximum Ratings (note1)

Symbol	Parameters	Range	Units	
I _{CC_MAX}	VCC pin maximum sink current	5	mA	
DRAIN	Internal HV MOSFET drain voltage	-0.3~500	V	
CS	Current sense pin input voltage	-0.3~6	v	
FB	Feedback voltage input voltage	-0.3~6	V	
TM	For test mode	-0.3~6	V	
P _{DMAX}	Power dissipation (note 2)	0.9	W	
θ_{JA}	Thermal resistance (Junction to Ambient)	80	°C/W	
T_J	Operating junction temperature	-40 to 150	്റ	
T _{STG}	Storage temperature range	-55 to 150	്റ	
	ESD (note 3)	2	KV	

Note 1: Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. Under "recommended operating conditions" the device operation is assured, but some particular parameter may not be achieved. The electrical characteristics table defines the operation range of the device, the electrical characteristics is assured on DC and AC voltage by test program. For the parameters without minimum and maximum value in the EC table, the typical value defines the operation range, the accuracy is not guaranteed by spec.

Note 2: The maximum power dissipation decrease if temperature rise, it is decided by T_{JMAX} , θ_{JA} , and environment temperature (T_A). The maximum power dissipation is the lower one between $P_{DMAX} = (T_{JMAX} - T_A)/\theta_{JA}$ and the number listed in the maximum table.

Note 3: Human Body mode, 100pF capacitor discharge on $1.5K\Omega$ resistor

Recommended Operation Conditions

Symbol	Parameter	Range	Unit
ILEDI	Output LED current @ Vout=72V (Input voltage 176V~265V)	480	mA
ILED2	Output LED current @ Vout=36V (Input voltage 176V~265V)	640	mA



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Electrical Characteristics (Notes 4, 5) (Unless otherwise specified, V_{CC} =15V and T_A =25 °C)

Symbol	Parameter	Conditions	Min	Тур	Max	Units
Supply Voltage	Section					
V _{CC_CLAMP}	V _{CC} Clamp Voltage	1mA		16.8		V
V _{CC_ON}	V _{CC} Turn On Threshold	V _{CC} Rising		13.8		V
V _{CC_UVLO}	V _{CC} Turn off Threshold	V _{CC} Falling		9		V
I _{ST}	V _{CC} Startup Current	$V_{CC} = V_{CC-ON} - 1V$		100	150	uA
I _{OP}	V _{CC} Operating Current	Fop=50KHz		120	200	uA
Current Sense S	Section					
V_{CS_TH}	Threshold Voltage for Peak Current Limit		388	400	412	mV
T_{LEB}	Leading Edge Blanking Time for Current Sense		$\langle \rangle$	350		ns
T _{DELAY}	Switch Off Delay Time			200		ns
Feedback Sectio	n		$\langle \rangle \rangle$	•		
V_{FB_FALL}	FB Falling Edge Threshold Voltage	FB Falling		0.1		v
V_{FB_HYS}	FB Hysteresis Voltage	FB Rising		0.12		V
V _{FB_OVP}	FB Over Voltage Protection Threshold	$\langle \mathbf{y}, \mathbf{y} \rangle$		1.2		v
Internal Time C	Control Section					
T _{OFF_MIN}	Minimum OFF Time			2.5		us
T _{OFF_MAX}	Maximum OFF Time	7		330		us
T _{ON_MAX}	Maximum On Time			40		us
MOSFET Sectio	on V					
R _{DS_ON}	Static Drain-source On-resistance	V _{GS} =15V/I _{DS} =0.4A		2		Ω
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V/I _{DS} =250uA	500			V
Ipss	 Power MOSFET Drain Leakage Current 	V_{GS} =0V/ V_{DS} =500V			1	uA
Thermal Regula	ntion Section					
T _{REG}	Thermal Regulation Temperature			150		°C

Note 4: production testing of the chip is performed at 25°C.

Note 5: the maximum and minimum parameters specified are guaranteed by test, the typical value are guaranteed by design, characterization and statistical analysis



Internal Block Diagram

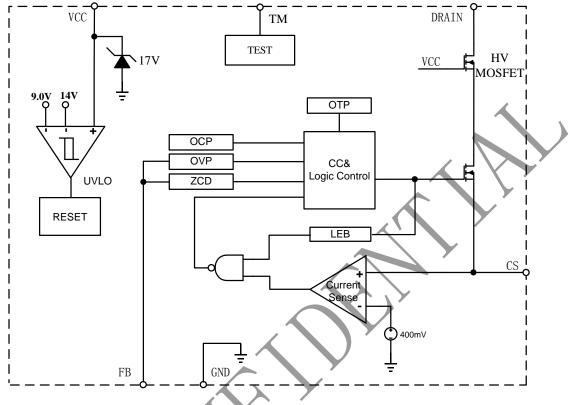


Figure 3. BP2857D Internal Block Diagram

Application Information

The BP2857D is a high performance non-isolated Buck converter specially designed for LED lighting. The device integrates a 500V power MOSFET. With very few external components, the converter achieves excellent constant current control. And it does not need auxiliary winding for powering the IC or voltage sensing, hence the system size and cost is greatly reduced.

Start Up

After system powered up, the VCC pin capacitor is charged up by the start up resistor. When the VCC pin voltage reaches the turn on threshold, the internal circuits start operating. The BP2857D integrates a 17V zener diode to clamp the VCC voltage. Due to the ultra-low operating current, the auxiliary winding is not needed to supply the IC.

Constant Current Control

Cycle by Cycle current sense is adopted in BP2857D, the CS pin is connected to the current sense comparator, and the voltage on CS pin is compared with the internal 400mV reference voltage. The MOSFET will be switched off when the voltage on CS pin reaches the threshold. The CS comparator includes a 350ns leading edge blanking time.

The peak inductor current is given by:

$$I_{PK} = \frac{400}{R_{CS}} (mA)$$

Where, R_{CS} is the current sense resistor value.

The current in LED can be calculated by the equation:



$$I_{LED} = \frac{I_{PK}}{2}$$

Where, I_{PK} is the peak current of the inductor.

Inductor Selection

The BP2857D works under inductor current critical conduction mode. When the power MOFET is switched on, the current in the inductor rises up from zero, the on time of the MOSFET can be calculated by the equation:

$$t_{\rm on} = \frac{L \times I_{\rm PK}}{V_{\rm IN} - V_{\rm LED}}$$

Where.

L is the inductance value

V_{IN} is the DC bus voltage after the rectifier bridge V_{LED} is the voltage on the LED

After the power MOSFET is switched off, the current in the inductor decreases. When the inductor current reaches zero, the power MOSFET is turned on again by IC internal logic. The off time of the MOSFET is given by:

$$t_{\rm off} = \frac{L \times I_{\rm PK}}{V_{\rm LED}}$$

The inductance can be calculated by the equation:



The f is the system switching frequency, which is proportional to the input voltage. So the minimum switching frequency is set at lowest input voltage, and the maximum switching frequency is set at highest input voltage.

The minimum and maximum off time of BP2857D is set at 2.5us and 330us, respectively. Referring to the equation of toFF calculation, if the inductance is too

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small, the t_{OFF} may be smaller than the minimum off time. system will operate in discontinuous conduction mode and the output current will be smaller than the designed value. If the inductance is too large, the toFF may be larger than the maximum off time, the system will operate in continuous conduction mode and the output current will be higher than the designed value. So it is important to choose a proper inductance.

Feedback Network

The BP2857D senses the output current zero crossing information through the feedback network, the FB falling threshold voltage is set to 0.1V with 0.12V hysteresis. The FB pin is also used to detect output OVP, the threshold voltage is 1.2V. The ratio of FB upper resistor to lower resistor can be set as:

$$\frac{R_{FBL}}{R_{FBL} + R_{FBH}} = \frac{1.2V}{V_{OVP}}$$

Where,

R_{FBL}: The lower resistor of the feedback network R_{FBH}: The upper resistor of the feedback network V_{OVP}: Output over voltage setting point

The minimum V_{OVP} should be set about 1.3Vo.It is recommended that the FB lower resistor is set to $2K\Omega$ --5K Ω paralleled with a 100pF ceramic capacitor.

Protection Function

The BP2857D offers rich protection functions to improve the system reliability, including LED open/short protection, CS resistor short protection, V_{CC} under voltage protection, thermal regulation. When the LED is open circuit, the system will trigger the over voltage protection and stop switching.

When the LED short circuit is detected, the system works at low frequency (3 kHz), so the system power consumption is very low. At some catastrophic fault condition, such as CS resistor shorted or inductor



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saturated, the internal fast fault detection circuit will be triggered, the system stops switching immediately.

After the system enters into fault condition, the VCC voltage will decrease until it reaches the UVLO threshold, then the system will re-start again. If the fault condition is removed, the system will recover to normal operation.

Thermal Regulation

The BP2857D integrates thermal regulation function. When the system is over temperature, the output current is gradually reduced; the output power and thermal dissipation are also reduced. The system temperature is regulated and the system reliability is improved. The thermal regulation temperature is set to 150° C internally.

PCB Layout

The following rules should be followed in BP2857D PCB layout:

Bypass Capacitor

The bypass capacitor on VCC pin should be as close as possible to the VCC Pin and GND pin.

Ground Path

The power ground path for current sense should be short, and the power ground path should be separated from small signal ground path before connecting to the negative node of the bulk capacitor.

The Area of Power Loop

The area of main current loop should be as small as possible to reduce EMI radiation, such as the inductor, the power MOSFET, the output diode and the bus capacitor loop.

FB Pin

The feedback resistor divider should be as close as possible to the FB pin, and the trace must keeps away from dynamic node of the inductor (DRAIN pin trace), otherwise the FB pin OVP function might have risk to be mis-triggered by the system noise.

TM pin

The TM pin must be connected to GND (Pin1).

Drain Pin

To increase the copper area of DRAIN pin for better thermal dissipation. However too large copper area may compromise EMI performance.



Physical Dimensions

