


Hyperfast Rectifier

**Features**

- Hyperfast Recovery Time
- Low Forward Voltage Drop
- Low Leakage Current
- 175°C Operating Junction Temperature
- Fully Isolated package ( $V_{INS} = 2500 V_{RMS}$ )
- UL E78996 approved 

$t_{rr} = 22ns$  typ.  
 $I_{F(AV)} = 15Amp$   
 $V_R = 600V$

**Description/ Applications**

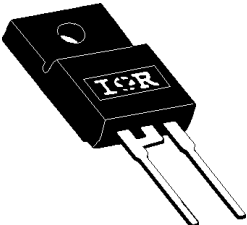
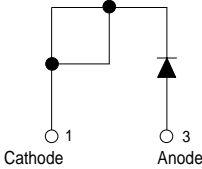
State of the art Hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, Hyperfast recover time, and soft recovery. The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics. These devices are intended for use in PFC Boost stage in the AC-DC section of SMPS, inverters or as freewheeling diodes. The IR extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

**Absolute Maximum Ratings**

Parameters		Max	Units
$V_{RRM}$	Peak Repetitive Peak Reverse Voltage	600	V
$I_{F(AV)}$	Average Rectified Forward Current @ $T_C = 134^\circ C$	15	A
$I_{FSM}$	Non Repetitive Peak Surge Current @ $T_J = 25^\circ C$	180	
$I_{FM}$	Peak Repetitive Forward Current	30	
$T_J, T_{STG}$	Operating Junction and Storage Temperatures	- 65 to 175	$^\circ C$

**Case Styles**

15ETH06FP

TO-220 FULLPACK

**Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise specified)**

Parameters	Min	Typ	Max	Units	Test Conditions
V <sub>BR</sub> , V <sub>r</sub> Breakdown Voltage, Blocking Voltage	600	-	-	V	I <sub>R</sub> = 100μA
V <sub>F</sub> Forward Voltage	-	1.8	2.2	V	I <sub>F</sub> = 15A, T <sub>J</sub> = 25°C
	-	1.3	1.6	V	I <sub>F</sub> = 15A, T <sub>J</sub> = 150°C
I <sub>R</sub> Reverse Leakage Current	-	0.2	50	μA	V <sub>R</sub> = V <sub>R</sub> Rated
	-	30	500	μA	T <sub>J</sub> = 150°C, V <sub>R</sub> = V <sub>R</sub> Rated
C <sub>T</sub> Junction Capacitance	-	20	-	pF	V <sub>R</sub> = 600V
L <sub>S</sub> Series Inductance	-	8.0	-	nH	Measured lead to lead 5mm from package body

**Dynamic Recovery Characteristics @ T<sub>C</sub> = 25°C (unless otherwise specified)**

Parameters	Min	Typ	Max	Units	Test Conditions
t <sub>rr</sub> Reverse Recovery Time	-	22	30	ns	I <sub>F</sub> = 1A, di <sub>F</sub> /dt = 100A/μs, V <sub>R</sub> = 30V
	-	28	35		I <sub>F</sub> = 15A, di <sub>F</sub> /dt = 100A/μs, V <sub>R</sub> = 30V
	-	29	-	ns	T <sub>J</sub> = 25°C
	-	75	-		T <sub>J</sub> = 125°C
I <sub>RRM</sub> Peak Recovery Current	-	3.5	-	A	I <sub>F</sub> = 15A di <sub>F</sub> /dt = 200A/μs V <sub>R</sub> = 390V
	-	7	-		T <sub>J</sub> = 125°C
Q <sub>rr</sub> Reverse Recovery Charge	-	57	-	nC	T <sub>J</sub> = 25°C
	-	300	-		T <sub>J</sub> = 125°C
t <sub>rr</sub> Reverse Recovery Time	-	51	-	ns	T <sub>J</sub> = 125°C I <sub>F</sub> = 15A di <sub>F</sub> /dt = 800A/μs V <sub>R</sub> = 390V
I <sub>RRM</sub> Peak Recovery Current	-	20	-		
Q <sub>rr</sub> Reverse Recovery Charge	-	580	-		

**Thermal - Mechanical Characteristics**

Parameters	Min	Typ	Max	Units
T <sub>J</sub> Max. Junction Temperature Range	-	-	175	°C
T <sub>Stg</sub> Max. Storage Temperature Range	-65	-	175	
R <sub>thJC</sub> Thermal Resistance, Junction to Case Per Leg	-	1.3	1.5	°C/W
R <sub>thJA</sub> ① Thermal Resistance, Junction to Ambient Per Leg	-	-	70	
R <sub>thCS</sub> ② Thermal Resistance, Case to Heatsink	-	1.5	-	
Weight	-	2.0	-	g
	-	0.07	-	(oz)
Mounting Torque	6.0	-	12	Kg-cm
	5.0	-	10	lbf.in

① Typical Socket Mount

② Mounting Surface, Flat, Smooth and Greased

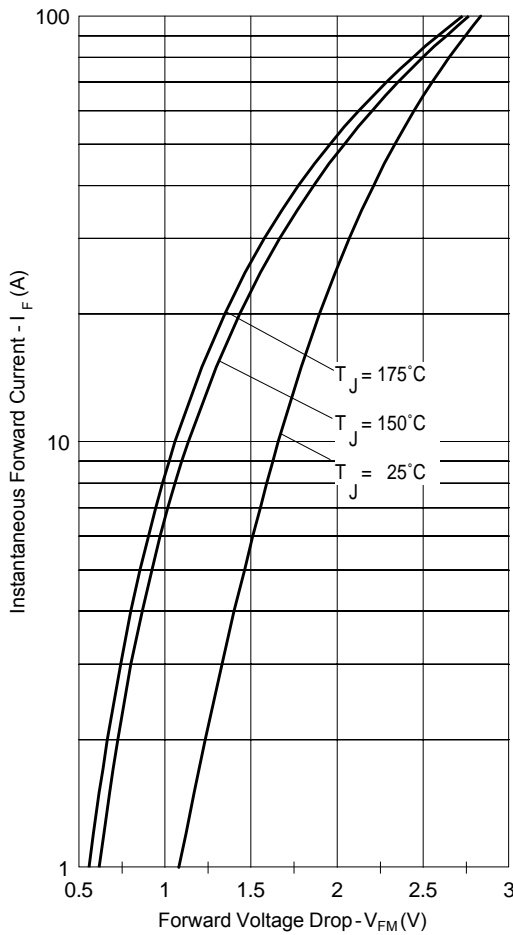


Fig. 1 - Typical Forward Voltage Drop Characteristics

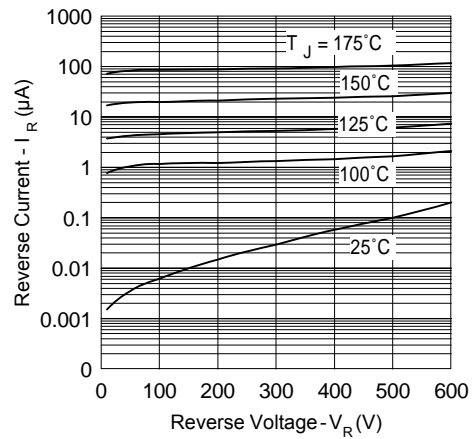


Fig. 2 - Typical Values Of Reverse Current Vs. Reverse Voltage

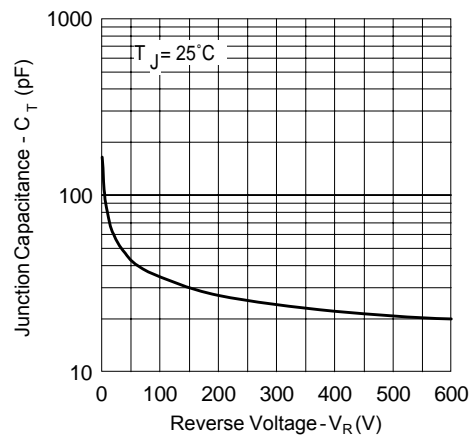


Fig. 3 - Typical Junction Capacitance Vs. Reverse Voltage

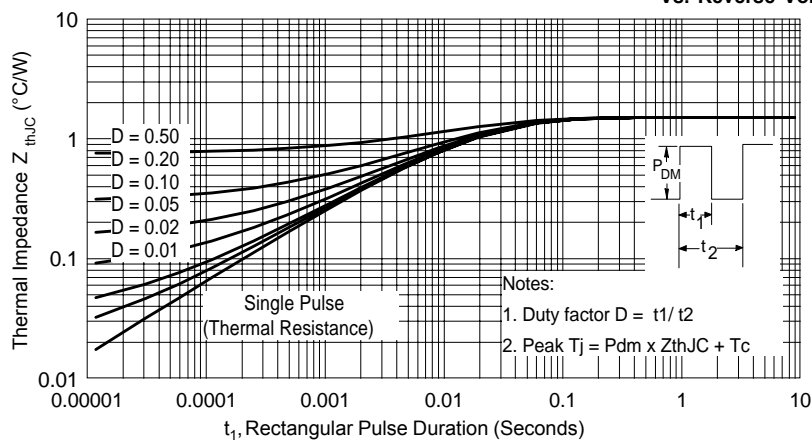
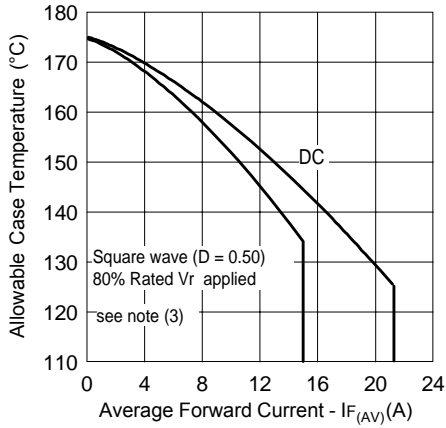
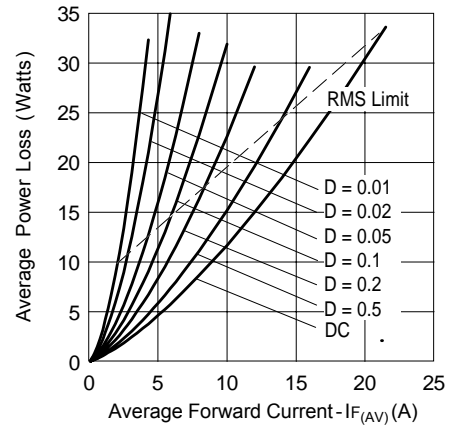


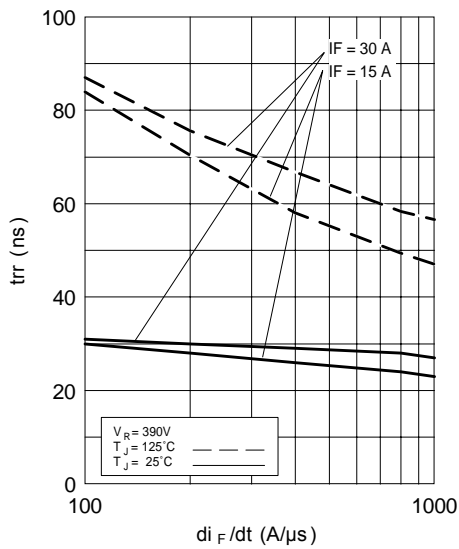
Fig. 4 - Max. Thermal Impedance  $Z_{thJC}$  Characteristics



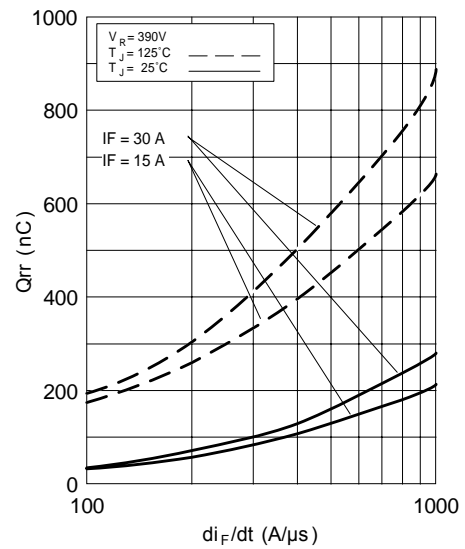
**Fig. 5 - Max. Allowable Case Temperature Vs. Average Forward Current**



**Fig. 6 - Forward Power Loss Characteristics**

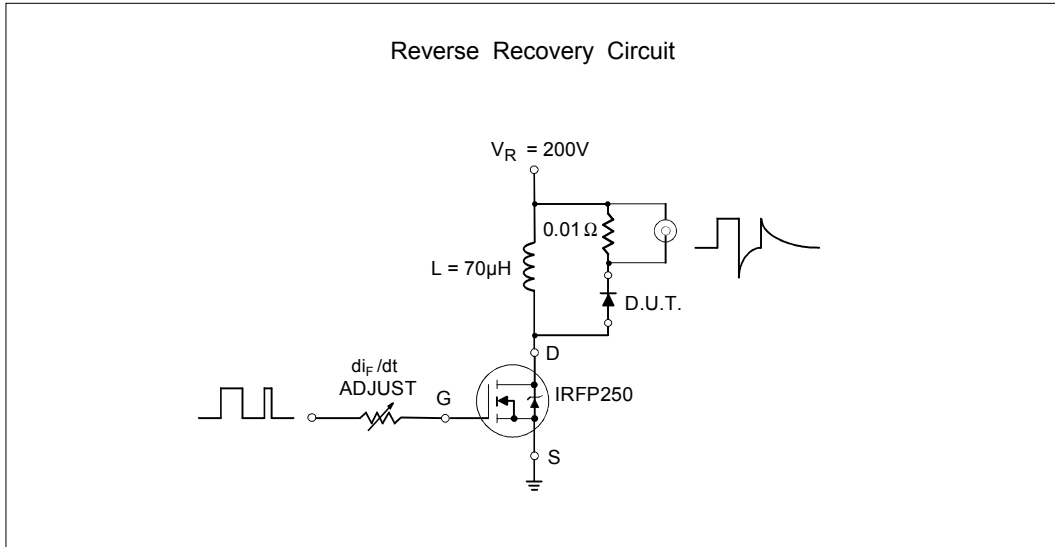


**Fig. 7 - Typical Reverse Recovery vs. di<sub>F</sub>/dt**

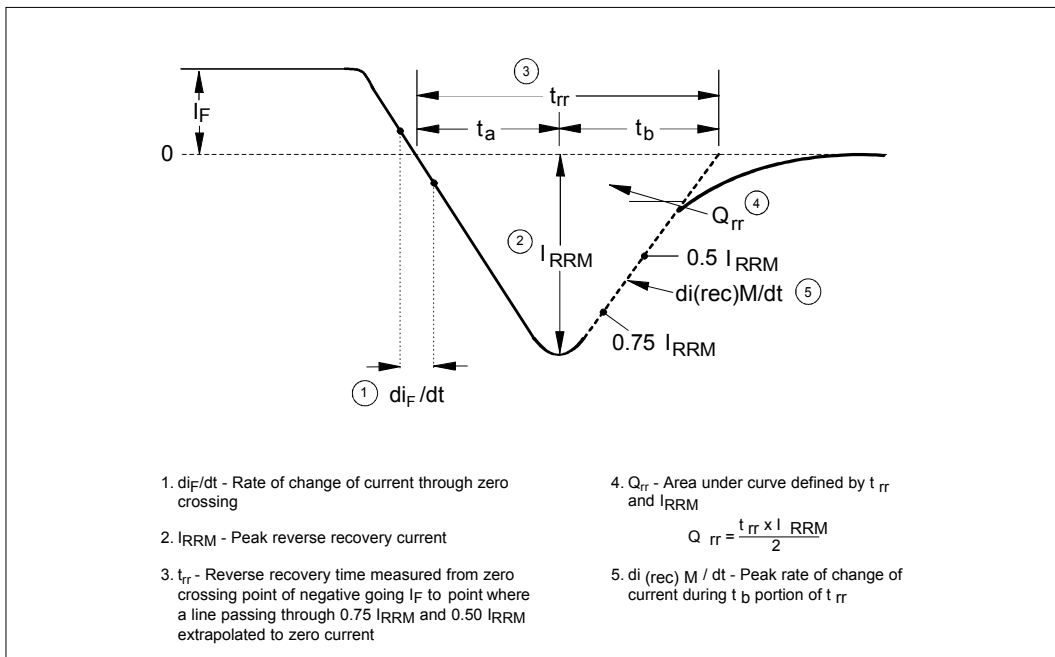


**Fig. 8 - Typical Stored Charge vs. di<sub>F</sub>/dt**

(3) Formula used:  $T_c = T_j - (P_d + P_{d_{REV}}) \times R_{thJC}$ ;  
 $P_d = \text{Forward Power Loss} = I_{F(AV)} \times V_{FM} @ (I_{F(AV)} / D)$  (see Fig. 6);  
 $P_{d_{REV}} = \text{Inverse Power Loss} = V_{R1} \times I_R (1 - D); I_R @ V_{R1} = \text{rated } V_R$

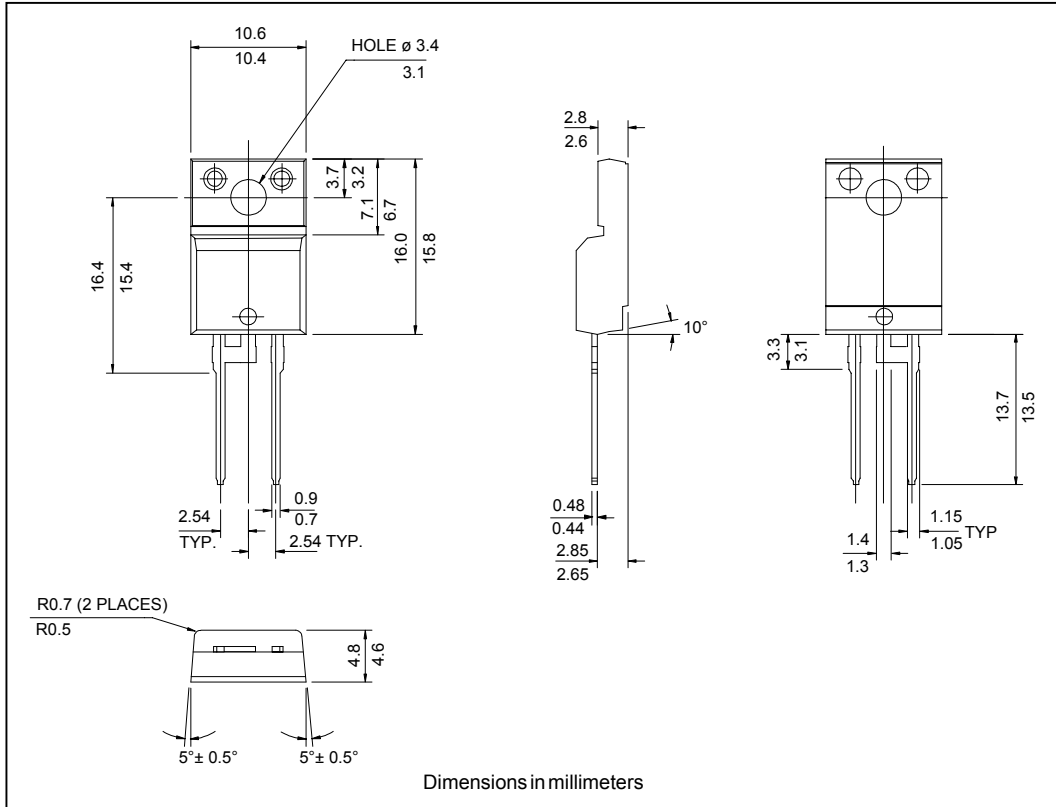


**Fig. 9- Reverse Recovery Parameter Test Circuit**

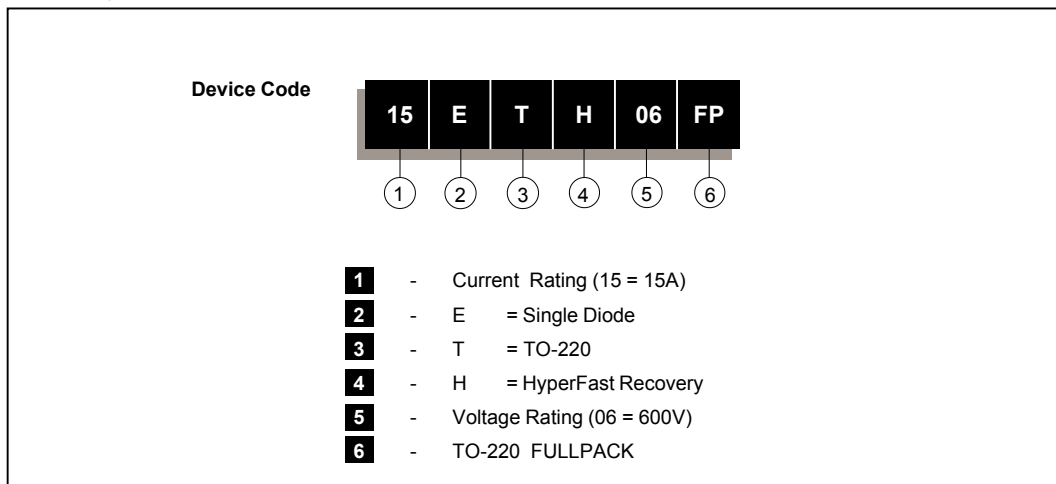


**Fig. 10 - Reverse Recovery Waveform and Definitions**

Outline Table



Ordering Information Table



Data and specifications subject to change without notice.  
This product has been designed and qualified for Industrial Level.  
Qualification Standards can be found on IR's Web site.

International  
**IOR** Rectifier

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