



DFLS230Q

2.0A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER PowerDI® 123

Product Summary

V _R (V)	I _F (A)	V _{F MAX} (V) @ +25°C	I _{R MAX} (mA) @ +25°C		
30	2.0	0.49	1.0		

Description and Applications

This Schottky Barrier Rectifier has been designed to meet the stringent requirements of Automotive Applications. It is ideally suited to use as:

- Polarity Protection Diode
- Re-circulating Diode
- Switching Diode

Features

- Ultra-Small Surface Mount Package
- Low Power Loss, High Efficiency
- Patented Interlocking Clip Design for High Surge Current
- Capacity
- Low Forward Voltage Drop
- Guard Ring Die Construction for Transient Protection
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Mechnical Data

- Case: PowerDI 123
- Case Material: Molded Plastic, "Green" Molding Compound.
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208(3)
- Weight: 0.01 grams (approximate)

Top View



Ordering Information (Note 5)

ĺ	Part Number	Compliance	Case	Packaging
	DFLS230Q-7	Automotive	PowerDI®123	3000/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q10x qualified and are PPAP capable. Automotive, AEC-Q10x and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_compliance_definitions/.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



F01A = Product Type Marking Code YM = Date Code Marking Y = Year (ex: B = 2014) M = Month (ex: 9 = September)

Date Code Key

Date Code Rey									
Year	2014	2015	2016	2017	2018	2019	2020	2021	2022
Code	В	С	D	Е	F	G	Н	I	J

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

Document number: DS37064 Rev. 1 - 2



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	30	V
Average Forward Current	I _{F(AV)}	2.0	Α
Non-Repetitive Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load	I _{FSM}	40	А

Thermal Characteristics

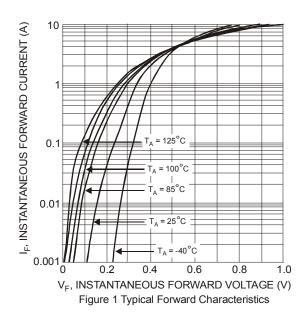
Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance Junction to Ambient (Note 6)	R _{⊝JA}	60	_	°C/W
Thermal Resistance Junction to Ambient (Note 7)	$R_{\Theta JA}$	180	_	°C/W
Thermal Resistance Junction to Soldering (Note 8)	R _{OJS}	10	_	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to	+125	°C

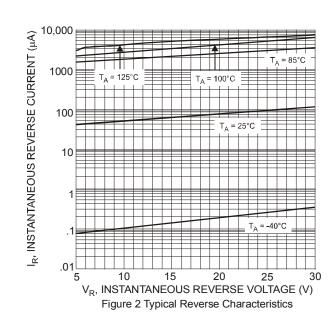
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 9)	$V_{(BR)R}$	30			V	I _R = 1.5mA
Forward Voltage	V _F		0.36 0.4	0.42 0.49	V	$I_F = 1.0A, T_A = +25^{\circ}C$ $I_F = 2.0A, T_A = +25^{\circ}C$
Leakage Current (Note 9)	I _R		0.15	1.0	mA	$V_R = 30V, T_A = +25^{\circ}C$
Total Capacitance	C _T		75		pF	V _R = 10V, f = 1.0MHz

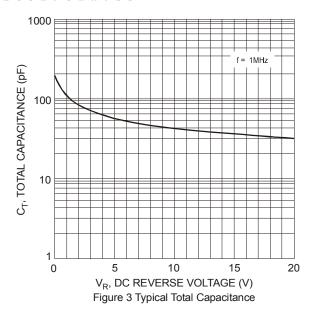
Notes:

- 6. Part mounted on 50.8mm X 50.8mm GETEK board with 25.4mm X 25.4mm copper pad, 25% anode, 75% cathode. $T_A = +25^{\circ}C$
- 7. Part mounted on FR-4 board with 1.8mm X 2.5mm cathode and 1.8mm X 1.2mm anode, 1 oz. copper pads. TA = +25°C
- 8. Theoretical R_{9JS} calculated from the top center of the die straight down to the PCB cathode tab solder junction.
- 9. Short duration pulse test to minimize self-heating effect.



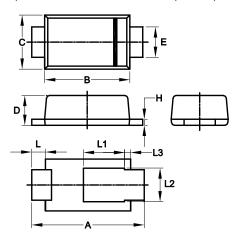






Package Outline Dimensions

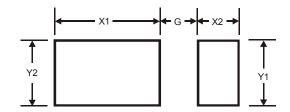
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



POWERDI [®] 123								
Dim	Min	Max	Тур					
A	3.50	3.90	3.70					
В	2.60	3.00	2.80					
С	1.63	1.93	1.78					
D	0.93	1.00	0.98					
Е	0.85	1.25	1.00					
Н	0.15	0.25	0.20					
L	0.40	0.50	0.45					
L1	1.25	1.40	1.35					
L2	1.025	1.125	1.10					
L3	0.125	0.275	0.20					
All Dimensions in mm								

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
G	1.0
X1	2.2
X2	0.9
Y1	1.4
Y2	1.4



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