



COMPLEMENTARY PAIR ENHANCEMENT MODE MOSFET

Product Summary

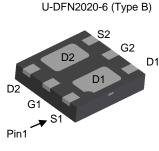
Device	BV _{DSS}	Rds(on) max	I _{D MAX} T _A = +25°С
		$34m\Omega @ V_{GS} = 4.5V$	5.1A
Q1	12V	$40m\Omega @ V_{GS} = 2.5V$	4.7A
N-Channel		50mΩ @ V _{GS} = 1.8V	4.2A
		70mΩ @ V _{GS} = 1.5V	3.6A
		$59m\Omega @ V_{GS} = -4.5V$	-3.9A
Q2 P-Channel	-12	81mΩ @ V _{GS} = -2.5V	-3.3A
		$115m\Omega @ V_{GS} = -1.8V$	-2.8A
		$215m\Omega @ V_{GS} = -1.5V$	-2.0A

Description and Applications

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Load Switch
- **Power Management Functions**
- Portable Power Adaptors





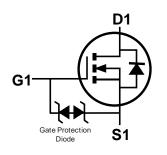
Bottom View

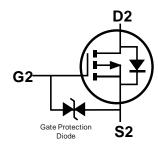
Features

- Low On-Resistance
- Low Input Capacitance
- Low Profile, 0.6mm Max Height
- **ESD** Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- **PPAP Capable (Note 4)**

Mechanical Data

- Case: U-DFN2020-6 (Type B)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020 •
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @4)
- Terminals Connections: See Diagram Below
- Weight: 0.0065 grams (Approximate)





N-CHANNEL MOSFET

P-CHANNEL MOSFET Internal Schematic

Ordering Information (Note 5)

Part Number	Case	Packaging
DMC1030UFDBQ-7	U-DFN2020-6 (Type B)	3000/Tape & Reel
DMC1030UFDBQ-13	U-DFN2020-6 (Type B)	10000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + CI) and 4.1000pm antimony compounds.
 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product_compliance_definitions.html.

5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

Notes:

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D3 = Product Type Marking Code
YM = Date Code Marking

ex: D = 2016)

(ex: 9 = September)

Date Code Key												
Year	201	5	2016		2017	20	18	2019		2020	2	2021
Code	С		D		E	I	-	G		Н		-
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Q1 N-CHANNEL	Q2 P-CHANNEL	Unit
Drain-Source Voltage			V _{DSS}	12	-12	V
Gate-Source Voltage			V _{GSS}	±8	±8	V
Continuous Drain Current (Note 6) N-CHANNEL: V _{GS} = 4.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	5.1 4.1	-3.9 -3.1	A
P-CHANNEL: $V_{GS} = 4.5V$	t < 5s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	Ι _D	6.6 5.3	-5.0 -4.0	A
Maximum Continuous Body Diode Forward Current (Note 6)			Is	2	-1.7	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	35	-25	A
Avalanche Current (L = 0.1mH)			IAS	5	-5	А
Avalanche Energy (L = 0.1mH)			E _{AS}	4	4	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 6)	Steady State	D -	1.36	W	
	t < 5s	PD	1.89	vv	
Thermal Desistance, Junction to Ambient (Note 6)	Steady State	R	92		
Thermal Resistance, Junction to Ambient (Note 6)	t < 5s	R _{θJA}	66	°C/W	
Thermal Resistance, Junction to Case (Note 6)		$R_{ ext{ heta}JC}$	18		
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C	

Electrical Characteristics Q1 N-CHANNEL (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)		IVIIII	тур	IVIAX	Unit	Test Condition	
Drain-Source Breakdown Voltage	D\/	12		_	V	$\lambda = 0$	
	BV _{DSS}		—		-	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	IDSS	_		1.0	μA	$V_{DS} = 12V, V_{GS} = 0V$	
Gate-Source Leakage	Igss		—	±10	μA	$V_{GS} = \pm 8V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)				Т	ł	1	
Gate Threshold Voltage	V _{GS(TH)}	0.4	—	1	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
		_	17	34		$V_{GS} = 4.5V, I_D = 4.6A$	
Static Drain-Source On-Resistance	P	—	20	40	mΩ	$V_{GS} = 2.5V, I_D = 4.2A$	
	R _{DS(ON)}	_	24	50	11152	$V_{GS} = 1.8V, I_D = 3.8A$	
		_	28	70		$V_{GS} = 1.5V, I_D = 1.5A$	
Diode Forward Voltage	V _{SD}		0.7	1.2	V	$V_{GS} = 0V, I_S = 4.8A$	
DYNAMIC CHARACTERISTICS (Note 8)	•		•		•	·	
Input Capacitance	C _{iss}	_	1003	_	pF		
Output Capacitance	Coss		132	_	pF	$V_{DS} = 6V, V_{GS} = 0V,$ - f = 1.0MHz	
Reverse Transfer Capacitance	Crss		115	_	pF		
Gate Resistance	Rg		11.3	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	0		12.2	—	nC		
Total Gate Charge (V _{GS} = 8V)	Qg		23.1	—	nC		
Gate-Source Charge	Q _{gs}		1.3	—	nC	$V_{DS} = 10V, I_D = 6.8A$	
Gate-Drain Charge	Q _{gd}		1.5	—	nC		
Turn-On Delay Time	t _{D(ON)}	_	4.4	—	ns		
Turn-On Rise Time	t _R	_	7.4	—	ns	$V_{DD} = 6V, V_{GS} = 4.5V,$	
Turn-Off Delay Time	t _{D(OFF)}	_	18.8	_	ns	$R_L = 1.1\Omega, R_G = 1\Omega$	
Turn-Off Fall Time	t _F	_	4.9	—	ns	7	
Body Diode Reverse Recovery Time	t _{RR}		7.6	_	ns	I _S = 5.4A, dI/dt = 100A/µs	
Body Diode Reverse Recovery Charge	Q _{RR}	_	0.9	—	nC	I _S = 5.4A, dl/dt = 100A/µs	

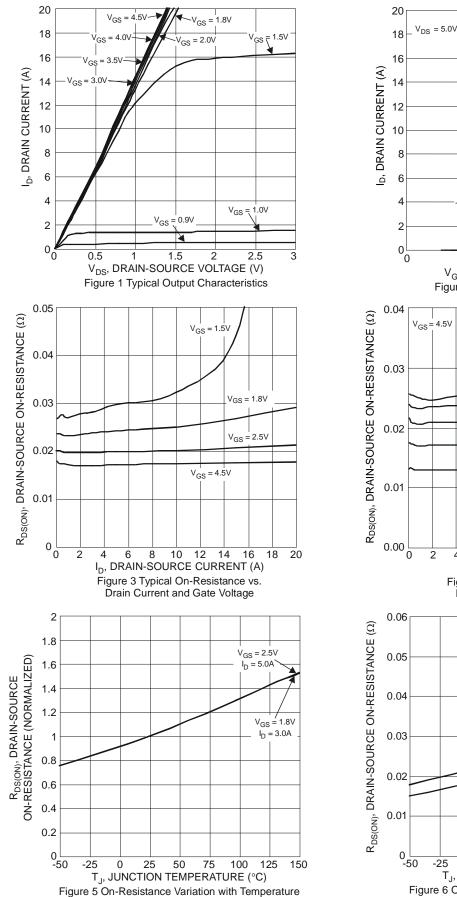
 Notes:
 6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.

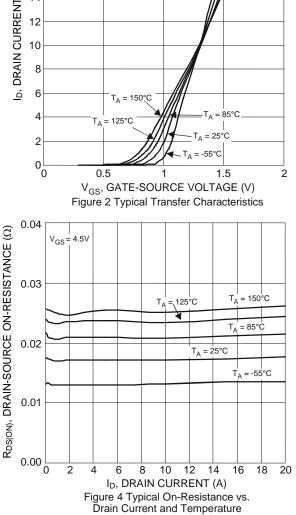
 7. Short duration pulse test used to minimize self-heating effect.

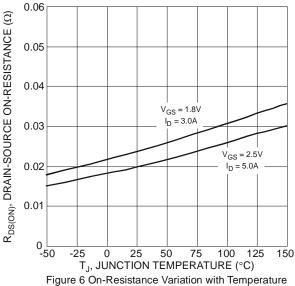
8. Guaranteed by design. Not subject to product testing.





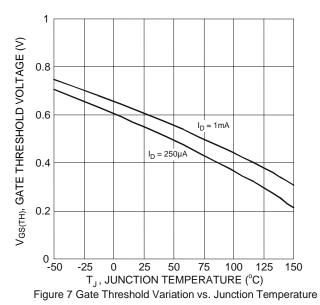


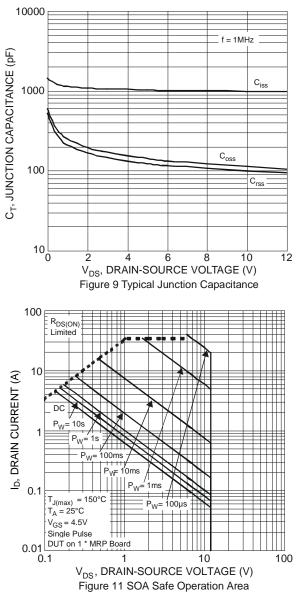


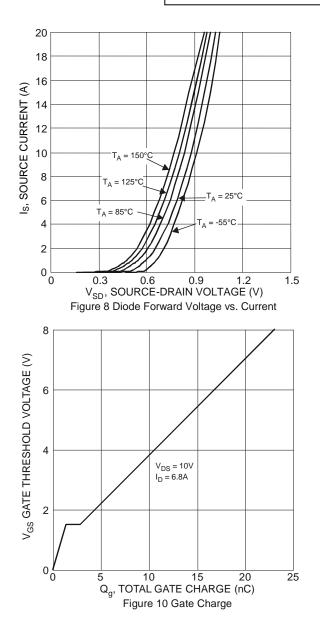




DMC1030UFDBQ





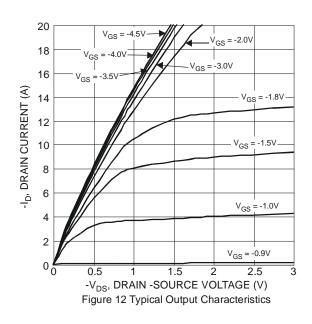


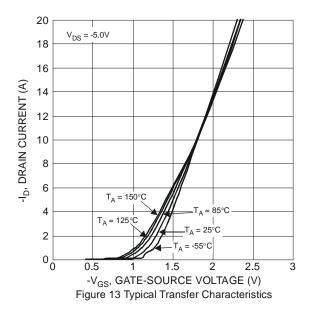


Electrical Characteristics Q2 P-CHANNEL (@ T_A = +25°C, unless otherwise specified.)

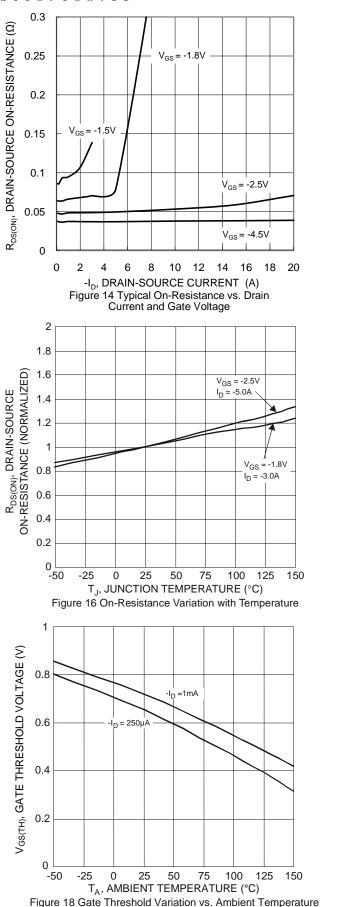
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-12	_		V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	_	-1.0	μA	$V_{DS} = -12V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±10	μA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)			•	•	•	·
Gate Threshold Voltage	V _{GS(TH)}	-0.4	—	-1	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$
		_	37	59		$V_{GS} = -4.5V, I_D = -3.6A$
Static Drain-Source On-Resistance			48	81	mΩ	V _{GS} = -2.5V, I _D = -3.1A
Static Drain-Source On-Resistance	R _{DS(ON)}	_	69	115	1112	V _{GS} = -1.8V, I _D = -2.6A
		_	88	215		V _{GS} = -1.5V, I _D = -0.5A
Diode Forward Voltage	V _{SD}	_	-0.7	-1.2	V	V _{GS} = 0V, I _S = -3.7A
DYNAMIC CHARACTERISTICS (Note 8)						•
Input Capacitance	C _{iss}	_	1028	_	pF	
Output Capacitance	C _{oss}	_	285	_	pF	−V _{DS} = -6V, V _{GS} = 0V, −f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	254	_	pF	
Gate Resistance	Rg	_	19.6	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge (V _{GS} = -4.5V)		_	13		nC	
Total Gate Charge (V _{GS} = -8V)	Qg	_	20.8		nC	
Gate-Source Charge	Q _{gs}	_	1.8	_	nC	$V_{DS} = -10V, I_{D} = -4.7A$
Gate-Drain Charge	Q _{gd}	_	4.5	_	nC	
Turn-On Delay Time	t _{D(ON)}	_	5.6	_	ns	
Turn-On Rise Time	t _R	_	12.8	_	ns	$V_{DD} = -6V, V_{GS} = -4.5V,$
Turn-Off Delay Time	t _{D(OFF)}	_	30.7	—	ns	$R_L = 1.6\Omega, R_G = 1\Omega$
Turn-Off Fall Time	t _F	_	25.4	—	ns	
Body Diode Reverse Recovery Time	t _{RR}	—	31.6	—	ns	$I_{S} = -3.6A, dI/dt = 100A/\mu s$
Body Diode Reverse Recovery Charge	Q _{RR}	—	7.8	_	nC	I _S = -3.6A, dI/dt = 100A/µs

Notes: 7. Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing.









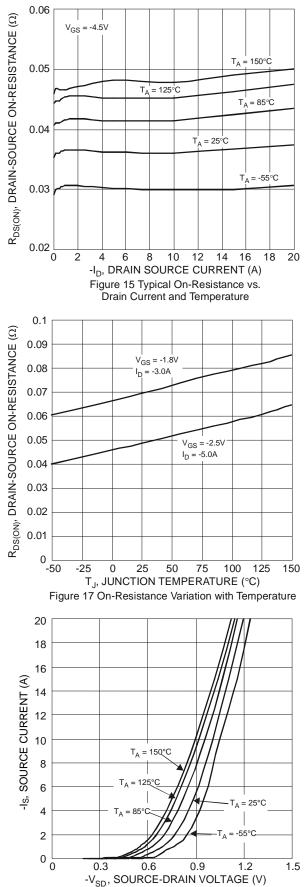


Figure 19 Diode Forward Voltage vs. Current

DMC1030UFDBQ

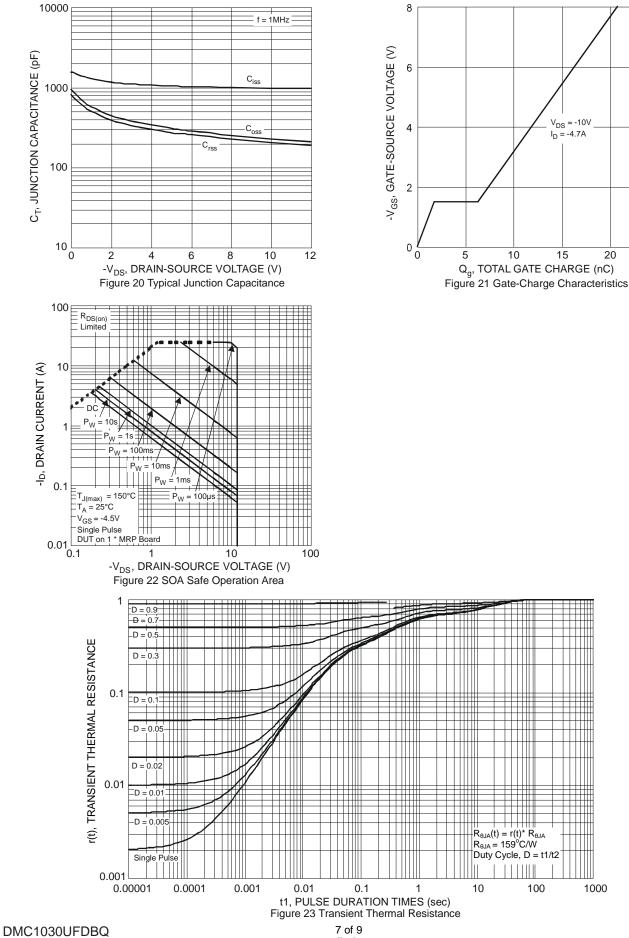
DMC1030UFDBQ Document number: DS38242 Rev.1 - 2



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Document number: DS38242 Rev.1 - 2

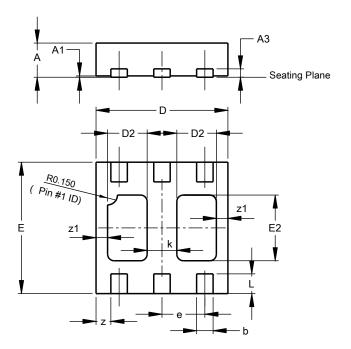
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Package Outline Dimensions

Please see AP02001 at http://www.diodes.com/_files/datasheets/ap02001.pdf for the latest version.



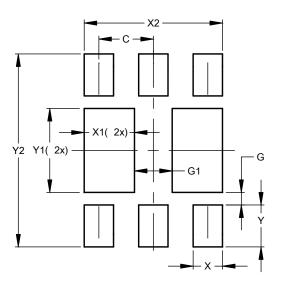
U-DFN2020-6 Type B							
Dim	Min	Max	Тур				
Α	0.545	0.605	0.575				
A1	0.00	0.05	0.02				
A3	-	-	0.13				
b	0.20	0.30	0.25				
D	1.95	2.075	2.00				
D2	0.50	0.70	0.60				
e	-	-	0.65				
ш	1.95	2.075	2.00				
E2	0.90	1.10	1.00				
k	-	-	0.45				
L	0.25	0.35	0.30				
z	-	-	0.225				
z1	-	-	0.175				
All	Dimens	ions in	mm				

U-DFN2020-6 (Type B)

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/_files/datasheets/ap02001.pdf for the latest version.

U-DFN2020-6 (Type B)



Dimensions	Value (in mm)
С	0.650
G	0.150
G1	0.450
Х	0.350
X1	0.600
X2	1.650
Y	0.500
Y1	1.000
Y2	2.300



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