



COMPLEMENTARY PAIR ENHANCEMENT MODE MOSFET

Product Summary

Device	V _{(BR)DSS}	R _{DS(ON)} max	l _D max T _A = +25°C
Q1	20V	0.5Ω @ V _{GS} = 4.5V	1030mA
QI	200	0.9Ω @ V _{GS} = 1.8V	740mA
Q2	-20V	1.0Ω @ V _{GS} = -4.5V	-700mA
QZ	-20V	2.0Ω @ V _{GS} = -1.8V	-460mA

Description

This new generation MOSFET has been designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Power management functions
- Battery Operated Systems and Solid-State Relays
- Load switch

Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage V_{GS(th)} <1V
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Complementary Pair MOSFET
- Ultra-Small Surface Mount Package
- ESD Protected Gate to 2kV HBM
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

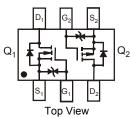
- Case: SOT563
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.003 grams (approximate)





SOT563

Bottom View



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMC2400UV-7	SOT563	3000/Tape & Reel
DMC2400UV-13	SOT563	10000/Tape & Reel

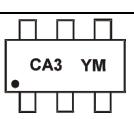
Notes: 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



CA3 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Y = 2011) M = Month (ex: 9 = September)

Date Code Key

Date Code Rey												
Year	201	1	2012		2013	20	14	2015		2016	2	2017
Code	Y		Z		А	[3	С		D		E
Month	Jan	Feb	Mar	Apr	Mav	Jun	Jul	Δυσ	Sep	Oct	Nov	Dec
WOTUT	Jali	ren	IVIAI	Арі	iviay	Juli	Jui	Aug	Sep	000	NOV	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings - Q1 N-CHANNEL (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units	
Drain-Source Voltage	V _{DSS}	20	V		
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Note C) // 4.5//	Steady State	T _A = +25°C T _A = +70°C	ID	1030 800	mA
Continuous Drain Current (Note 6) V _{GS} = 4.5V t<10s		T _A = +25°C T _A = +70°C	ID	1150 900	mA
$\begin{array}{c} \text{Steady} & \text{T}_{\text{A}} = +25^{\circ}\text{C} \\ \text{State} & \text{T}_{\text{A}} = +70^{\circ}\text{C} \end{array}$			ID	740 570	mA
Continuous Drain Current (Note 6) V_{GS} = 1.8V	I _D	870 700	mA		
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I _{DM}	3	A
Maximum Body Diode Continuous Current	IS	800	mA		

Maximum Ratings - Q2 P-CHANNEL (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units	
Drain-Source Voltage	V _{DSS}	-20	V		
Gate-Source Voltage			V _{GSS}	±8	V
State $T_A = +70^{\circ}$		T _A = +25°C T _A = +70°C	ID	-700 -550	mA
Continuous Drain Current (Note 6) V_{GS} = -4.5V	t<10s	T _A = +25°C T _A = +70°C	ID	-820 -640	mA
Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$			ID	-460 -350	mA
Continuous Drain Current (Note 6) $V_{GS} = -1.8V$ t<10s $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$			ID	-550 -420	mA
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I _{DM}	-2	A
Maximum Body Diode Continuous Current	Is	-800	mA		

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	PD	0.45	W	
Thermal Decisiones Junction to Ambient (Note 5)	Steady state	D	281	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	210	°C/W	
Total Power Dissipation (Note 6)		PD	1	W
Thermal Decisioner, Junction to Ambient (Note C)	Steady state	D	129	°C/W
Thermal Resistance, Junction to Ambient (Note 6) t<10s		R _{0JA}	97	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

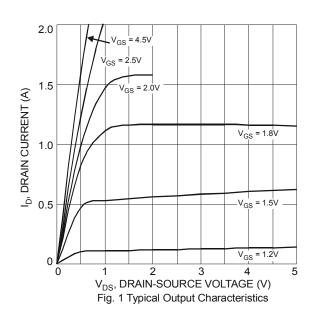


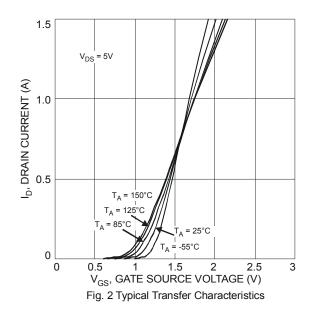
Electrical Characteristics - Q1 N-CHANNEL (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)			•		•	
Drain-Source Breakdown Voltage	BV _{DSS}	20	—	_	V	V _{GS} = 0V, I _D = 1mA
Zero Gate Voltage Drain Current TJ = +25°C	I _{DSS}	_	_	100	nA	V _{DS} = 20V, V _{GS} = 0V
		_	—	±1		$V_{GS} = \pm 5V, V_{DS} = 0V$
Gate-Source Leakage	I _{GSS}	_	—	±4.0	μA	V_{GS} = ±8V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						·
Gate Threshold Voltage	V _{GS(th)}	0.5	—	0.9	V	V_{DS} = V_{GS} , I_D = 250 μ A
		_	0.3	0.48		V _{GS} = 5.0V, I _D = 200mA
		_	0.35	0.5		V _{GS} = 4.5V, I _D = 200mA
Static Drain-Source On-Resistance	B	_	0.45	0.7	Ω	V _{GS} = 2.5V, I _D = 200mA
Static Drain-Source On-Resistance	R _{DS(ON)}	_	0.55	0.9		V _{GS} = 1.8V, I _D = 100mA
	-	_	0.65	1.5		V _{GS} = 1.5V, I _D = 50mA
		_	2	—		V _{GS} = 1.2V, I _D = 1mA
Forward Transfer Admittance	Y _{fs}	_	1.4	—	S	V _{DS} = 3V, I _D = 200mA
Diode Forward Voltage	V _{SD}	_	0.7	1.2	V	V _{GS} = 0V, I _S = 500mA,
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	_	37.1	—		
Output Capacitance	C _{oss}	_	6.5	_	pF	V _{DS} = 10V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	4.8	—		
Gate Resistance	Rg	_	68	—	Ω	$V_{DS} = 0V, V_{GS} = 0V,$
Total Gate Charge	Qg	_	0.5	—		
Gate-Source Charge	Q _{gs}	_	0.07	—	nC	V_{GS} = 4.5V, V_{DS} = 10V, I _D = 250mA
Gate-Drain Charge	Q _{gd}	_	0.1	—		
Turn-On Delay Time	t _{D(on)}	_	4.06	—		
Turn-On Rise Time	tr	_	7.28	—		$V_{DD} = 10V, V_{GS} = 4.5V,$
Turn-Off Delay Time	t _{D(off)}	_	13.74	—	ns	$R_L = 47\Omega, R_G = 10\Omega,$ $I_D = 200mA$
Turn-Off Fall Time	t _f	_	10.54	—	1	

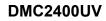
Notes:

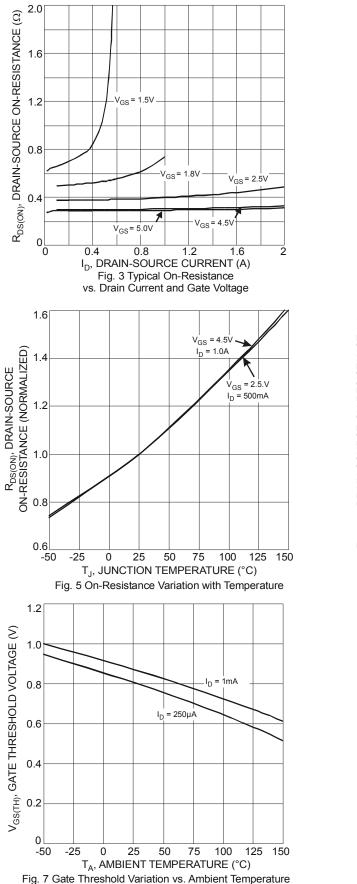
Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing.

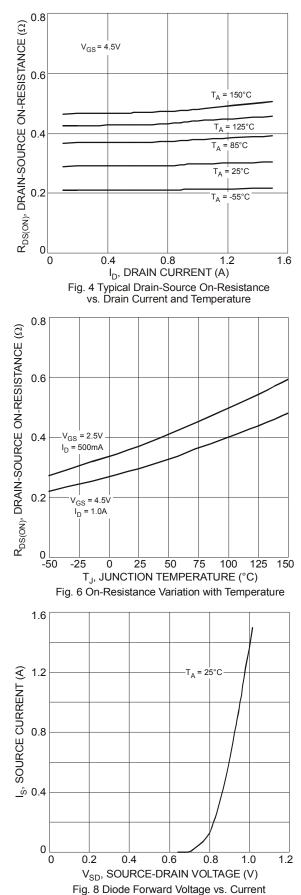






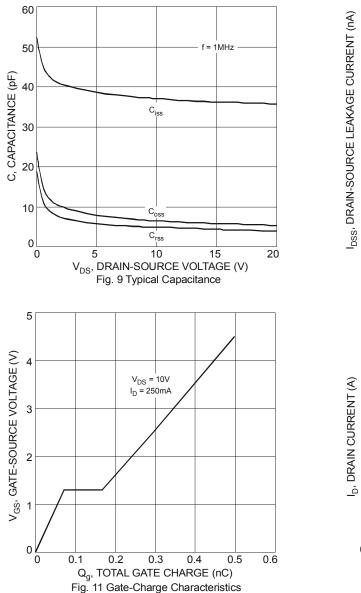


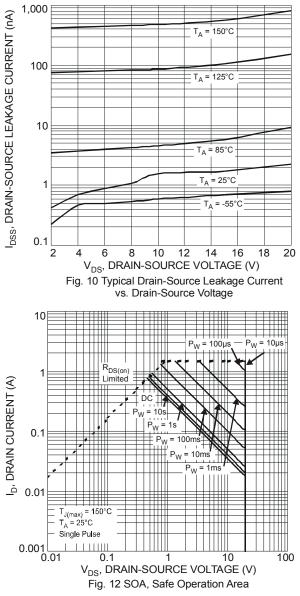




DMC2400UV





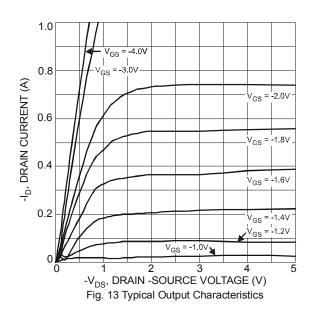


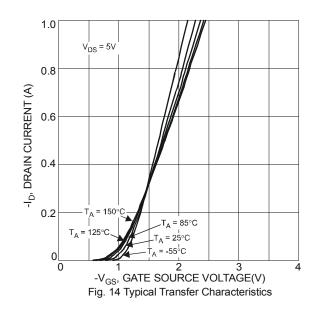


Electrical Characteristics - Q2 P-CHANNEL (@T_A = +25°C, unless otherwise specified.)

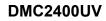
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						·
Drain-Source Breakdown Voltage	BV _{DSS}	-20	—	_	V	$V_{GS} = 0V, I_D = -1mA$
Zero Gate Voltage Drain Current TJ = 25°C	I _{DSS}	_	_	-100	nA	V_{DS} = -20V, V_{GS} = 0V
Gate-Source Leakage	1	—	-	±1.0		$V_{GS} = \pm 5V, V_{DS} = 0V$
Gale-Source Leakage	I _{GSS}	_	_	±5.0	μΑ	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V _{GS(th)}	-0.5		-1.0	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$
		—	0.67	0.97		V_{GS} = -5V, I_{D} = -100mA
		—	0.7	1.0		V_{GS} = -4.5V, I_{D} = -100mA
Static Drain-Source On-Resistance		—	0.9	1.5	Ω	V _{GS} = -2.5V, I _D = -80mA
Static Drain-Source On-Resistance	R _{DS (ON)}	—	1.2	2.0		V _{GS} = -1.8V, I _D = -40mA
		_	1.5	3.0		V _{GS} = -1.5V, I _D = -30mA
		_	5	—		V _{GS} = -1.2V, I _D = -1mA
Forward Transfer Admittance	Y _{fs}	_	0.7	—	S	V _{DS} = -3V, I _D = -100mA
Diode Forward Voltage	V _{SD}	_	-0.75	-1.2	V	V _{GS} = 0V, I _S = -330mA,
DYNAMIC CHARACTERISTICS (Note 7)						
Input Capacitance	C _{iss}	—	46.1	—		
Output Capacitance	C _{oss}	—	7.2	—	pF	V _{DS} = 10V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	—	4.9	-		1 - 1.00012
Gate Resistance	Rg	_	14.3	_	Ω	$V_{DS} = 0V, V_{GS} = 0V,$
Total Gate Charge V _{GS} = -4.5V	Qg	_	0.5	—		
Total Gate Charge V _{GS} = -10V	Qg	_	0.85	_	nC	V _{DS} = -10V, I _D = -250mA
Gate-Source Charge	Q _{gs}	—	0.09	—	nc	
Gate-Drain Charge	Q _{gd}	—	0.09	—]	
Turn-On Delay Time	t _{D(on)}	—	8.5	—		
Turn-On Rise Time	tr	_	4.3	_		$V_{DD} = -3V, V_{GS} = -2.5V,$
Turn-Off Delay Time	t _{D(off)}	_	20.2	_	ns	$R_{L} = 300\Omega, R_{G} = 25\Omega,$
Turn-Off Fall Time	t _f	_	19.2	_	1	I _D = -100mA

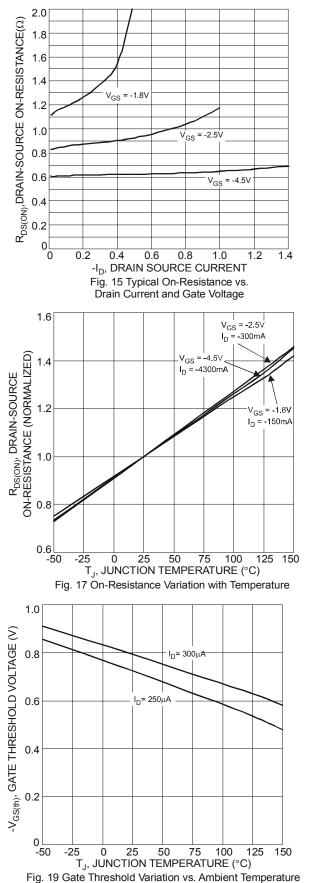
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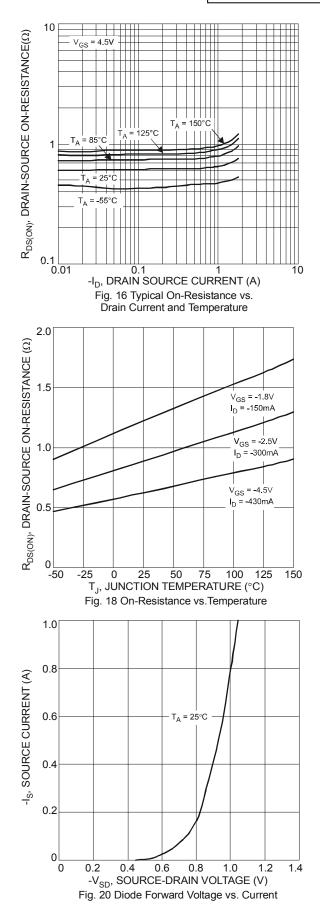






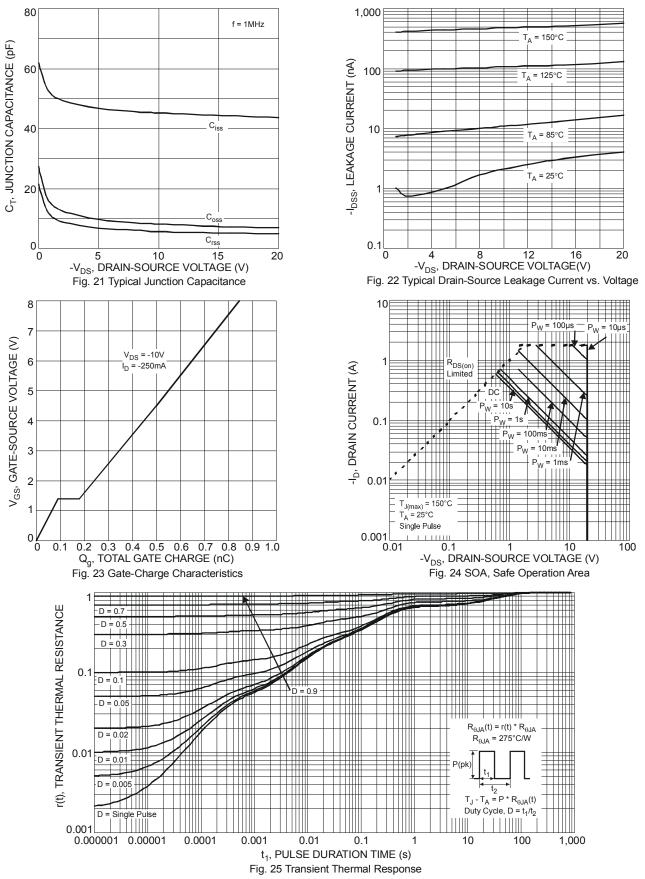








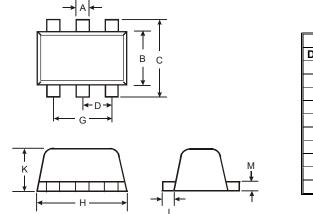
DMC2400UV





Package Outline Dimensions

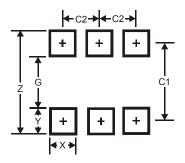
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	SOT563						
Dim	Min	Max	Тур				
Α	0.15	0.30	0.20				
В	1.10	1.25	1.20				
С	1.55	1.70	1.60				
D	-	-	0.50				
G	0.90	1.10	1.00				
н	1.50	1.70	1.60				
κ	0.55	0.60	0.60				
L	0.10	0.30	0.20				
М	0.10	0.18	0.11				
All	Dimens	sions in	mm				

Suggested Pad Layout

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



Dimensions	Value (in mm)
Z	2.2
G	1.2
Х	0.375
Y	0.5
C1	1.7
C2	0.5



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