



DMG1016VQ

COMPLEMENTARY PAIR ENHANCEMENT MODE MOSFET

Product Summary

Device	BV _{DSS}	R _{DS(ON)} max	I_D max $T_A = +25$ °C
		$0.4\Omega @ V_{GS} = 4.5V$	870mA
Q1 20	20V	0.5Ω @ V _{GS} = 2.5V	780mA
		$0.7\Omega @ V_{GS} = 1.8V$	640mA
	-20V	0.7Ω @ $V_{GS} = -4.5V$	-640mA
Q2		0.9Ω @ $V_{GS} = -2.5V$	-580mA
		1.3Ω @ V _{GS} = -1.8V	-465mA

Description and Applications

This MOSFET is designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Switches

Features

- Low On-Resistance
- Low Gate Threshold Voltage V_{GS(th)} <1V
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Complementary Pair MOSFET
- Ultra-Small Surface Mount Package
- ESD Protected Gate to 2.5kV HBM
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Mechanical Data

- Case: SOT-563
- Case Material: Molded Plastic, "Green" Molding Compound;
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminal Finish Matte Tin Annealed over Copper Leadframe;
 Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.006 grams (Approximate)

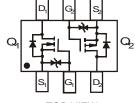
SOT-563





TOP VIEW





BOTTOM VIEW

TOP VIEW Internal Schematic

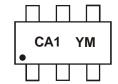
Ordering Information (Note 5)

Part Number	Case	Packaging
DMG1016VQ-7	SOT-563	3,000/Tape & Reel
DMG1016VQ-13	SOT-563	10,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_compliance_definitions/.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



CA1 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: W = 2009) M = Month (ex: 9 = September)

Date Code Key

Date Code Ney												
Year	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Code	W	Χ	Υ	Z	Α	В	С	D	Е	F	G	Н
						_			_	_		
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	Ο	N	D



Maximum Ratings (Q1 N-Channel) (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain Source Voltage	V_{DSS}	20	V
Gate-Source Voltage	V _{GSS}	±6	V
Drain Current (Note 6) $ T_A = +25^{\circ}C $ $ T_A = +85^{\circ}C $	l ln	870 630	mA

Maximum Ratings (Q2 P-Channel) (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain Source Voltage	V _{DSS}	-20	V
Gate-Source Voltage	V_{GSS}	±6	V
Drain Current (Note 6) $T_A = +25^{\circ}C$ $T_A = +85^{\circ}C$	l ln	-640 -460	mA

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	P_{D}	530	mW
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	235	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C



Electrical Characteristics (Q1 N-Channel) (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	100	nA	$V_{DS} = 20V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	± 1.0	μA	$V_{GS} = \pm 4.5V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(th)}	0.5	_	1.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
		_	0.3	0.4		$V_{GS} = 4.5V, I_D = 600mA$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	0.4	0.5	Ω	$V_{GS} = 2.5V, I_D = 500mA$	
		_	0.5	0.7		$V_{GS} = 1.8V, I_D = 350mA$	
Forward Transfer Admittance	Y _{fs}	_	1.4	_	S	$V_{DS} = 10V, I_D = 400 \text{mA}$	
Diode Forward Voltage (Note 7)	V _{SD}	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 150mA$	
DYNAMIC CHARACTERISTICS							
Input Capacitance	C _{iss}	_	60.67		pF	1/ 40)/)/ 0)/	
Output Capacitance	Coss	_	9.68		pF $V_{DS} = 16V, V_{GS} = 0V$ $f = 1.0MHz$		
Reverse Transfer Capacitance	C _{rss}	_	5.37	_	pF	1 = 1.0IVII IZ	
Total Gate Charge	Qg	_	736.6	_		\/ 45\/\/ 40\/	
Gate-Source Charge	Q_{gs}	_	93.6	_	рC	$V_{GS} = 4.5V, V_{DS} = 10V,$	
Gate-Drain Charge	Q_{gd}	_	116.6	_	$I_D = 250 \text{mA}$		
Turn-On Delay Time	t _{d(on)}	_	5.1	_	V 40V V 15		
Turn-On Rise Time	t _r	_	7.4	_	nS	$V_{DD} = 10V, V_{GS} = 4.5V,$	
Turn-Off Delay Time	t _{d(off)}	_	26.7	_	113	$R_L = 47\Omega$, $R_G = 10\Omega$, $I_D = 200\text{mA}$	
Turn-Off Fall Time	t _f	_	12.3			ID = ZUUIIIA	

Electrical Characteristics (Q2 P-Channel) (@T_A = +25°C, unless otherwise specified.)

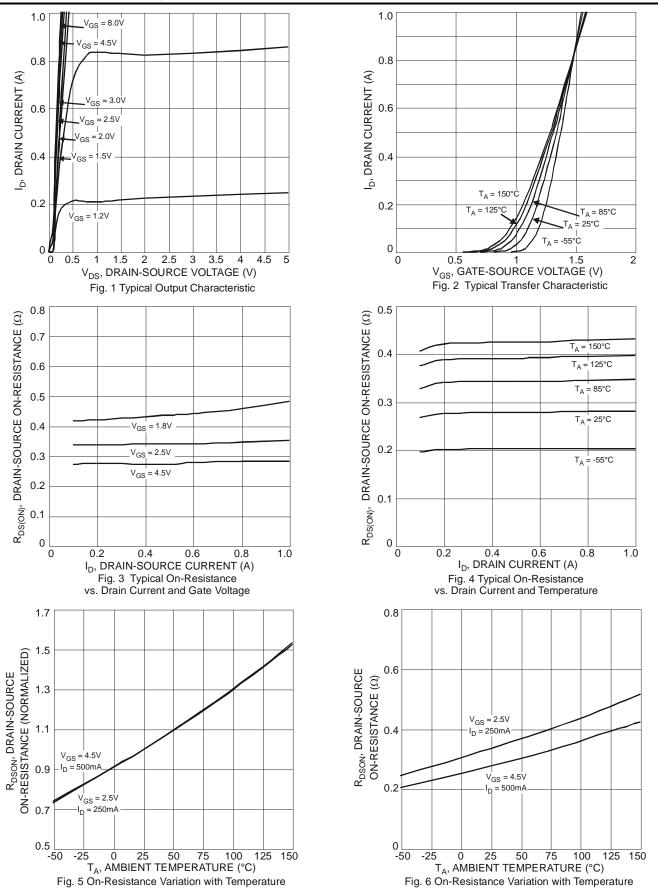
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}			-100	nA	$V_{DS} = -20V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	± 2.0	μΑ	$V_{GS} = \pm 4.5V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(th)}$	-0.5		-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
			0.5	0.7		$V_{GS} = -4.5V$, $I_{D} = -430mA$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	0.7	0.9	Ω	$V_{GS} = -2.5V, I_{D} = -300mA$	
			1.0	1.3		$V_{GS} = -1.8V, I_{D} = -150mA$	
Forward Transfer Admittance	Y _{fs}	_	-0.9	_	S	$V_{DS} = 10V, I_{D} = -250mA$	
Diode Forward Voltage (Note 7)	V_{SD}		-0.8	-1.2	V	$V_{GS} = 0V, I_{S} = -150mA$	
DYNAMIC CHARACTERISTICS							
Input Capacitance	C _{iss}	_	59.76	_	pF		
Output Capacitance	Coss	_	12.07	_	pF	$V_{DS} = -16V, V_{GS} = 0V$ - f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	6.36	_	pF	-1 = 1.0IVIIIZ	
Total Gate Charge	Qg	_	622.4	_		\\\\ 45\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	
Gate-Source Charge	Q_{gs}	_	100.3	_	$V_{GS} = -4.5V, V_{DS} = -4.5V$		
Gate-Drain Charge	Q_{gd}	_	132.2	_		$I_D = -250 \text{mA}$	
Turn-On Delay Time	t _{d(on)}		5.1			101/11/	
Turn-On Rise Time	t _r		8.1		$V_{DD} = -10V, V_{GS} = -10V$		
Turn-Off Delay Time	t _{d(off)}	_	28.4	_	113	$R_L = 47\Omega$, $R_G = 10\Omega$, $I_D = -200\text{mA}$	
Turn-Off Fall Time	t _f	_	20.7	_		ID = -200IIIA	

Notes:

6. Device mounted on FR-4 PCB.7. Short duration pulse test used to minimize self-heating effect.

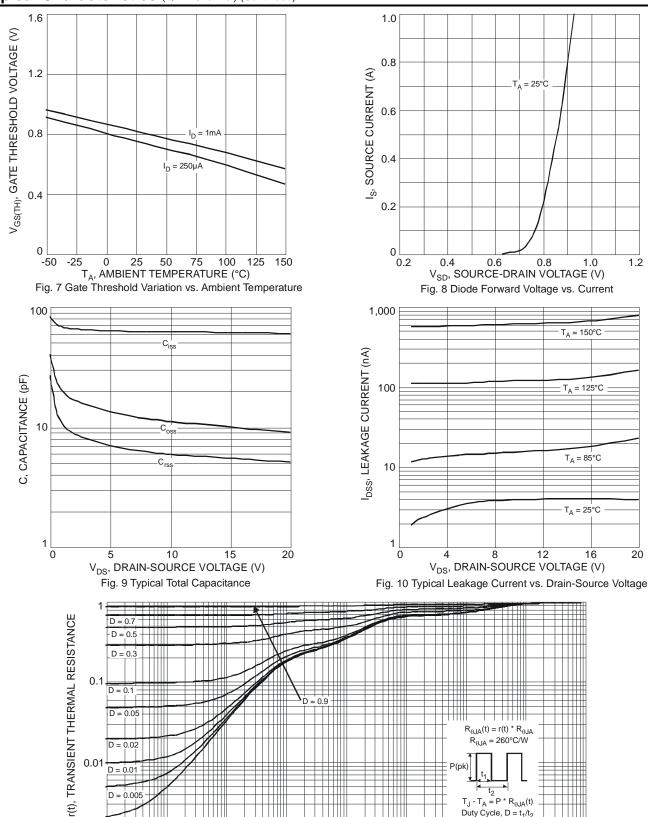


Typical Characteristics (Q1 N-Channel)





Typical Characteristics (Q1 N-Channel) (Continued)



0.1 t₁, PULSE DURATION TIME (s) Fig. 11 Transient Thermal Response

t₂ $T_J - T_A = P * R_{\theta JA}(t)$ Duty Cycle, $D = t_1/t_2$

100

1,000

10

D = 0.01

0.00001

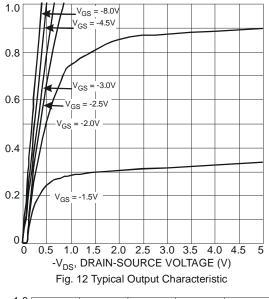
D = Single Pulse

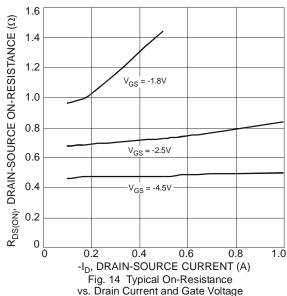
0.0001

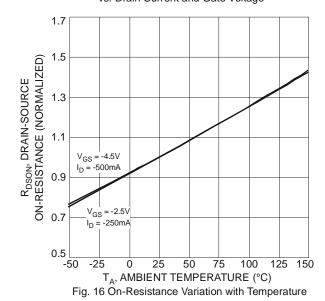
0.001



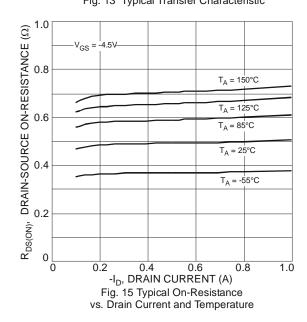
Typical Characteristics (Q2 P-Channel)

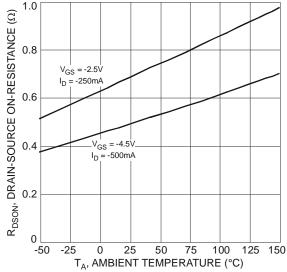






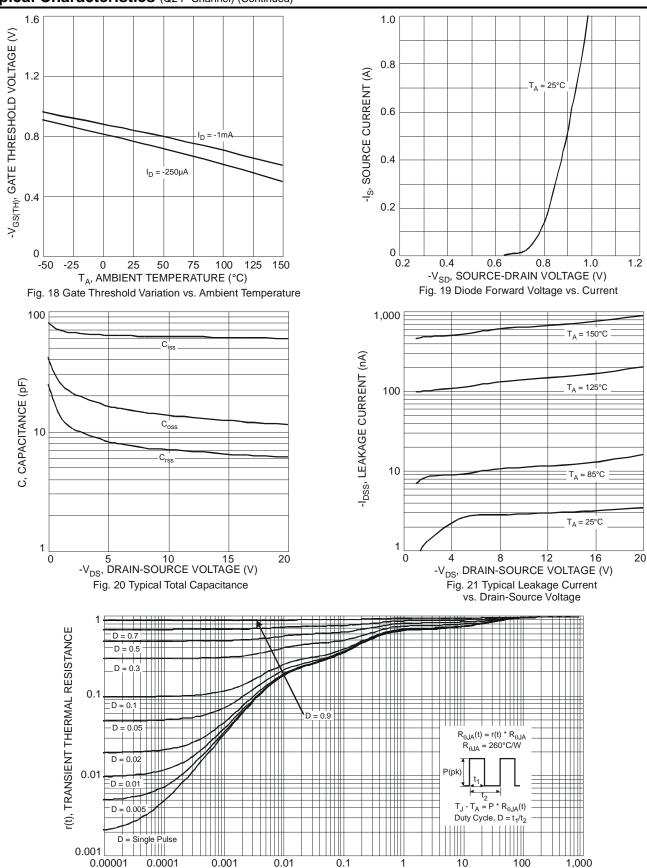
1.0 V_{DS} 0.8 -I_D, DRAIN CURRENT (A) 0.4 0.2 T_A = 85°C = 25°C 0 0 1.0 1.5 2.0 2.5 3.0 -V_{GS}, GATE-SOURCE VOLTAGE (V) Fig. 13 Typical Transfer Characteristic







Typical Characteristics (Q2 P-Channel) (Continued)



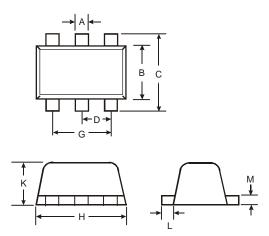
t₁, PULSE DURATION TIME (s) Fig. 22 Transient Thermal Response



Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

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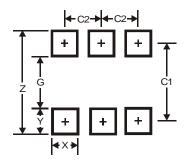


SOT-563							
Dim	Min	Max	Тур				
Α	0.15	0.30	0.20				
В	1.10	1.25	1.20				
C	1.55	1.70	1.60				
ם	_	-	0.50				
G	0.90	1.10	1.00				
Ξ	1.50	1.70	1.60				
K	0.55	0.60	0.60				
L	0.10	0.30	0.20				
М	0.10	0.18	0.11				
All	All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

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Dimensions	Value (in mm)
Z	2.2
G	1.2
Х	0.375
Y	0.5
C1	1.7
C2	0.5



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