

Product Summary

Device	BV _{DSS}	Rds(on) max	Ι _D T _A = +25°C
Q1 & Q4	100V	160mΩ @ V_{GS} = 10V	2.9A
	100 v	$200m\Omega @ V_{GS} = 4.5V$	2.6A
Q2 & Q3	-100V	250mΩ @ V _{GS} = -10V	-2.3A
		$300m\Omega @ V_{GS} = -4.5V$	-2.1A

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

High-Efficiency Bridge Rectifiers

Features

- Low On-Resistance
- Low Input Capacitance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: V-DFN5045-12
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0

6 S2

5 S2

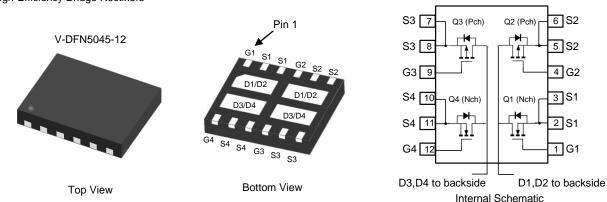
4 G2

3 S1

2 S1

1 G1

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @
- Weight: 0.056 grams (Approximate)



Ordering Information (Note 4)

Part Number Case		Tape Width	Packaging	
DMHC10H170SFJ-13	V-DFN5045-12	12mm	3,000/Tape & Reel	

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

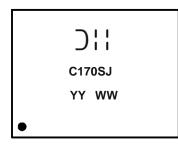
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

Notes:



☐]]=Manufacturer's Marking C170SJ = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 16 = 2016) WW = Week Code (01 to 53)



Maximum Ratings Q1 & Q4 N-Channel (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		V _{DSS}	100	V
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current (Note 5) V_{GS} = 10V	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	2.9 2.3	А
Maximum Body Diode Forward Current (Note 5)		Is	2.5	А
Pulsed Drain Current (10µs pulse, Duty Cycle = 1%)	I _{DM}	13	А	

Maximum Ratings Q2 & Q3 P-Channel (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	-100	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current (Note 5) V _{GS} = -10V	ID	-2.3 -1.9	A
Maximum Body Diode Forward Current (Note 5)	Is	-2.4	A
Pulsed Drain Current (10µs pulse, Duty Cycle = 1%)	I _{DM}	-11	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	PD	2.1	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	60	°C/W	
Thermal Resistance, Junction to Case (Note 5)	$R_{\theta JC}$	6	C/W	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Note: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.



Electrical Characteristics Q1 & Q4 N-Channel (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition			
OFF CHARACTERISTICS (Note 6)									
Drain-Source Breakdown Voltage	BV _{DSS}	100	_	—	V	$V_{GS} = 0V, I_D = 250 \mu A$			
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μA	$V_{DS} = 80V, V_{GS} = 0V$			
Gate-Source Leakage	IGSS	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$			
ON CHARACTERISTICS (Note 6)									
Gate Threshold Voltage	V _{GS(TH)}	1.0	2.0	3.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$			
Static Drain-Source On-Resistance	Б		111	160	mΩ	$V_{GS} = 10V, I_D = 5A$			
	R _{DS(ON)}	_	121	200	11152	$V_{GS} = 4.5V, I_D = 5A$			
Diode Forward Voltage	V _{SD}	_	0.9	1.0	V	$V_{GS} = 0V, I_{S} = 10A$			
DYNAMIC CHARACTERISTICS (Note 7)									
Input Capacitance	Ciss		1,167	—	pF	$V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz$			
Output Capacitance	Coss		36						
Reverse Transfer Capacitance	Crss		25	—					
Gate Resistance	R _G	_	1.3	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$			
Total Gate Charge ($V_{GS} = 4.5V$)	Qg		4.9			V 00V I 40.04			
Total Gate Charge (V _{GS} = 10V)	Qg	_	9.7	_	nC				
Gate-Source Charge	Q _{gs}	_	2.0	_	nc	$V_{DS} = 80V, I_D = 12.8A$			
Gate-Drain Charge	Q _{gd}		2.0	_					
Turn-On Delay Time	t _{D(ON)}	_	10.5	_		$V_{DD} = 50V, R_G = 25\Omega, I_D = 12.8A$			
Turn-On Rise Time	t _R		11.1						
Turn-Off Delay Time	t _{D(OFF)}	_	42.6	_	ns				
Turn-Off Fall Time	t _F		12.8	_	1				
Body Diode Reverse Recovery Time	t _{RR}		30.3	_	ns	V _{GS} = 0V, I _S = 12.8A, dI/dt = 100A/µs			
Body Diode Reverse Recovery Charge	Q _{RR}		35.2		nC	V _{GS} = 0V, I _S = 12.8A, dI/dt = 100A/µs			

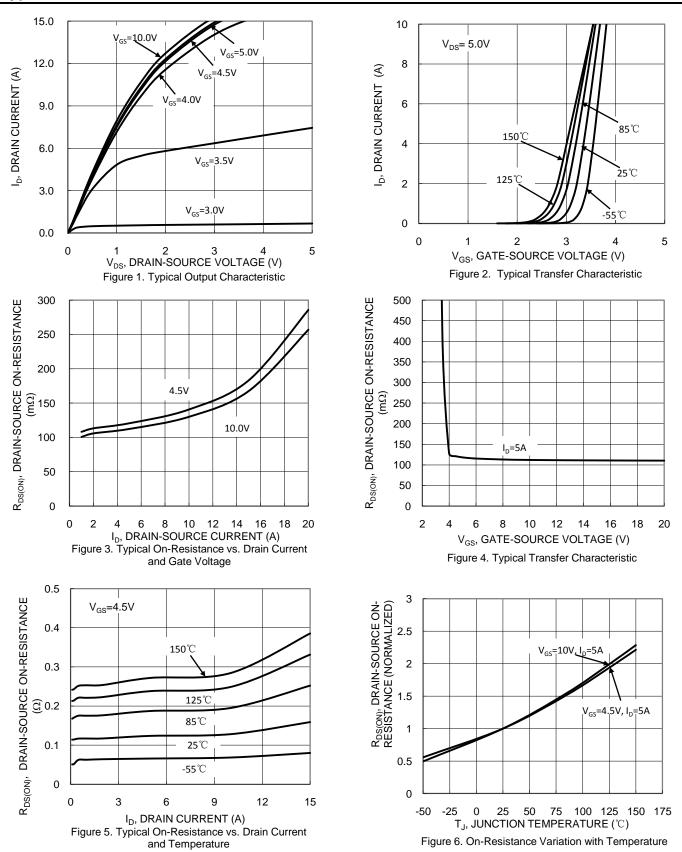
Electrical Characteristics Q2 & Q3 P-Channel (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition			
OFF CHARACTERISTICS (Note 6)									
Drain-Source Breakdown Voltage	BV _{DSS}	-100	_	—	V	$V_{GS} = 0V, I_D = -250\mu A$			
Zero Gate Voltage Drain Current	I _{DSS}	_	—	1	μA	$V_{DS} = -80V, V_{GS} = 0V$			
Gate-Source Leakage	I _{GSS}			±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$			
ON CHARACTERISTICS (Note 6)									
Gate Threshold Voltage	V _{GS(TH)}	-1.0	-1.6	-3.0	V	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$			
Static Drain-Source On-Resistance	Passa		191	250	mΩ	$V_{GS} = -10V, I_D = -5A$			
	R _{DS(ON)}		213	300	11152	$V_{GS} = -4.5V, I_D = -5A$			
Diode Forward Voltage	V _{SD}		-0.9	-1.2	V	$V_{GS} = 0V, I_{S} = -5A$			
DYNAMIC CHARACTERISTICS (Note 7)									
Input Capacitance	Ciss		1,239	—	pF	V _{DS} = -25V, V _{GS} = 0V, f = 1.0MHz			
Output Capacitance	C _{oss}		42						
Reverse Transfer Capacitance	C _{rss}		28	—					
Gate Resistance	Rg		13	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$			
Total Gate Charge (V _{GS} = -4.5V)	Qg		8.4	_		V _{DS} = -60V. I _D = -5A			
Total Gate Charge (V _{GS} = -10V)	Qg		17.5	—	nC				
Gate-Source Charge	Q _{gs}	_	2.8	_	10	$v_{\rm DS} = -60 v, \ i_{\rm D} = -5 A$			
Gate-Drain Charge	Q _{gd}	_	3.2	_					
Turn-On Delay Time	t _{D(ON)}	_	9.1	—					
Turn-On Rise Time	t _R	_	14.9	_					
Turn-Off Delay Time	tD(OFF)		57.4		ns	$V_{DD} = -50V, R_g = 9.1\Omega, I_D = -5A$			
Turn-Off Fall Time	t _F		34.4	_]				
Body Diode Reverse Recovery Time	t _{RR}		25.2		ns	V _{GS} = 0V, I _S = -5A, di/dt = 100A/µs			
Body Diode Reverse Recovery Charge	Q _{RR}		24.5	—	nC	V _{GS} = 0V, I _S = -5A, di/dt = 100A/µs			

Notes:6. Short duration pulse test used to minimize self-heating effect.7. Guaranteed by design. Not subject to production testing.

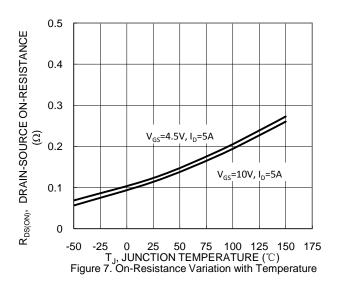


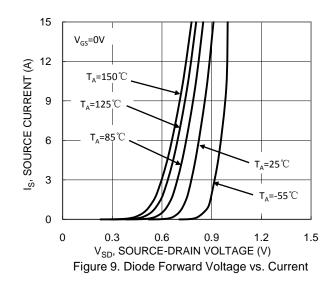
Typical Characteristics - N-CHANNEL

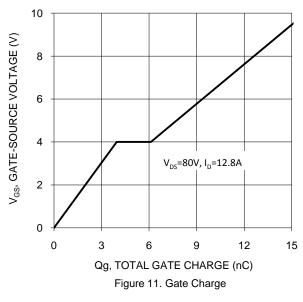


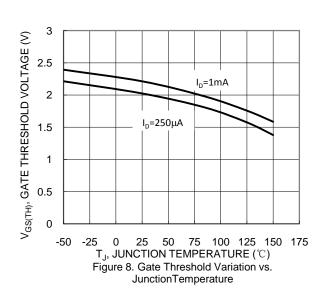
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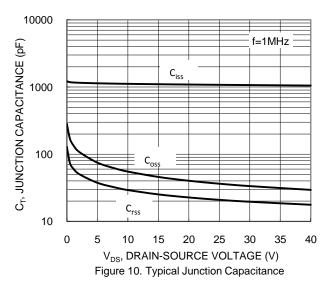


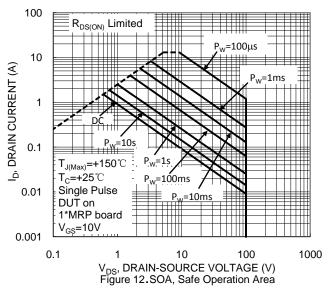








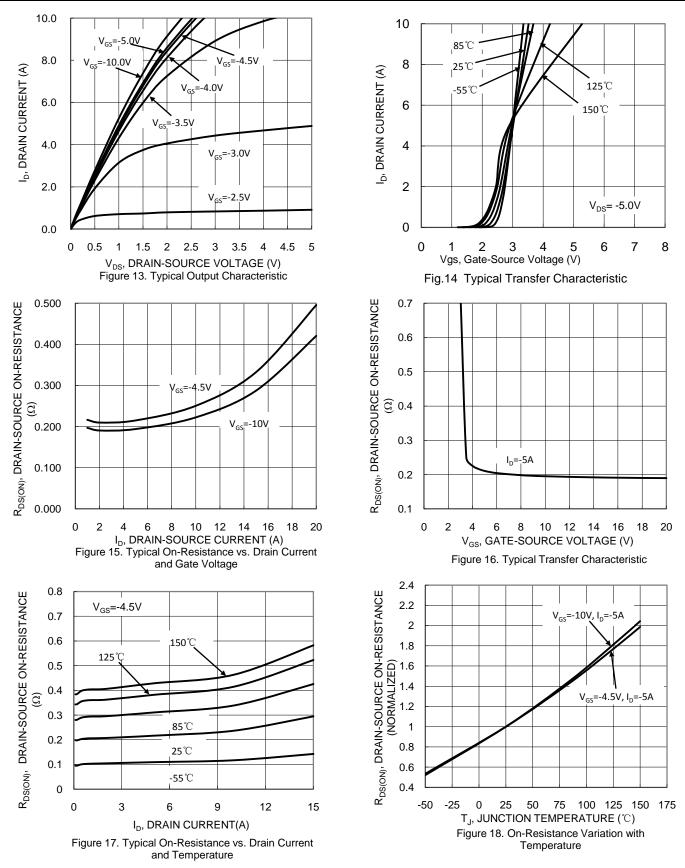




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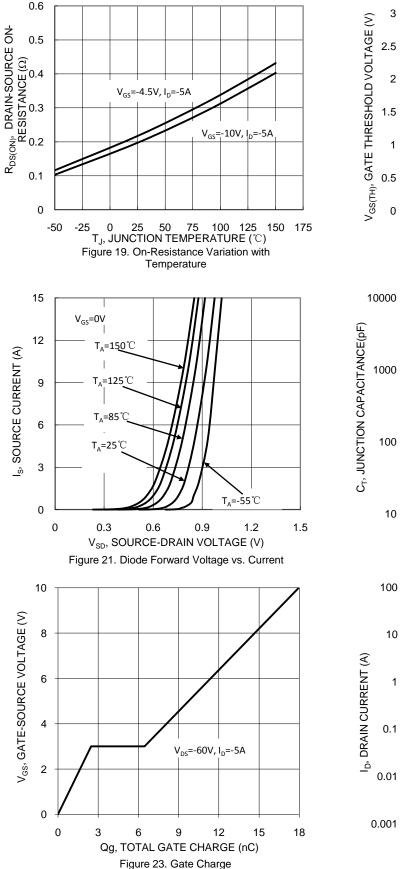


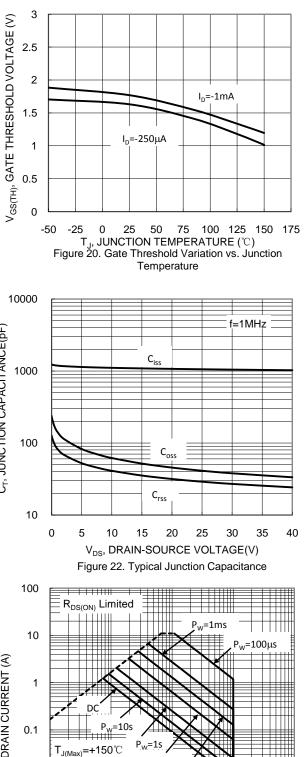
Typical Characteristics - P-CHANNEL



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P_w=100ms

P_w=10ms

100

10

V_{DS}, DRAIN-SOURCE VOLTAGE (V)

Figure 24. SOA, Safe Operation Area

T_c=+25℃

board V_{GS}=-10V

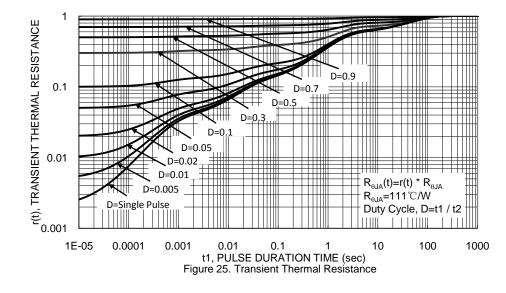
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Single Pulse DUT on 1*MRP

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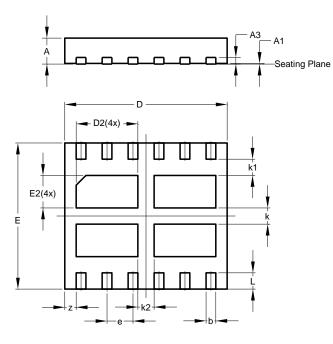




Package Outline Dimensions

Please see AP02001 at http://www.diodes.com/_files/datasheets/ap02001.pdf for the latest version.

V-DFN5045-12

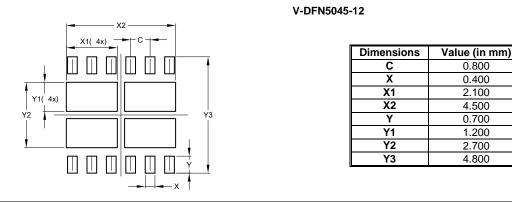


V-DFN5045-12						
Dim	Min	Max	Тур			
Α	0.75	0.85	0.80			
A1	0.00	0.05	0.02			
A3	-	-	0.203			
b	0.25	0.35	0.30			
D	4.95	5.05	5.00			
D2	1.80	2.00	1.90			
E	4.45	4.55	4.50			
E2	0.90	1.10	1.00			
e	-	-	0.80			
k	-	-	0.50			
k1	-	-	0.50			
k2	-	-	0.50			
L	0.45	0.55	0.50			
z	-	-	0.35			
All Dimensions in mm						



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/_files/datasheets/ap02001.pdf for the latest version.



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