



#### 700V N-CHANNEL ENHANCEMENT MODE MOSFET

## **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(on) max</sub>	I <sub>D</sub> T <sub>C</sub> = +25°C
700V	1.25Ω @ V <sub>GS</sub> = 10V	3.9A

### **Description**

This new generation MOSFET has been designed to minimize the onstate resistance (RDS(on)) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

### Applications

Switching

## **Features**

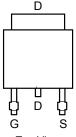
- 100% Unclamped Inductive Switch (UIS) test in production
- Low Gate Input Resistance
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

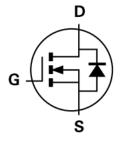
- Case: TO252 (DPAK)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.33 grams (approximate)











Internal Schematic

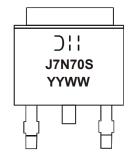
### Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
DMJ7N70SK3-13	Standard	TO252	2,500/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

# **Marking Information**



⊃¦¦=Manufacturer's Marking J7N70S = Product Type Marking Code YYWW = Date Code Marking YY = Last Digit of Year (ex: 13 = 2013) WW = Week Code (01 to 53)



# **Maximum Ratings** ( $@T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Units
Drain-Source Voltage	$V_{DSS}$	700	V
Gate-Source Voltage	$V_{GSS}$	±30	V
Continuous Drain Current (Note 5) V <sub>GS</sub> = 10V	I <sub>D</sub>	3.9 2.5	А
Maximum Body Diode Forward Current (Note 5)	Is	3.0	A
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I <sub>DM</sub>	15.6	A
Avalanche Current (Note 6)	I <sub>AR</sub>	1.5	А
Avalanche Energy (Note 6)	E <sub>AR</sub>	76	mJ
Peak Diode Recovery dv/dt	dv/dt	11.8	V/ns

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	T <sub>C</sub> = +25°C	Б	28	W
Total Power Dissipation (Note 3)	T <sub>C</sub> = +100°C	P <sub>D</sub>	11	
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	38	°C/W	
Thermal Resistance, Junction to Case (Note 5)	R <sub>0JC</sub>	2.1	C/VV	
Operating and Storage Temperature Range	T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C	

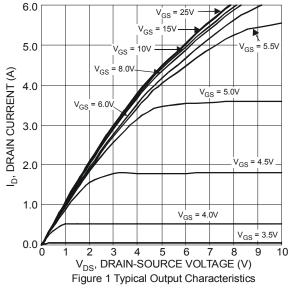
# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

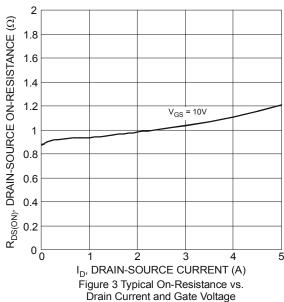
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	700	_	_	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μA	V <sub>DS</sub> = 700V, V <sub>GS</sub> = 0V	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	100	nA	V <sub>GS</sub> = ±30V, V <sub>DS</sub> = 0V	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(th)}$	2	2.9	4	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	1	1.25	Ω	$V_{GS} = 10V, I_D = 2.5A$	
Diode Forward Voltage	$V_{SD}$	_	0.9	1.3	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 5A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	351	_		V <sub>DS</sub> = 50V, f = 1MHz, V <sub>GS</sub> = 0V	
Output Capacitance	Coss	_	66	_	pF		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	1.1	_		VGS - 0V	
Gate Resistance	R <sub>G</sub>	_	3.5	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge	Qg	_	13.9	_		V <sub>DD</sub> = 560V, I <sub>D</sub> = 5A, V <sub>GS</sub> = 10V	
Gate-Source Charge	Q <sub>gs</sub>	_	1.9	_	nC		
Gate-Drain Charge	$Q_{gd}$	_	8.5	_			
Turn-On Delay Time	t <sub>D(on)</sub>	_	8.5	_		$V_{DD}$ = 350V, $V_{GS}$ = 10V, $R_{G}$ = 4.7 $\Omega$ , $I_{D}$ = 2.5A	
Turn-On Rise Time	t <sub>r</sub>	_	11.6	_			
Turn-Off Delay Time	t <sub>D(off)</sub>	_	24.5	_	ns		
Turn-Off Fall Time	t <sub>f</sub>	_	10	_			
Body Diode Reverse Recovery Time	t <sub>rr</sub>	_	212	_	ns		
Body Diode Reverse Recovery Time (T <sub>J</sub> = +150°C)	t <sub>rr</sub>		251		ns	1 - 50 41/44 - 4000///-	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>		1.8		μC	$I_S = 5A$ , dl/dt = 100A/ $\mu$ s	
Body Diode Reverse Recovery Charge (T <sub>J</sub> = +150°C)	Q <sub>rr</sub>	_	2.3		μC	1	

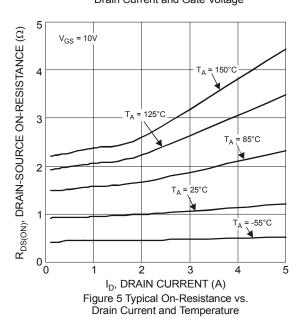
Notes:

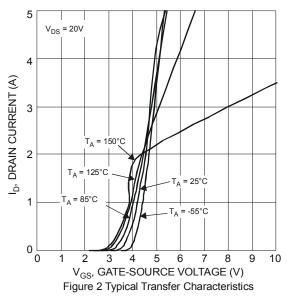
- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
- 6. UIS in production with  $V_{DD}$  = 50V,  $V_{GS}$  = 10V, L = 60mH,  $T_J$  = +25°C. 7. Short duration pulse test used to minimize self-heating effect 8. Guaranteed by design. Not subject to production testing

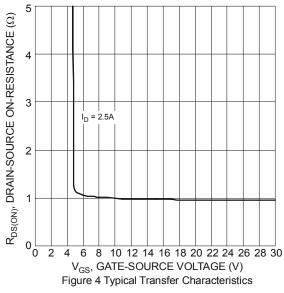


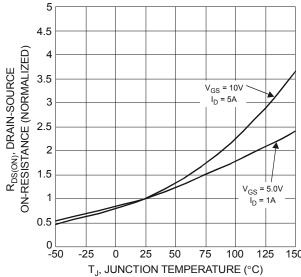




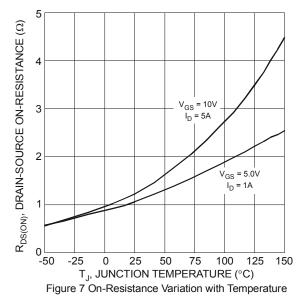


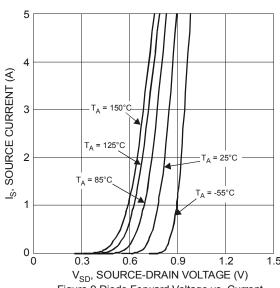


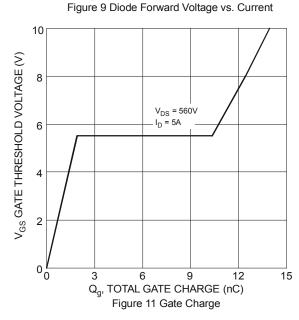












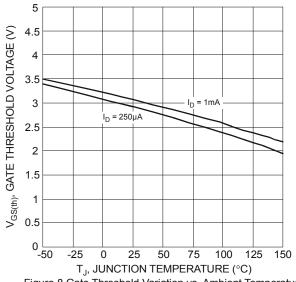
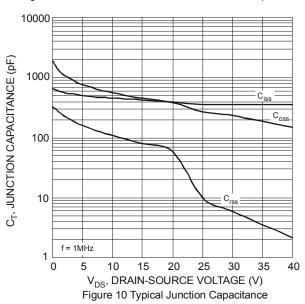
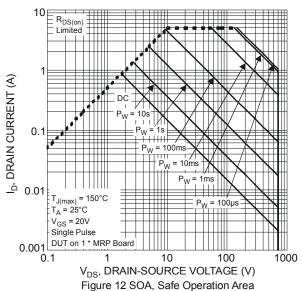
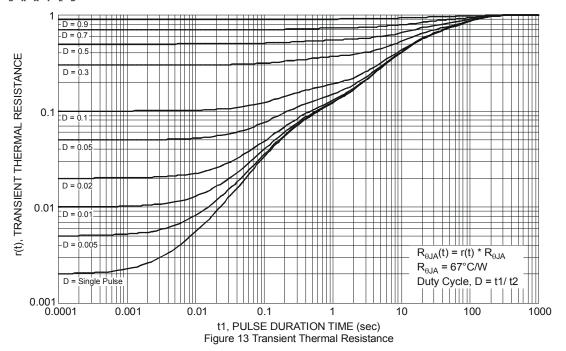


Figure 8 Gate Threshold Variation vs. Ambient Temperature



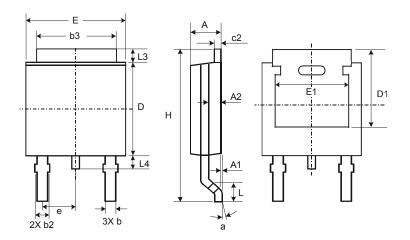






# **Package Outline Dimensions**

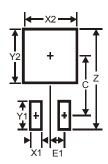
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



TO252					
Dim	Min	Max	Тур		
A	2.19	2.39	2.29		
<b>A</b> 1	0.00	0.13	0.08		
<b>A2</b>	0.97	1.17	1.07		
b	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.46	5.33		
c2	0.45	0.58	0.531		
ם	6.00	6.20	6.10		
D1	5.21	_	_		
е	_	_	2.286		
Е	6.45	6.70	6.58		
E1	4.32	_	_		
Н	9.40	10.41	9.91		
L	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°	_		
All Dimensions in mm					

## **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
Z	11.6		
X1	1.5		
X2	7.0		
Y1	2.5		
Y2	7.0		
С	6.9		
E1	2.3		



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