



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	Rds(on)	Ι _D T _A = +25°C
12V	$20m\Omega @ V_{GS} = 4.5V$	6.6A
120	$23m\Omega @ V_{GS} = 2.5V$	6.1A

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

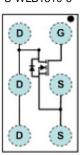
- Battery Management
- Load Switch
- Battery Protection

Features and Benefits

- Low Q_g & Q_{gd}
- Small Footprint
- Low Profile 0.62mm Height
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: U-WLB1510-6
- Terminal Connections: See Diagram Below
- Weight: 0.0018 grams (Approximate)



Top View

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN1016UCB6-7	U-WLB1510-6	3,000/Tape & Reel

1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

U-WLB1510-6



PW = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Y = 2011) M = Month (ex: 9 = September)

Date	Code	Key

Notes:

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Year	201	1	2012		2013	20	14	2015		2016	2	2017
Code	Y		Z		А	E	3	С		D		E
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D

U-WLB1510-6



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units	
Drain-Source Voltage			V _{DSS}	12	V
Gate-Source Voltage	V _{GSS}	±8	V		
Continuous Drain Current (Note 5) V_{GS} =4.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	5.5 4.2	А
Continuous Drain Current (Note 6) V_{GS} =4.5V	Steady State	T _A = +25°C T _A = +70°C	ID	6.6 5.3	А
Pulsed Drain Current (Note 7)		I _{DM}	30	А	

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	PD	0.92	W
Total Power Dissipation (Note 6)	PD	1.47	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	136	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	R _{0JA}	94	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

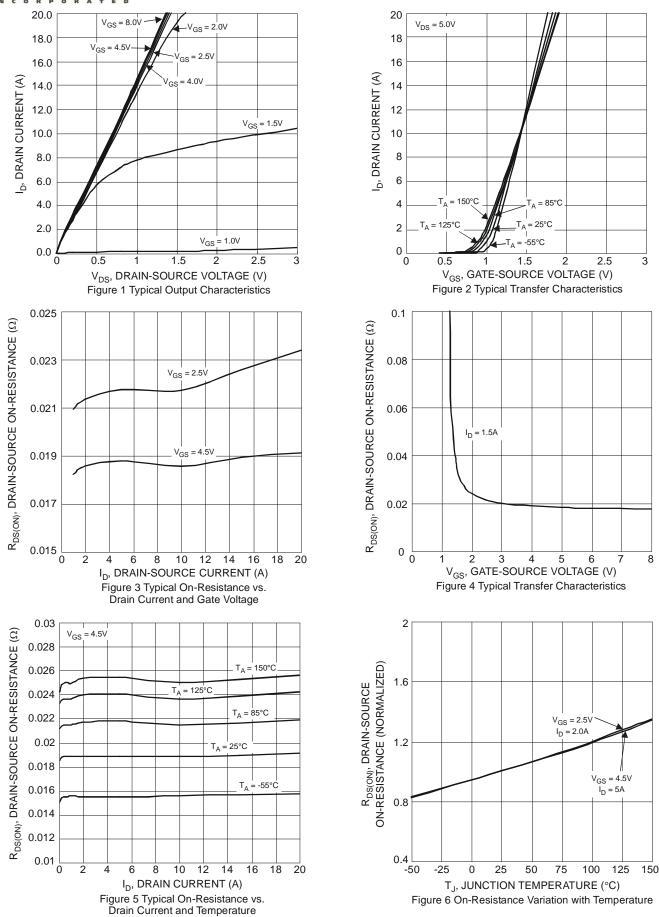
Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)		-,		- 71-		-	
Drain-Source Breakdown Voltage		BV _{DSS}	12	_	—	V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current (@	$T_{\rm C} = +25^{\circ}{\rm C})$	I _{DSS}	_	_	1.0	μA	V _{DS} = 9.6V, V _{GS} = 0V
Gate-Source Leakage		I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage		V _{GS(th)}	0.4	0.6	1.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance		Р	_	16	20	mΩ	$V_{GS} = 4.5V, I_D = 1.5A$
		R _{DS(ON)}	_	20	23	11122	V _{GS} = 2.5V, I _D = 1.5A
Forward Transfer Admittance		Y _{fs}	_	14	—	S	V _{DS} = 6V, I _D = 1.5A
Diode Forward Voltage (Note 6)		V _{SD}	_	0.7	1.0	V	$V_{GS} = 0V, I_{S} = 1.5A$
Reverse Recovery Charge		Qrr	—	8	—	nC	$V_{dd} = 6V, I_F = 1.5A,$
Reverse Recovery Time		t _{rr}	_	43.6	—	ns	di/dt =200A/µs
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Input Capacitance		_	423	_	pF	
Output Capacitance		Coss	_	238	—	pF	V _{DS} = 6V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance		C _{rss}	_	41	_	pF	
Series Gate Resistance		R_{G}	—	3	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge (4.5V)		Qg	—	4.2	—	nC	
Gate-Source Charge		Q _{gs}	_	0.6	—	nC	V _{GS} = 4.5V, V _{DS} = 6V, I _D =1.5A
Gate-Drain Charge		Q _{gd}	_	0.4	—	nC	10 = 1.3A
Turn-On Delay Time		t _{D(on)}	_	5	_	ns	
Turn-On Rise Time		tr		10	_	ns	$V_{DS} = 6V, V_{GS} = 4.5V,$
Turn-Off Delay Time		t _{D(off)}		25	—	ns	$R_G = 4\Omega$, $I_D = 1.5A$
Turn-Off Fall Time			_	10		ns]

5. Device mounted on FR-4 PCB with minimum recommended pad layout.
6. Device mounted on FR4 material with 1-inch² (6.45-cm²), 2-oz (0.071-mm thick) Cu.
7. 300ms pulse, pulse duty cycle<=2%.
8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to production testing.

Notes:

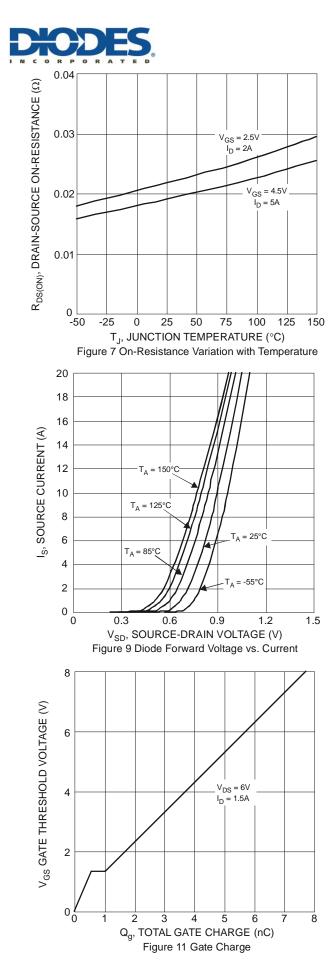


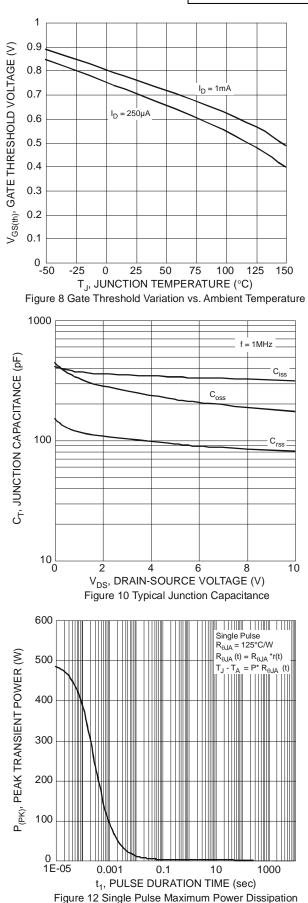
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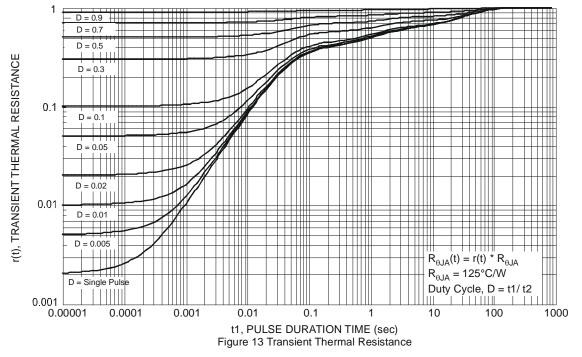
DMN1016UCB6 Document number: DS37124 Rev. 4 - 2

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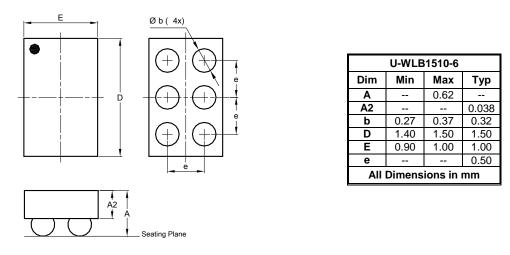






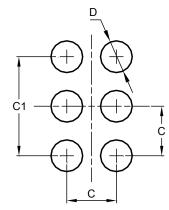
Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensione	Value
Dimensions	(in mm)
С	0.50
C1	1.00
D	0.25



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