



#### N-CHANNEL ENHANCEMENT MODE MOSFET

## Product Summary (Typ. @ V<sub>GS</sub> = 4.5V, T<sub>A</sub> = +25°C)

V <sub>DSS</sub>	R <sub>DS(ON)</sub>	Qg	$Q_{gd}$	Ι <sub>D</sub>
8V	$35m\Omega$	9.6nC	0.9nC	4.0A

### **Description**

The DMN1054UCB4 is a Trench MOSFET, engineered to minimize on-state losses and switch ultra-fast, making it ideal for high-efficiency power transfer. Using Chip-Scale Package (CSP) to increase power density by combining low thermal impedance with minimal R<sub>DS(ON)</sub> per footprint area.

## **Applications**

- DC-DC Converters
- **Battery Management**
- Load Switch

## **Features**

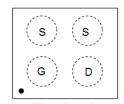
- Trench-CSP Technology with the Lowest on Resistance:
  - $R_{DS(ON)} = 35m\Omega$  to Minimize On-State Losses
  - Q<sub>q</sub> = 9.6nC for Ultra-Fast Switching
- V<sub>GS(TH)</sub> = 0.6V Typ. for a Low Turn-On Potential
- CSP with Footprint 0.8mm × 0.8mm
- Height = 0.375mm for Low Profile
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

#### **Mechanical Data**

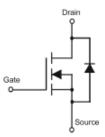
Case: X1-WLB0808-4

Terminal Connections: See Diagram Below

#### X1-WLB0808-4



Top-View Pin Configuration



**Equivalent Circuit** 

#### **Ordering Information** (Note 4)

Part Number	Case	Packaging
DMN1054UCB4-7	X1-WLB0808-4	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + CI) and <a href="https://www.diodes.com/products/packages.html">https://www.diodes.com/products/packages.html</a>.
   4. For packaging details, go to our website at http://www.diodes.com/products/packages.html

# **Marking Information**



YW = Product Type Marking Code YM = Date Code Marking Y or  $\overline{Y}$  = Year (ex: D = 2016) M or  $\overline{M}$  = Month (ex: 9 = September)

Date Code Key

Year	201	2	2013		2014	20	15	2016		2017	2	2018
Code	Z		Α		В	(	2	D		Е		F
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

April 2016



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	$V_{DSS}$	8	V
Gate-Source Voltage	$V_{GSS}$	±5	V
Continuous Source Current @ V <sub>GS</sub> = 4.5V (Note 5)	I <sub>D</sub>	2.7 2.2	А
Continuous Source Current @ V <sub>GS</sub> = 4.5V (Note 6)	I <sub>D</sub>	4.0 3.2	А
Pulsed Drain Current (Pulse duration 10µs, duty cycle ≤1%)	I <sub>DM</sub>	8	A
Continuous Source-Drain Diode Current	I <sub>S</sub>	0.74	Α
Pulse Diode Forward Current	I <sub>SM</sub>	15	A

### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P <sub>D</sub>	0.74	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	169	°C/W
Total Power Dissipation (Note 6)	P <sub>D</sub>	1.34	W
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>θJA</sub>	93	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

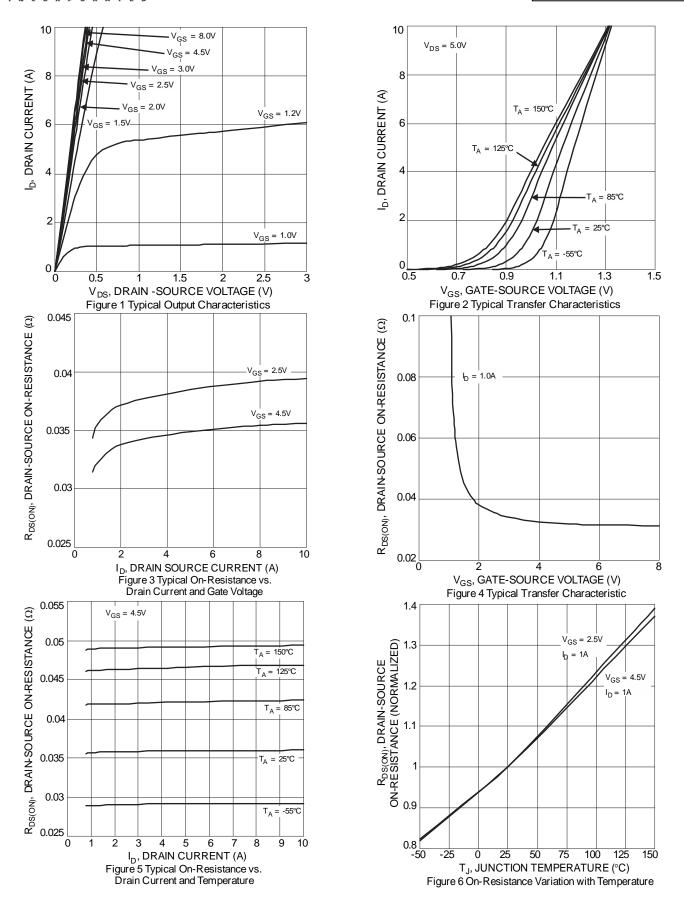
# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
DFF CHARACTERISTICS (Note 7)								
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	8	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	-	ı	1.0	μA	$V_{DS} = 8V$ , $V_{GS} = 0V$		
Gate-Body Leakage	I <sub>GSS</sub>	-	-	±100	nA	$V_{GS} = \pm 5V$ , $V_{DS} = 0V$		
ON CHARACTERISTICS (Note 7)								
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.35	-	0.7	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		
			35	42		$V_{GS} = 4.5V, I_D = 1.0A$		
			38.5	50		$V_{GS} = 2.5V, I_D = 1.0A$		
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	-	46.4	65	mΩ	$V_{GS} = 1.8V, I_D = 0.5A$		
			53.3	80		$V_{GS} = 1.5V, I_D = 0.2A$		
			64.7	110		$V_{GS} = 1.2V, I_D = 0.1A$		
Forward Transfer Admittance	Y <sub>fs</sub>	-	6.0	-	S	$V_{DS} = 6V, I_{S} = 1.0A$		
Body Diode Forward Voltage	$V_{SD}$	•	0.7	1	V	$V_{GS} = 0V, I_{S} = 1.0A$		
DYNAMIC CHARACTERISTICS (Note 8)								
Input Capacitance	C <sub>iss</sub>	•	698	908	pF			
Output Capacitance	Coss	-	97	127	pF	$V_{DS} = 6V$ , $V_{GS} = 0V$ , $V_{DS} = 6V$ , $V_{GS} = 0V$ ,		
Reverse Transfer Capacitance	C <sub>rss</sub>	-	90	126	pF	1 = 1.000112		
Gate Resistance	$R_{g}$	-	1.3	2.6	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$		
Total Gate Charge	$Q_g$	-	9.6	15	nC			
Gate-Source Charge	$Q_{gs}$	ı	0.9	-	nC	$V_{GS} = 4.5V, V_{DS} = 6V,$ $I_{D} = 1.0A$		
Gate-Drain Charge	$Q_{gd}$	-	0.9	-	nC	ID = 1.0A		
Turn-On Delay Time	t <sub>D(ON)</sub>	-	5.2	10	ns			
Turn-On Rise Time	t <sub>R</sub>	-	6.7	14	ns	$V_{DD} = 6V, I_D = 1.0A$		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	-	16.6	32	ns	$V_{GEN} = 4.5V$ , $R_G = 1\Omega$ , $R_L = 6\Omega$		
Turn-Off Fall Time	t <sub>F</sub>	=	2	4	ns			
Reverse Recovery Charge	Q <sub>RR</sub>		0.7	1.5	nC	I_ 10 di/dt 1000/ug		
Body Diode Reverse Recovery Time	t <sub>RR</sub>	-	6.9	14	ns	$I_F = 1A$ , di/dt = 100A/ $\mu$ s		

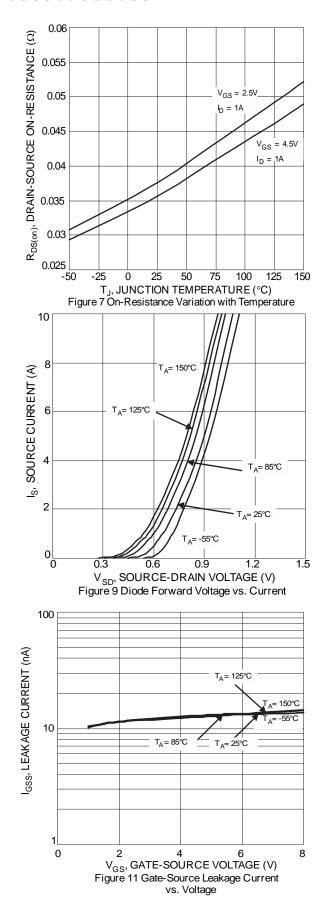
 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate.
 Short duration pulse test used to minimize self-heating effect. Notes:

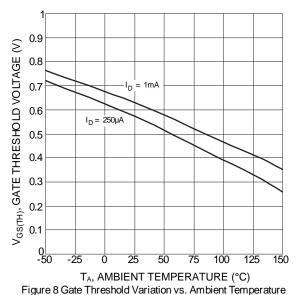
<sup>8.</sup> Guaranteed by design. Not subject to production testing.

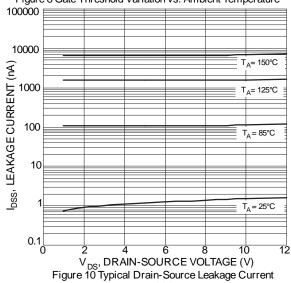


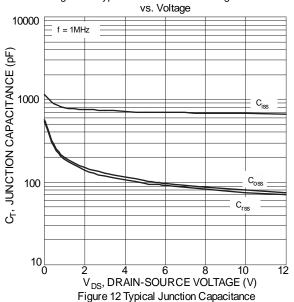


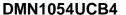




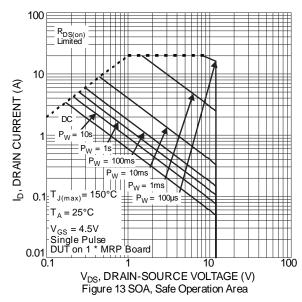


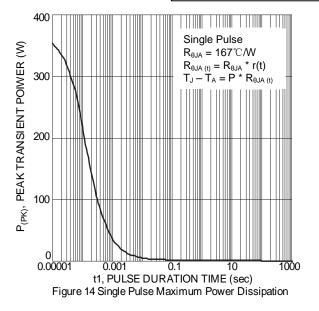


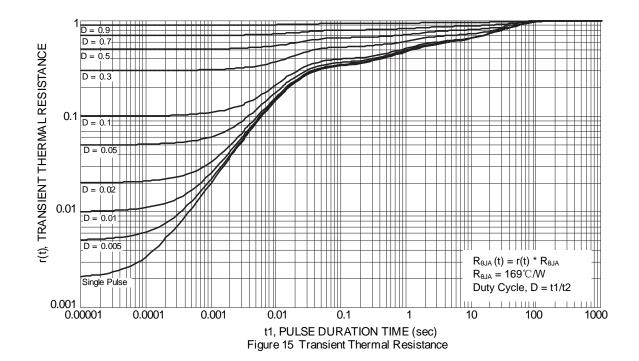










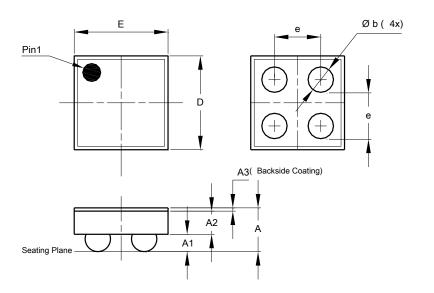




## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### X1-WLB0808-4

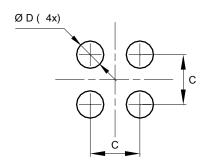


X1-WLB0808-4						
Dim	Min	Max	Тур			
Α	0.3320	0.4180	0.3750			
<b>A</b> 1	0.1350	0.1650	0.1500			
A2	0.1750	0.2250	0.2000			
А3	0.0220	0.0280	0.0250			
b	0.1971	0.2409	0.2190			
D	0.7900	0.8100	0.8000			
E	0.7900	0.8100	0.8000			
е	e 0.400 BSC					
All Dimensions in mm						

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### X1-WLB0808-4



Dimensions	Value (in mm)		
С	0.4000		
D	0.2190		



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