



100V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on) max}	Ι _D T _C = +25°C
100V	140mΩ @ V _{GS} = 10V	12A
1000	160mΩ @ V _{GS} = 4.5V	11A

Description

This new generation MOSFET has been designed to minimize the onstate resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

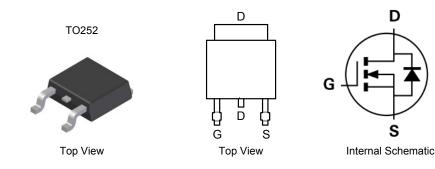
- DC-DC Converters
- Power management functions
- Analog Switch

Features

- Low On-Resistance
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Available

Mechanical Data

- Case: TO252 (DPAK)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.33 grams (approximate)



Ordering Information (Note 4 & 5)

Part Number Compliance	Case	Packaging
DMN10H170SK3Q-13 Automotive	TO252	2,500/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

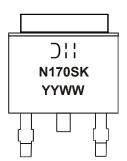
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_grade_definitions/.

5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

Notes:



) | | =Manufacturer's Marking
N170SK = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Digit of Year (ex: 14 = 2014)
WW = Week Code (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V _{DSS}	100	V		
Gate-Source Voltage	V _{GSS}	±20	V		
Continuous Drain Current (Note 6) V_{GS} = 10V	ID	12 7.5	A		
Maximum Body Diode Forward Current (Note 6)	Is	4	А		
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I _{DM}	16	A		
Avalanche Current (Note 7)	I _{AR}	5.3	A		
Avalanche Energy (Note 7)			E _{AR}	20	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Tatal Dawar Dissingtion (Nata 6)	T _C = +25°C	D	42	W
Total Power Dissipation (Note 6)	T _C = +100°C	PD	17	
Thermal Resistance, Junction to Ambient (Note 6)	R _{0JA}	44	°C/W	
Thermal Resistance, Junction to Case (Note 6)	R _{0JC}	3	°C/W	
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)	Cymbol		тур	Max	Onic	Test condition	
Drain-Source Breakdown Voltage	BV _{DSS}	100			V	V _{GS} = 0V, I _D = 250µA	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μA	$V_{DS} = 100V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(th)}	1.0	_	3.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance	_		99	140		V _{GS} = 10V, I _D = 5A	
Static Drain-Source On-Resistance	R _{DS (ON)}	_	104	160	mΩ	V _{GS} = 4.5V, I _D = 5A	
Diode Forward Voltage	V _{SD}	_	0.7	1.0	V	V _{GS} = 0V, I _S = 10A	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss		1167	_		V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz	
Output Capacitance	Coss	_	36	_	pF		
Reverse Transfer Capacitance	C _{rss}		25	_			
Gate Resistance	R _G		1.3	_	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	4.9	_		V _{DS} = 80V, I _D = 12.8A	
Total Gate Charge (V _{GS} = 10V)	Qg	_	9.7		nC		
Gate-Source Charge	Q _{gs}	_	2.0		nc		
Gate-Drain Charge	Q _{gd}	_	2.0	_			
Turn-On Delay Time	t _{D(on)}	_	10.5			V _{DD} = 50V, R _G = 25Ω, I _D = 12.8A	
Turn-On Rise Time	tr	_	11.1				
Turn-Off Delay Time	t _{D(off)}		42.6		ns		
Turn-Off Fall Time	tf		12.8				
Body Diode Reverse Recovery Time	t _{rr}		30.3		ns	V _{GS} = 0V, I _S = 12.8A, dl/dt = 100A/µs	
Body Diode Reverse Recovery Charge	Qrr		35.2		nC	V _{GS} = 0V, I _S = 12.8A, dl/dt = 100A/µs	

Notes: 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.

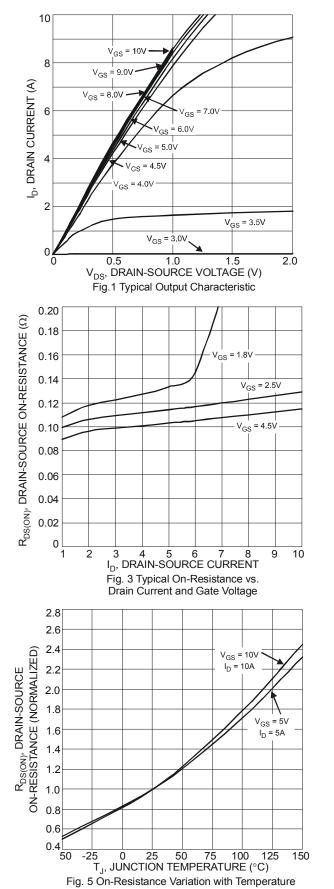
7. UIS in production with L = 1.43mH, TJ = $+25^{\circ}$ C.

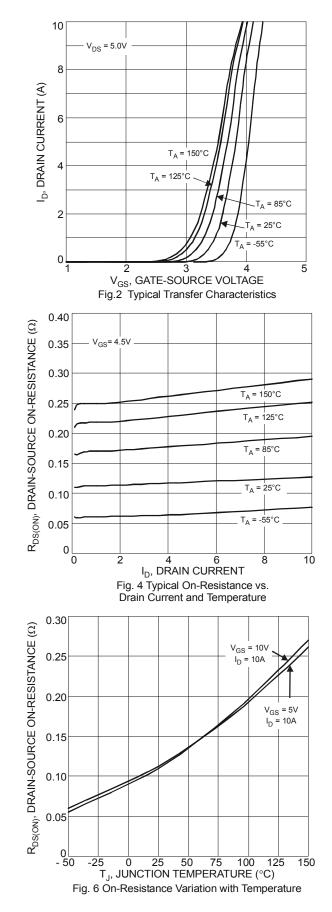
8. Short duration pulse test used to minimize self-heating effect

9. Guaranteed by design; not subject to production testing

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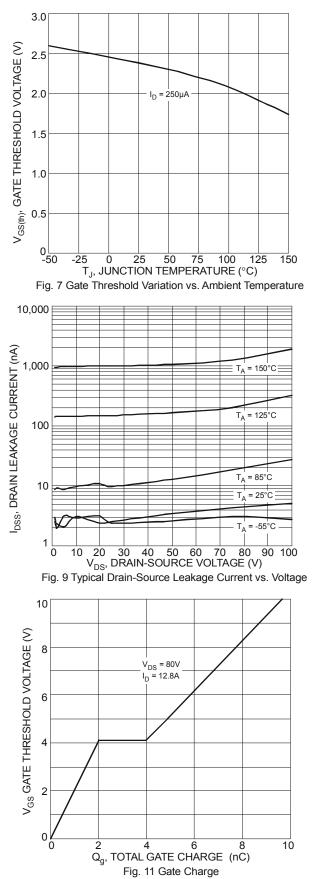


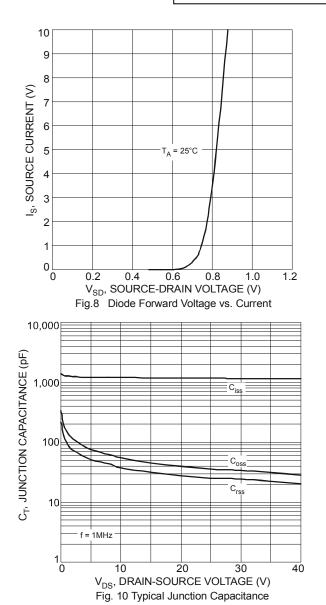




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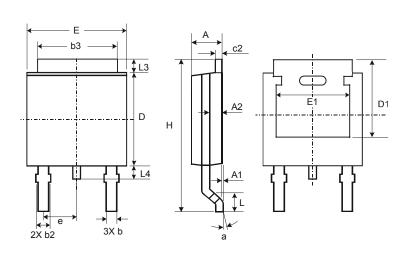


DMN10H170SK3Q Document number: DS37058 Rev. 1 - 2



Package Outline Dimensions

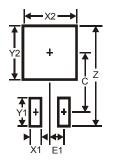
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



TO252						
Dim	Min	Max	Тур			
Α	2.19	2.39	2.29			
A1	0.00	0.13	0.08			
A2	0.97	1.17	1.07			
b	0.64	0.88	0.783			
b2	0.76	1.14	0.95			
b3	5.21	5.46	5.33			
c2	0.45	0.58	0.531			
D	6.00	6.20	6.10			
D1	5.21	_	-			
е	-	-	2.286			
Е	6.45	6.70	6.58			
E1	4.32 – –		-			
Н	9.40	10.41	9.91			
L	1.40	1.78	1.59			
L3	0.88	1.27	1.08			
L4	0.64	1.02	0.83			
а	0°	10°	-			
All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	11.6
X1	1.5
X2	7.0
Y1	2.5
Y2	7.0
С	6.9
E1	2.3



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