

20V N-CHANNEL ENHANCEMENT MODE MOSFET

#### Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- Ultra-Low Package Profile, 0.4mm Maximum Package Height
- ESD Protected up to 1.5kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 standards for High Reliability

#### **Mechanical Data**

- Case: X2-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe; Solderable per MIL-STD-202, Method 208 •
- Weight: 0.001 grams (Approximate)

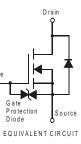




BOTTOM VIEW







#### Ordering Information (Note 4)

Part Number	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DMN2400UFB4-7	NC	7	8	3,000
DMN2400UFB4-7B	NC	7	8	10,000

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.



## Marking Information

DMN2400UFB4-7	From date code 1527 (YYWW), this changes to: Top View Dot Denotes Drain Side Top View Bar Denotes Gate and Source Side
DMN2400UFB4-7B	$\mathbf{F}_{Top View}$ $\mathbf{F}_{Dar Denotes Gate and Source Side}$ $\mathbf{NC} = Part Marking Code$ $\mathbf{F}_{Part} = \mathbf{F}_{Part} \mathbf{F}_{Part$

# Maximum Ratings @T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage			V <sub>DSS</sub>	20	V
Gate-Source Voltage			V <sub>GSS</sub>	±12	V
Continuous Drain Current (Note 5) $V_{GS}$ = 4.5V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +85°C	ID	0.75 0.55	А
Pulsed Drain Current (Notes 5 & 6)			I <sub>DM</sub>	3	A

#### **Thermal Characteristics** @T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	PD	0.47	mW
Thermal Resistance, Junction to Ambient	R <sub>0JA</sub>	258	°C/W
Operating and Storage Temperature Range	T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

Notes: 5. Device mounted on FR-4 PCB, with minimum recommended pad layout, single sided.

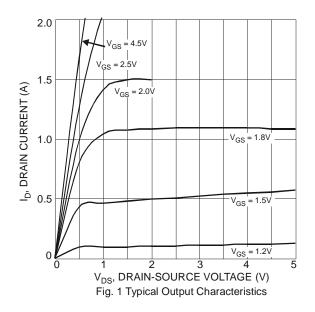
6. Device mounted on minimum recommended pad layout test board, 10µs pulse duty cycle = 1%.

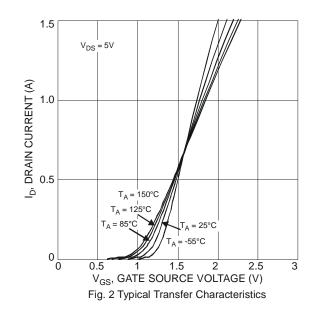


## Electrical Characteristics @T<sub>A</sub> = +25°C, unless otherwise specified.)

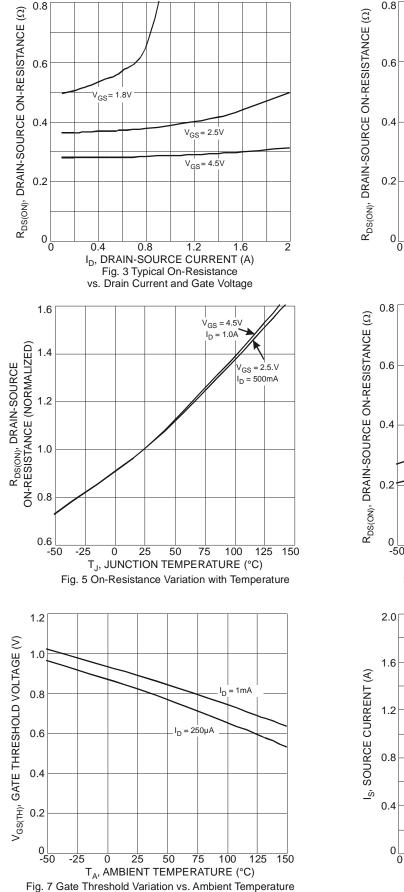
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	-	_	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zara Cata Valtaga Drain Current T (25%)		_	_	100	nA	$V_{DS} = 20V, V_{GS} = 0V$	
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	IDSS			50		$V_{DS} = 5V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	—	±100	nA	$V_{GS} = \pm 3V, V_{DS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	—	±1.0	μA	$V_{GS} = \pm 4.5 V, V_{DS} = 0 V$	
Gate-Source Leakage	IGSS	—	—	±50	μA	$V_{GS} = \pm 10V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.5	—	0.9	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
		_	_	0.55		$V_{GS} = 4.5V, I_D = 600mA$	
Static Drain-Source On-Resistance	RDS (ON)	_	_	0.75	Ω	$V_{GS} = 2.5V, I_D = 500mA$	
	. ,		_	0.9		V <sub>GS</sub> = 1.8V, I <sub>D</sub> = 350mA	
Forward Transfer Admittance	Y <sub>fs</sub>	_	1.0	—	S	$V_{DS} = 10V, I_D = 400mA$	
Diode Forward Voltage (Note 7)	V <sub>SD</sub>		0.7	1.2	V	$V_{GS} = 0V, I_{S} = 150mA$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	36.0	_	pF		
Output Capacitance	C <sub>oss</sub>	_	5.7	—	pF	−V <sub>DS</sub> =16V, V <sub>GS</sub> = 0V, −f = 1.0MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>		4.2	_	pF		
Total Gate Charge	Qa		0.5	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$	
Gate-Source Charge	Q <sub>as</sub>		0.07		nC	ID = 250 mA	
Gate-Drain Charge	Q <sub>gd</sub>		0.1	—	nC		
Turn-On Delay Time	t <sub>D(on)</sub>	_	4.11	_	ns		
Turn-On Rise Time	tr	_	3.82		ns	$V_{DD} = 10V, V_{GS} = 4.5V,$	
Turn-Off Delay Time	t <sub>D(off)</sub>	_	14.8	_	ns	$R_L = 47\Omega, R_G = 10\Omega,$	
Turn-Off Fall Time	tf	_	9.6	_	ns	$I_D = 200 \text{mA}$	

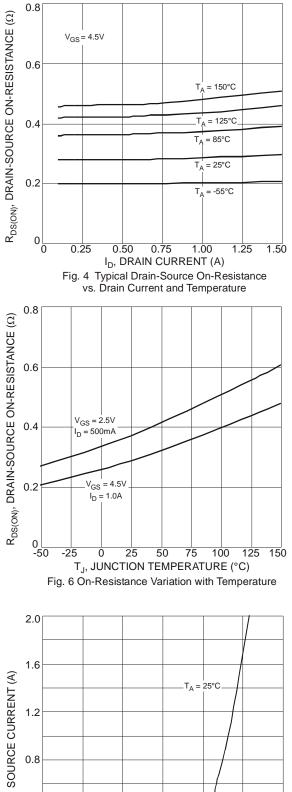
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing. Notes:











0.2

0.4

0.6

V<sub>SD</sub>, SOURCE-DRAIN VOLTAGE (V)

Fig. 8 Diode Forward Voltage vs. Current

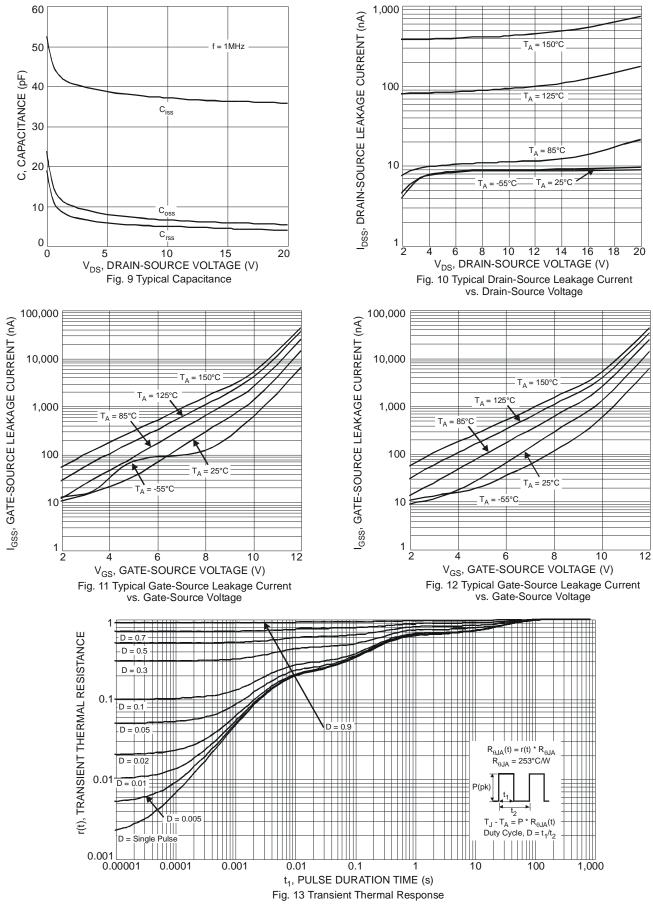
0.8

1.0

1.2



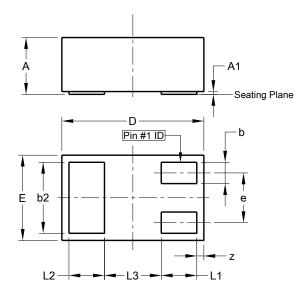
#### DMN2400UFB4





#### **Package Outline Dimensions**

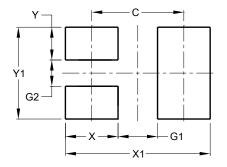
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



X2-DFN1006-3					
Dim	Min	Max	Тур		
Α		0.40			
A1	0.00	0.05	0.03		
b	0.10	0.20	0.15		
b2	0.45	0.55	0.50		
D	0.95	1.05	1.00		
Е	0.55	0.65	0.60		
е	-	1	0.35		
L1	0.20	0.30	0.25		
L2	0.20	0.30	0.25		
L3	-	-	0.40		
z	0.02	0.08	0.05		
All Dimensions in mm					

### Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.70
G1	0.30
G2	0.20
Х	0.40
X1	1.10
Y	0.25
Y1	0.70



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