



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max
201/	26.5mΩ @ V _{GS} = 10V	5.8A
30V	$32m\Omega$ @ $V_{GS} = 4.5V$	5.0A

Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(on)}) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Battery Charging
- Power Management Functions
- DC-DC Converters
- Portable Power Adaptors

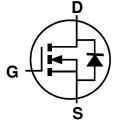
Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)

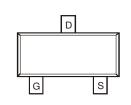
SOT23



Top View



Internal Schematic



Top View

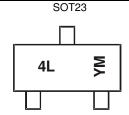
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3042L-7	SOT23	3,000/Tape & Reel
DMN3042L-13	SOT23	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html.

Marking Information



4L = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: B = 2014) M = Month (ex: 9 = September)

Date Code Key

Year	200	9	2010		2011	20)12	2013		2014	2	2015
Code	W		X		Υ		Z	Α		В		С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25 ℃ unless otherwise specified.)

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V_{DSS}	30	V
Gate-Source Voltage	V_{GSS}	±12	V
Continuous Drain Current (Note 6) V _{GS} = 10V	l _D	5.8 4.0	А
Maximum Body Diode Forward Current (Note 6)	I _S	1.5	Α
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I _{DM}	30	Α

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Power Dissipation (Note 5)		P_{D}	0.72	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	171	°C/W
Power Dissipation (Note 6)		P_{D}	1.4	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	93	°C/W
Operating and Storage Temperature Range		T_{J}, T_{STG}	-55 to +150	∞

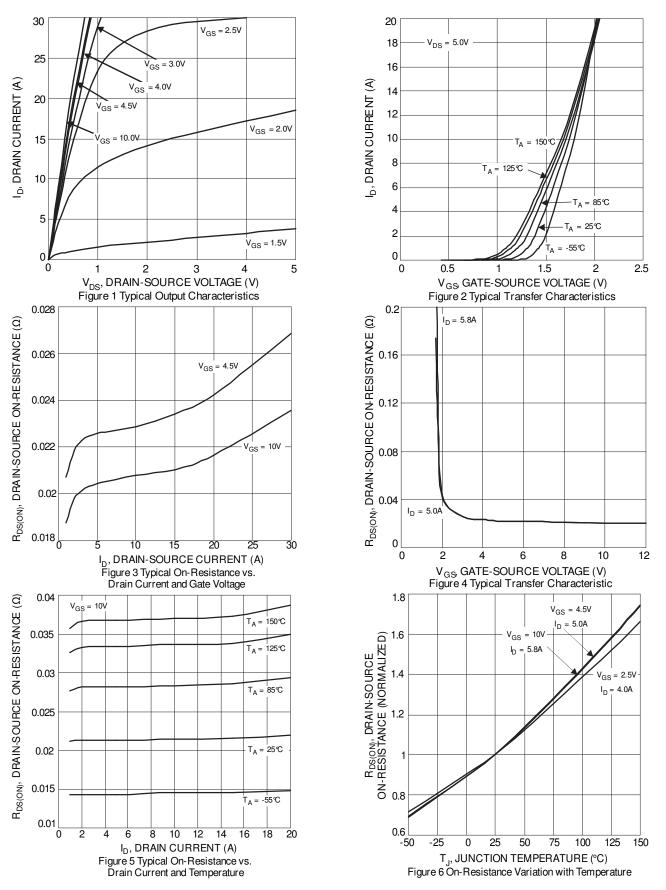
Electrical Characteristics (@T_A = +25 °C unless otherwise specified.)

T-			· · ·			-
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage	IGSS		_	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(th)}$	0.6	_	1.4	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
			21	26.5		$V_{GS} = 10V, I_D = 5.8A$
Static Drain-Source On-Resistance	R _{DS (ON)}		23	32	mΩ	$V_{GS} = 4.5V, I_D = 5.0A$
			29	48		$V_{GS} = 2.5V, I_D = 4.0A$
Diode Forward Voltage	V_{SD}		0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	1	570	860		V 45V V 6V
Output Capacitance	Coss		63	95	pF	$V_{DS} = 15V$, $V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}		53	80		
Gate Resistance	R_{G}	1	3.2	4.5	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge (V _{GS} = 10V)	Q_g		13.3	20		
Total Gate Charge (V _{GS} = 4.5V)	Q_g		6.1	8	nC	V _{DS} = 15V. I _D = 6.9A
Gate-Source Charge	Q_{gs}		1.0	1.5	110	VDS = 15V, ID = 6.9A
Gate-Drain Charge	Q_{gd}	_	1.6	2.5		
Turn-On Delay Time	$t_{D(on)}$		1.5	2.4		
Turn-On Rise Time	t _r		3.3	5	nS	$V_{GS} = 10V, V_{DD} = 15V, R_G = 3\Omega,$
Turn-Off Delay Time	t _{D(off)}	1	13.9	22	113	$I_D = 6.9A$
Turn-Off Fall Time	t _f		4.9	7		
Body Diode Reverse Recovery Time	t _{rr}		7.8	12	nS	$I_S = 5A$, $dI/dt = 100A/\mu s$
Body Diode Reverse Recovery Charge	Q _{rr}	_	1.9	3	nC	$I_S = 5A$, $dI/dt = 100A/\mu s$

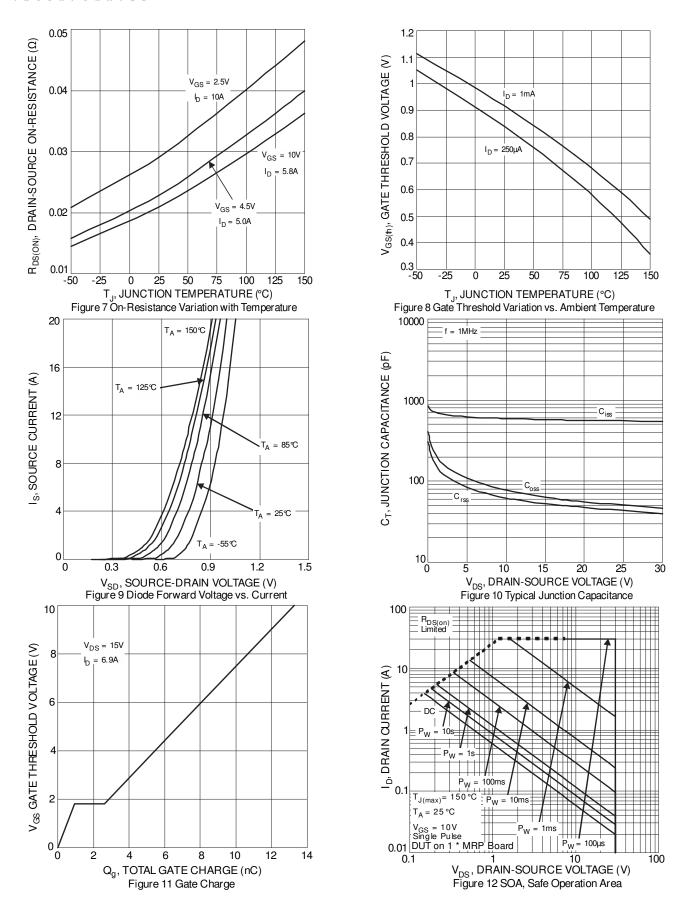
Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.

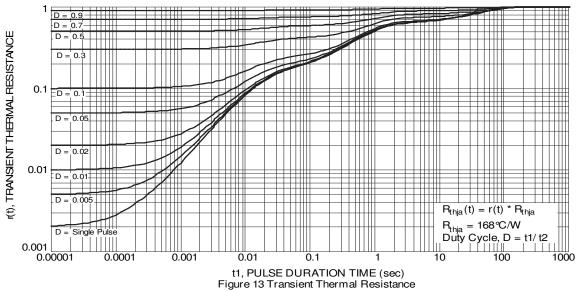






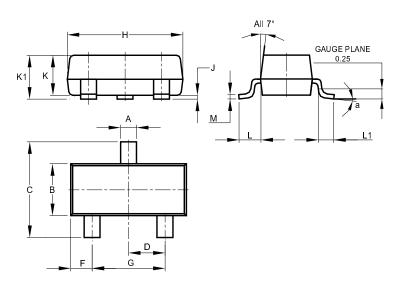






Package Outline Dimensions

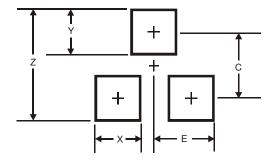
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
K	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
М	0.085	0.150	0.110				
а	a 8°						
All Dimensions in mm							

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Υ	0.9
С	2.0
E	1.35



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