



#### **DUAL N-CHANNEL ENHANCEMENT MODE MOSFET**

### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> (MAX)	Package	I <sub>D (MAX)</sub> T <sub>A</sub> = +25°C	
30V	190mΩ @ V <sub>GS</sub> = 10V	SOT363	1A	
307	$335$ m $Ω$ @ $V_{GS}$ = $4.5$ V	301303	0.75A	

#### Description

This MOSFET has been designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

## **Applications**

- Motor Control
- Power Management Functions
- Load Switch

### **Features and Benefits**

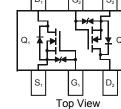
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

### **Mechanical Data**

- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound.
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See diagram
- Terminals: Finish Matte Tin annealed over Alloy42 leadframe.
  Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.006 grams (approximate)







Internal Schematic

Top View

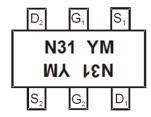
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3190LDW-7	SOT363	3000K/Tape & Reel
DMN3190LDW-13	SOT363	10000K/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

# **Marking Information**



N31 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Y = 2011) M = Month (ex: 9 = September)

Date Code Key

	Year	201	1	2012		2013	20	14	2015		2016		2017
	Code	Υ		Z		Α		В	С		D		E
ı	Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
	Code	1	2	3	4	5	6	7	8	9	0	N	D



# **Maximum Ratings** (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic		Symbol	Value	Units		
Drain-Source Voltage		$V_{DSS}$	30	V		
Gate-Source Voltage	_	V <sub>GSS</sub>	±20	V		
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	l <sub>D</sub>	1000 900	mA	
Continuous Diain Current (Note 6) VGS – 10V	T < 5s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	l <sub>D</sub>	1300 1000	mA	
Maximum Continuous Body Diode Forward Current	(Note 5)	Is	0.5	Α		
Pulsed Drain Current (10µs pulse, duty cycle=1%)		I <sub>DM</sub>	2.0	А		

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	P <sub>D</sub>	0.32	W
Total Fower Dissipation (Note 3)	T <sub>A</sub> = +70°C	FD	0.19	VV
Thermal Desistance, Junction to Ambient (Note 5)		D	395	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	T < 5s	$R_{\theta JA}$	320	C/VV
Total Power Dissipation (Note 6)	T <sub>A</sub> = +25°C	P <sub>D</sub>	0.4	W
Total Fower Dissipation (Note o)	T <sub>A</sub> = +70°C	FD	0.25	VV
Thermal Resistance, Junction to Ambient (Note 6)		<u> </u>	320	
Thermal Resistance, Junction to Ambient (Note 0)	T < 5s	$R_{\theta JA}$	250	°C/W
Thermal Resistance, Junction to Case		$R_{ heta JC}$	143	
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

# Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

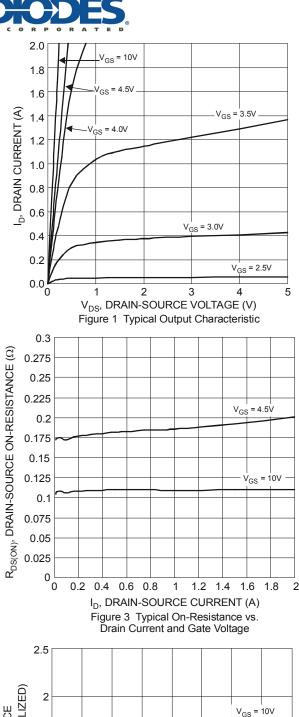
Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)							•
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	30	_	_	V	$V_{GS} = 0V$ , $I_D = 1mA$
Zero Gate Voltage Drain Current	@T <sub>C</sub> = +25°C	I <sub>DSS</sub>	_		1	μA	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage		I <sub>GSS</sub>	_	_	±10	μΑ	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage		V <sub>GS(th)</sub>	1.5	_	2.8	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
Static Drain-Source On-Resistance			_	122	190	mΩ	$V_{GS} = 10V, I_D = 1.3A$
Static Dialii-Source Off-resistance		R <sub>DS(ON)</sub>		181	335	11177	$V_{GS} = 4.5V, I_D = 290mA$
Forward Transfer Admittance		Y <sub>fs</sub>	_	0.7	_	mS	V <sub>DS</sub> = 10V, I <sub>D</sub> = 250mA
Diode Forward Voltage		$V_{SD}$	_	_	1.2	V	$V_{GS} = 0V, I_{S} = 250mA$
DYNAMIC CHARACTERISTICS (Note 8)							
nput Capacitance		C <sub>iss</sub>	_	87	_	pF	\\ - 20\\ \\ - 0\\
Output Capacitance		Coss	_	17	_	pF	$V_{DS} = 20V, V_{GS} = 0V,$ - f = 1.0MHz
Reverse Transfer Capacitance		Crss	_	12	_	pF	71 - 1.0101112
Gate Resistance		$R_g$	_	69.8	_	Ω	$f = 1MHz$ , $V_{GS} = 0V$ , $V_{DS} = 0V$
Total Gate Charge (V <sub>GS</sub> = 4.5V)		Qg	_	0.9	_	nC	
Total Gate Charge (V <sub>GS</sub> = 10V)		$Q_g$	_	2.0	_	nC	V <sub>DS</sub> = 10V, I <sub>D</sub> = 250mA
Gate-Source Charge		Q <sub>gs</sub>	_	0.3	_	nC	7
Gate-Drain Charge		Q <sub>gd</sub>	_	0.3	_	nC	
Turn-On Delay Time		t <sub>D(on)</sub>	_	4.5	_	ns	
Turn-On Rise Time		t <sub>r</sub>	_	8.9	_	ns	V <sub>DD</sub> = 30V, V <sub>GS</sub> = 10V,
Turn-Off Delay Time	t <sub>D(off)</sub>	_	30.3	_	ns	$R_G = 10\Omega, I_D = 100 \text{mA}$	
Turn-Off Fall Time		t <sub>f</sub>	_	15.6	_	ns	

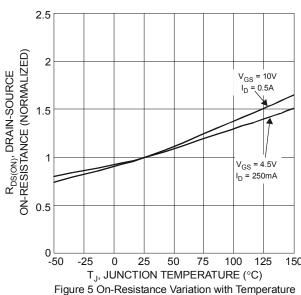
Notes: 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.

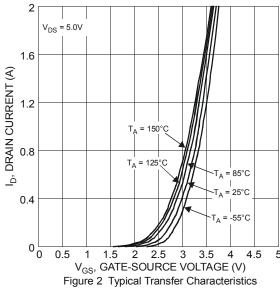
- 6. Device mounted on 1"  $\times$  1" FR-4 PCB with high coverage 2oz. Copper, single sided.
- 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.











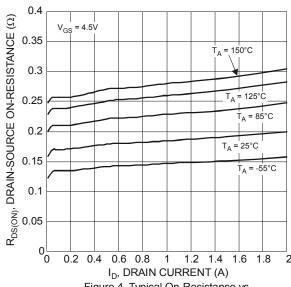


Figure 4 Typical On-Resistance vs. Drain Current and Temperature

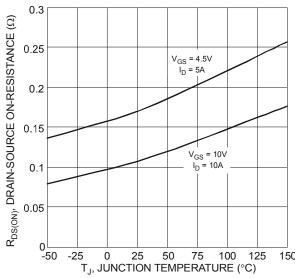


Figure 6 On-Resistance Variation with Temperature



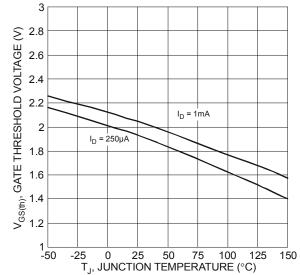
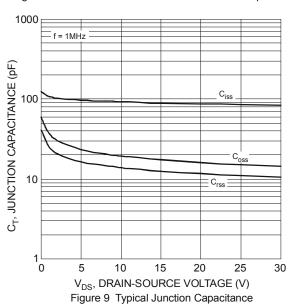
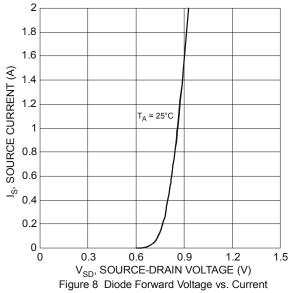
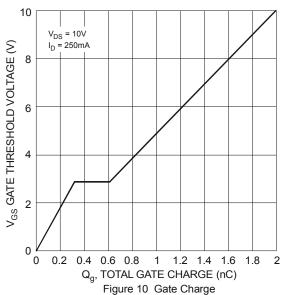


Figure 7 Gate Threshold Variation vs. Ambient Temperature

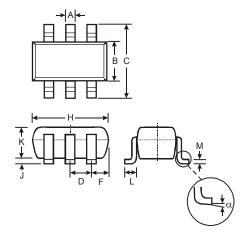






# **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

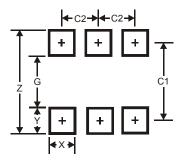


501363							
Dim	Min	Max	Тур				
Α	0.10	0.30	0.25				
В	1.15	1.15 1.35 1.30					
C	2.00	2.20	2.10				
D		0.65 Ty	р				
F	0.40	0.45	0.425				
Ξ	1.80	2.20	2.15				
7	0	0.10	0.05				
K	0.90	1.00	1.00				
L	0.25	0.40	0.30				
M	0.10	0.22	0.11				
ď	0°	8°	-				
All	Dimen	sions i	n mm				



### Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.5
G	1.3
Х	0.42
Υ	0.6
C1	1.9
C2	0.65

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